

Synthesize NTF procedure (Algorithm).

* Textbook page 114

① Choose the order N of the modulator. \rightarrow Based upon the specified SQNR and OSR.

② Choose the NTF high-pass filter type (Butterworth, inv chobyshev) etc.

③ Place the 3-dB cutoff frequency ω_{3dB} of the NTF slightly above the edge of the signal band $\Rightarrow \omega_{3dB} > \frac{\pi}{OSR}$.

④ Based on the choices made in steps ① and ②, find the NTF zeros z_i 's and the poles p_i of the NTF. Also to satisfy the realizability condition $H(\infty) = 1$, the NTF is of the form

$$H(z) = \prod_{i=1}^N \frac{z - z_i}{z - p_i} \quad \Delta t. \quad H(\infty) = 1$$

⑤ Predict the stability of the modulator. For multi-bit quantization, ~~the main~~ May use the theorem for guaranteed stability:

$$MSA = \|u\|_{\infty} \leq M+2 - \|h\|_1 \triangleq \frac{\Delta}{2} (M+2 - \|h\|_1) \quad \text{Toolbox uses } \Delta=2$$

• For single-bit quantization use Lee's rule.

$$|H(-1)| = \prod_{i=1}^N \frac{1+z_i}{1+p_i} < 1.5$$

⑥ Confirm the stability estimation with extensive simulations.

⑦ If the predicted stability is unsatisfactory, shift the poles away from $z=-1$ point (i.e. ^{reduce} the OBG), while maintaining flat low-pass filter gain in the signal band. \Rightarrow Done by reducing the ω_{3dB} .

\Rightarrow reduce OBG and enhance stability

⑧ If the stability is robust, but the SQNR doesn't reach the specified limit, make the design more aggressive by increasing ω_{3dB} .

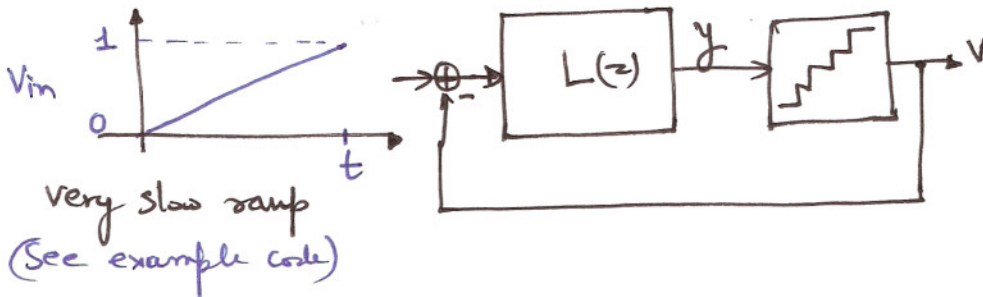
⑨ Goto step ⑥ ~~still~~ all the specs are met.

Estimating MSA (Maximum Stable Amplitude)

(5)

- * Use simulation.
- * Simulate for sine wave inputs for all possible frequencies in the signal band.
- * For each frequency step up the input amplitude and compute in-band SNR.
- Beyond MSA, the NTF poles will move out of the unit circle
- ↳ Noise shaping is destroyed and the SNR falls.
- ↳ At this point the quantizer input ~~blows~~ $y[n]$ blows up.
- Simulate SNR function s in the toolbox does the same.
 - ↳ uses $f_{in} = \frac{fs}{4096}$.
- Time consuming and slow debugging procedure.

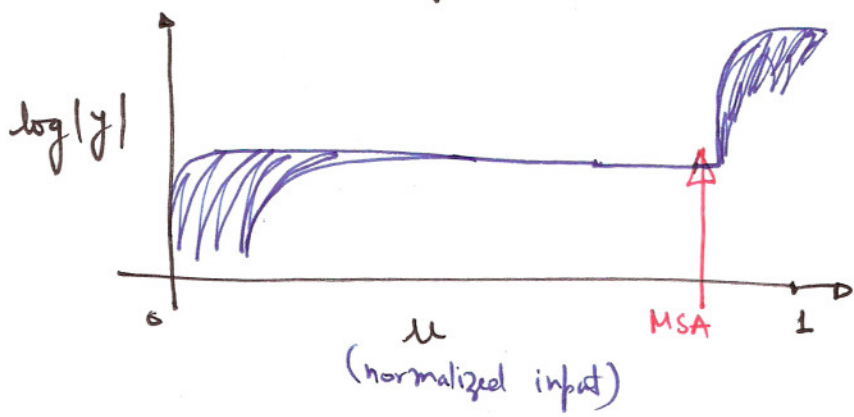
Better Method



Estimating MSA without sine wave inputs:

- suggested by Lars Risbo.
- Use a slow ramp \hat{x} input with increasing value from 0 to full scale input.

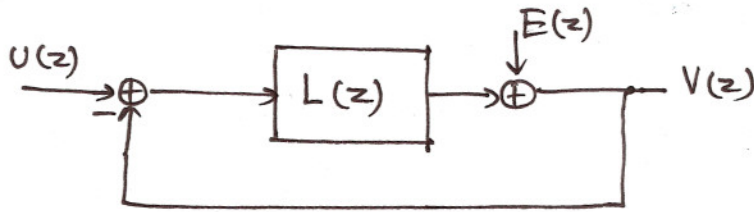
- Beyond the MSA, the ω NTF poles move out of the unit circle
 - Observe $\log_{10}|y[n]|$. Beyond MSA, this value blows up to infinity rapidly. ($y[n] \rightarrow \infty$).
 - Use 90% of this value where $y[n]$ blows up as a conservative estimate for the MSA.
 - Results in an MSA close to the one predicted by sine wave inputs.
 - Much quicker than `simulateSNR` function.
- ↳ write your own `bolbox` function to do this!



See example code
 "MSA_Risbo_Method.m"

Sensitivity of a feedback loop

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$$V(z) = U(z) \cdot \frac{L(z)}{1+L(z)} + E(z) \cdot \frac{1}{1+L(z)}$$

⇒ The loop rejects the disturbance E at frequencies where the loop gain is high.

⇒ The sensitivity of the loop is $\frac{1}{1+L(e^{j\omega})}$.

↳ how effectively the disturbance is suppressed is called the sensitivity of the loop.

↳ The loop is insensitive to the disturbance at low frequencies. ∵ $L(e^{j\omega})$ is high at low frequencies

• Sensitivity is same as the NTF.

• $k(0) = \text{NTF}(\infty) = 1$

The NTF can be expanded as

$$\text{NTF}(z) = \frac{(1+a_1z^{-1})(1+a_2z^{-1}+a_3z^{-2}) \dots}{(1+b_1z^{-1})(1+b_2z^{-1}+b_3z^{-2}) \dots}$$

Complex zeros pair

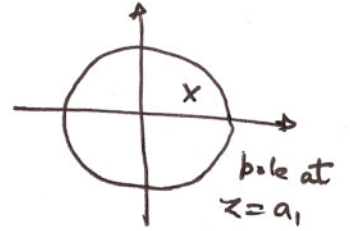
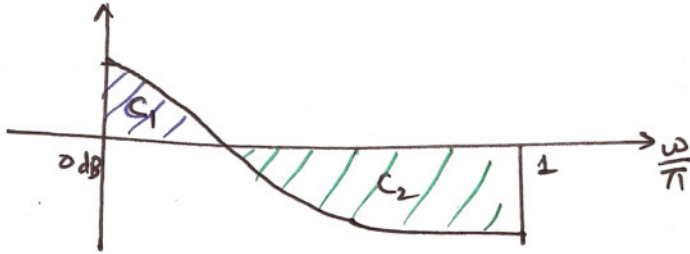
Complex pole pair

• poles must be within the unit circle.

• zeros are inside (on) the unit circle.

① It can be shown that

$$\int_0^\pi \log |1 + a_1 e^{-j\omega}| d\omega = 0 \quad \text{if } |a_1| \leq 1$$



$$\Rightarrow C_1 = C_2$$

\Rightarrow Area above the 0 dB line = Area below the 0-dB line

② #1 Using ① we can show that

$$\int_0^\pi \log |1 + a_2 e^{-j\omega} + a_3 e^{-j2\omega}| d\omega = 0$$

if the roots of $(1 + a_2 z^{-1} + a_3 z^{-2})$ lie within ^(or on) the unit circle.

Using ① and ②, we ~~get~~ have

$$\begin{aligned} \int_0^\pi \log |NTF(e^{j\omega})| d\omega &= \int_0^\pi \log \left| \frac{(1 + a_1 e^{-j\omega})(1 + a_2 e^{-j\omega} + a_3 e^{-j2\omega}) \dots}{(1 + b_1 e^{-j\omega})(1 + b_2 e^{-j\omega} + b_3 e^{-j2\omega}) \dots} \right| d\omega \\ &= \int_0^\pi \log |1 + a_1 e^{-j\omega}| d\omega + \int_0^\pi \log |1 + a_2 e^{-j\omega} + a_3 e^{-j2\omega}| d\omega + \dots \\ &\quad - \int_0^\pi \log |1 + b_1 e^{-j\omega}| d\omega - \int_0^\pi \log |1 + b_2 e^{-j\omega} + b_3 e^{-j2\omega}| d\omega + \dots \\ &= 0 \end{aligned}$$

$\int_0^\pi \log |NTF(e^{j\omega})| d\omega = 0$

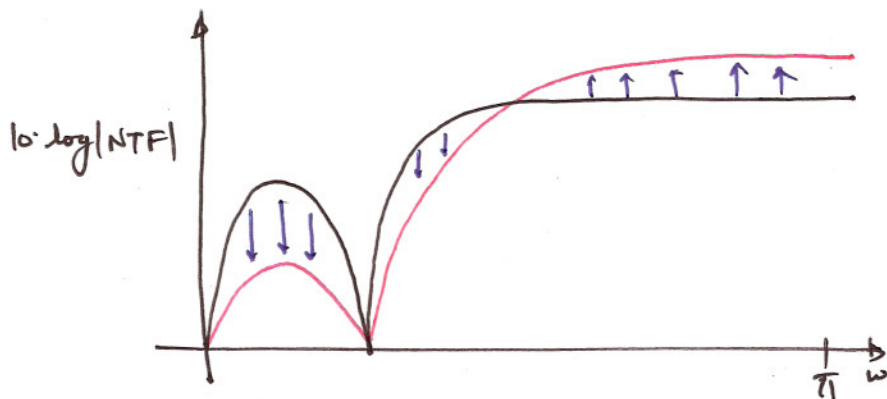
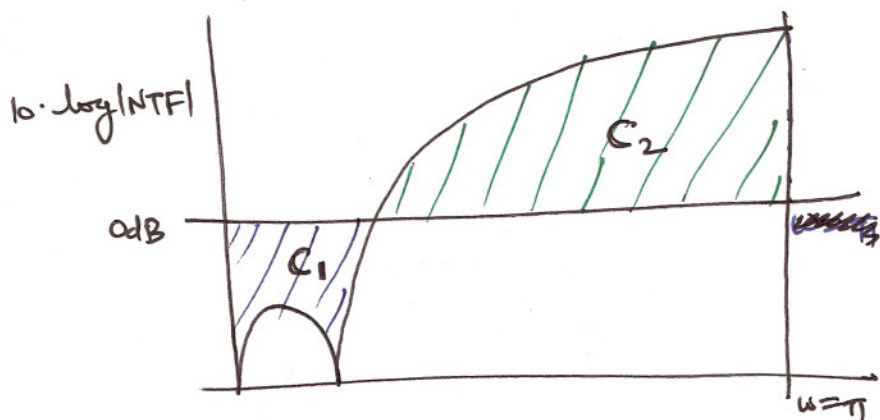
← Bode's Sensitivity Theorem

\Rightarrow The integral of the log-magnitude of a 'stable' NTF is 0

⇒ Loop cannot be insensitive to the disturbance at all the frequencies.

↳ Loop is highly sensitive to the disturbance at high frequencies and very less sensitive ~~to~~ at low frequencies.

↳ maximum sensitivity at $\omega = \pi$.

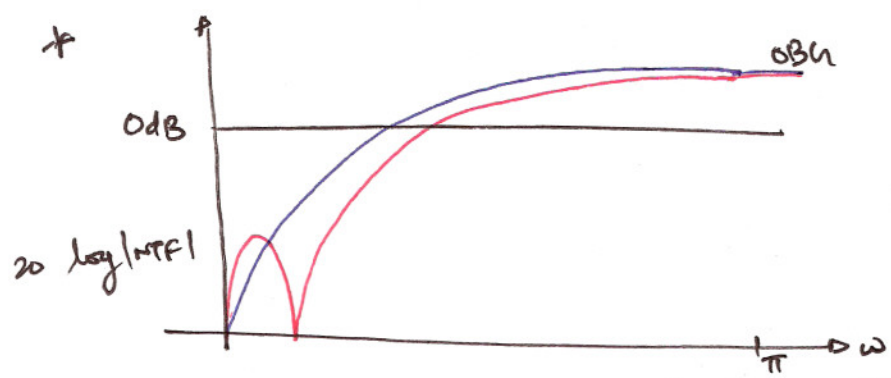


To keep $C_1 = C_2$, if the in band performance is improved, the SBG increases.

↳ Like a waterbed example.

⇒ "Good in-band performance comes at the expense of poor out-of-band performance!"

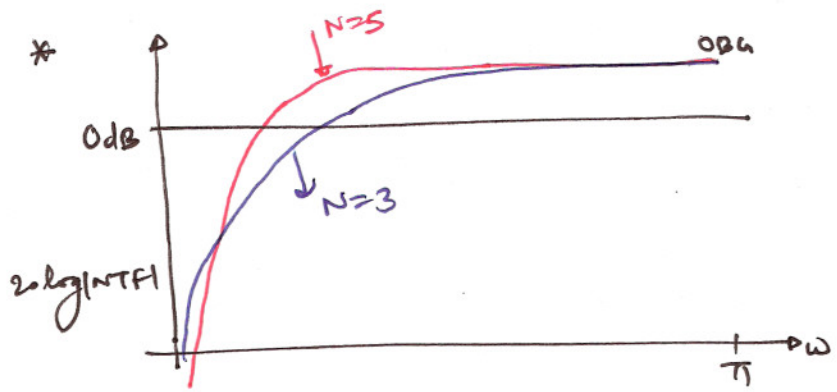
⇒ trade off between IBN and OBG



⊕

Complex NTF zeros better than choosing all NTF zeros at $z=1$.

⇒ lower IBN for same OBG.



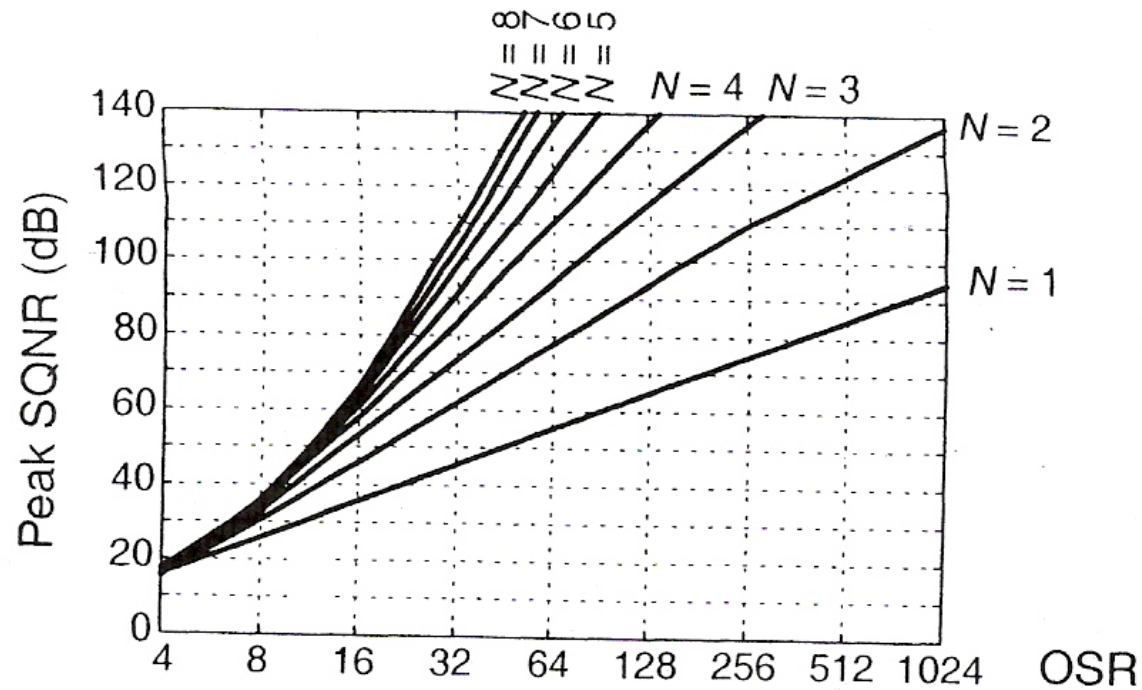
Higher order NTFs exhibit lower in band noise (IBN) for the same OBG.

ECE 697 Delta-Sigma Converters Design

Lecture#13 Slides

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SQNR Limit for DSMs with 1-bit Quantizers



4.14: Empirical SQNR limit for 1-bit modulators of order N .

SQNR Limit for DSMs with 2-bit Quantizers

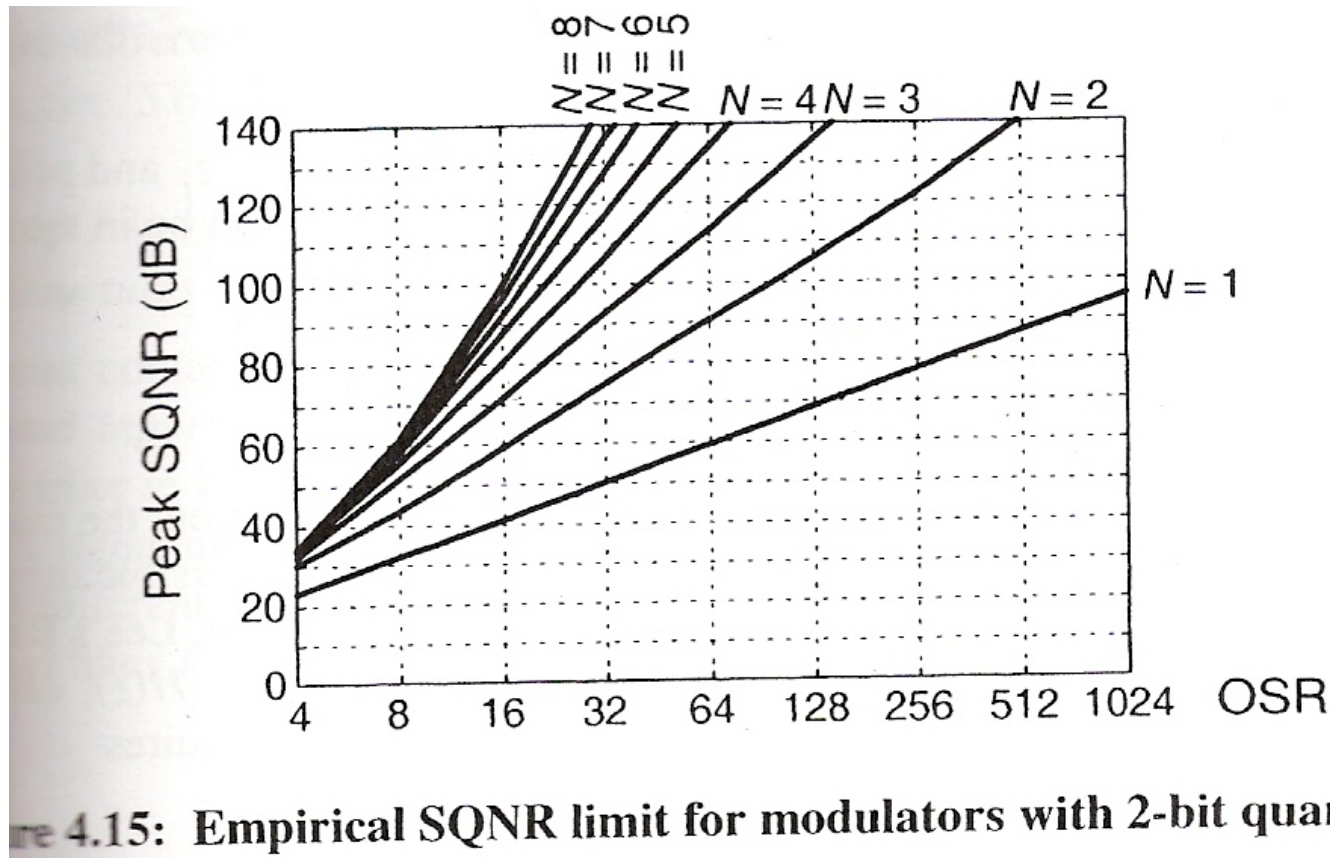


Figure 4.15: Empirical SQNR limit for modulators with 2-bit quantizers.

SQNR Limit for DSMs with 3-bit Quantizers

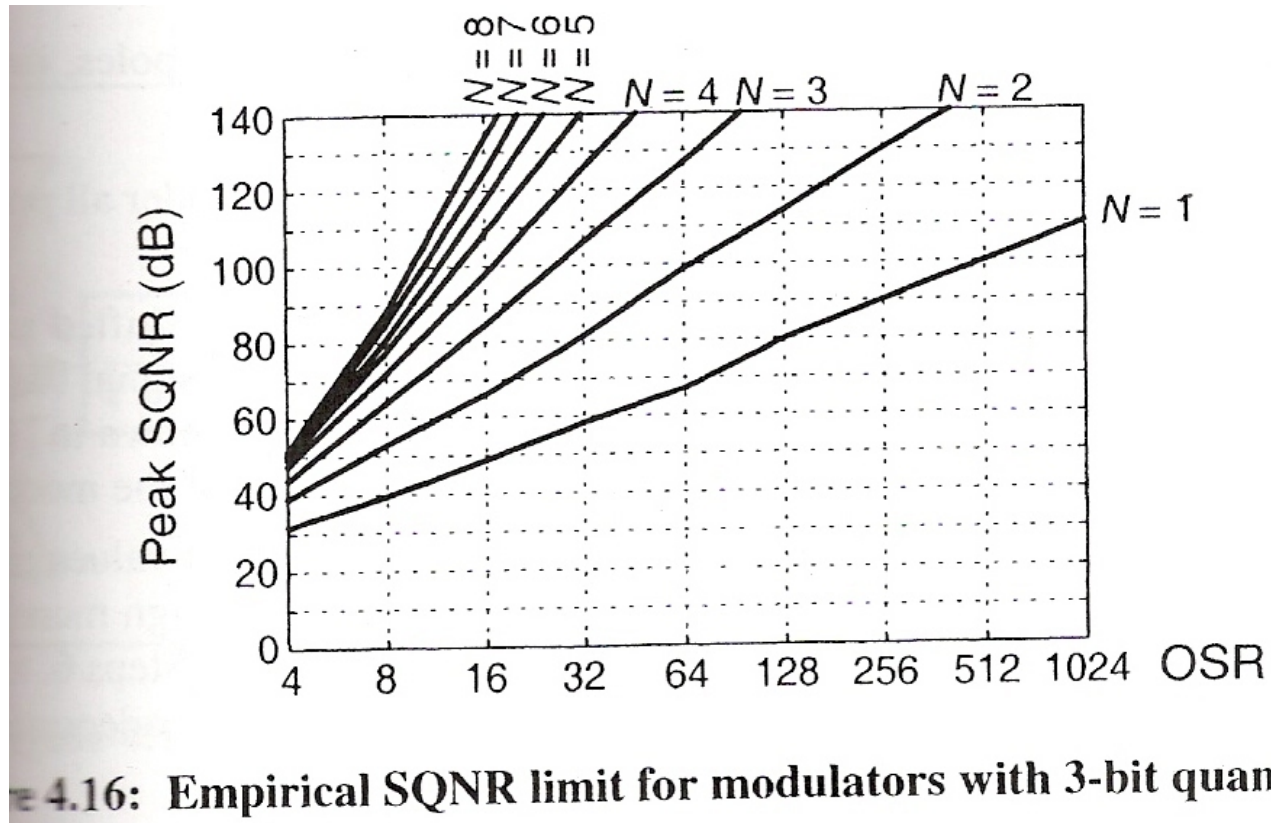
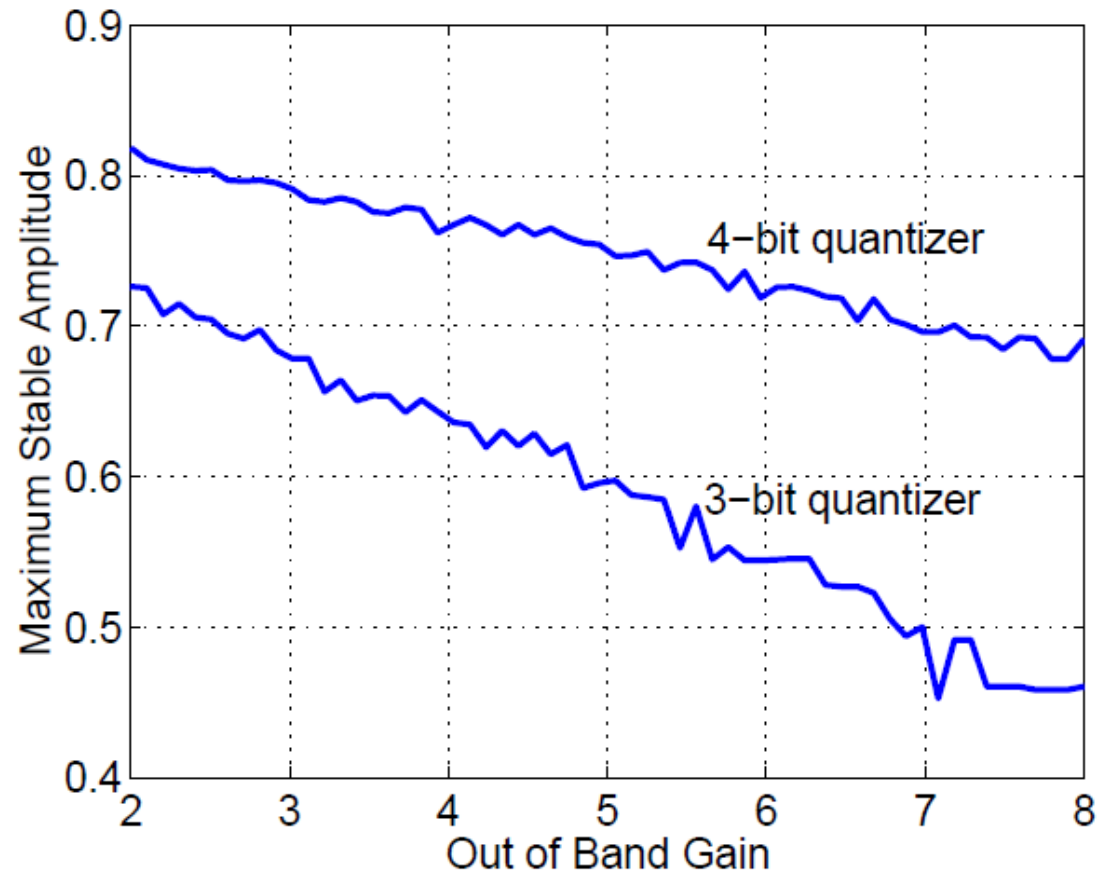


Fig. 4.16: Empirical SQNR limit for modulators with 3-bit quantization.

MSA vs OBG for a Third-Order NTF



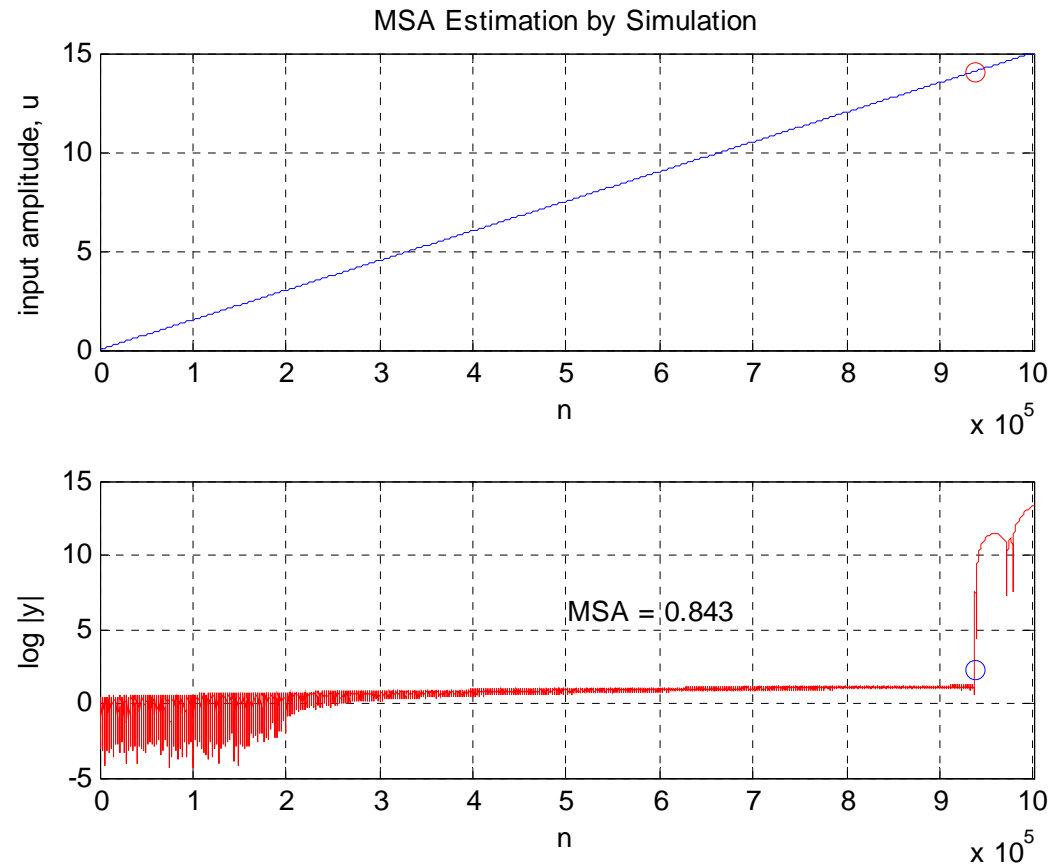
Estimating MSA (Maximum Stable Amplitude)

- ❑ MSA is found through extensive simulation.
- ❑ Simulate for input sinusoids of varying amplitudes for all possible signal frequencies in the signal band.
 - ✓ For every input amplitude compute in-band SNR.
 - ✓ Beyond the MSA, the NTF poles move out of the unit circle.
 - ✓ Noise shaping is disrupted and the in-band SNR drops.
 - ✓ At this point the quantizer input ($y[n]$) blows up.
- ❑ `simulateSNR` function in the toolbox does exactly the same.
- ❑ Time consuming and often impractical for iterative design.

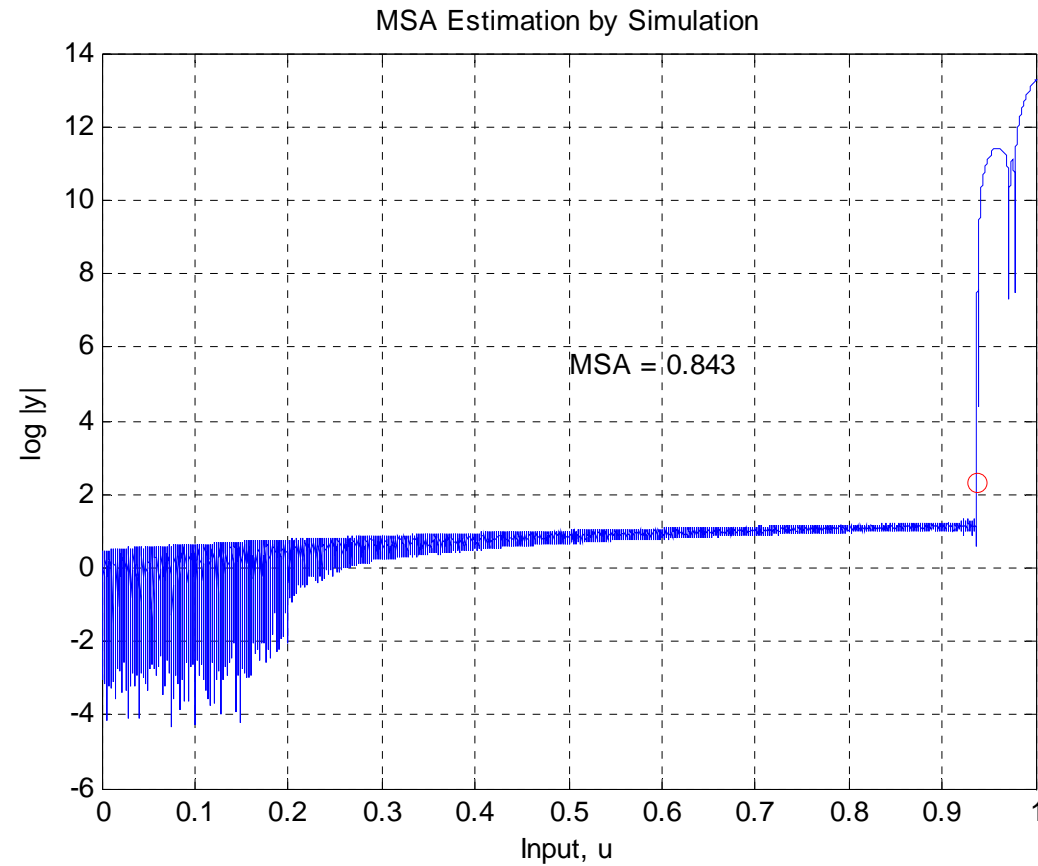
Estimating MSA using Risbo's Method

- ❑ Lars Risbo suggested a method for estimating MSA without sinewave inputs.
- ❑ Use a slow ramp input from 0 to FS value.
 - ✓ Plot $\log_{10}|y[n]|$. Observe where this plot blows up.
 - ✓ Take 90% of the input amplitude where $\log_{10}|y[n]|$ blows up as a conservative estimate for MSA.
 - ✓ Estimated MSA is close to that predicted by the sinewave input method.
- ❑ Much quicker than the sinewave technique (simulateSNR function).
- ❑ Write your own toolbox function generalizing this method !

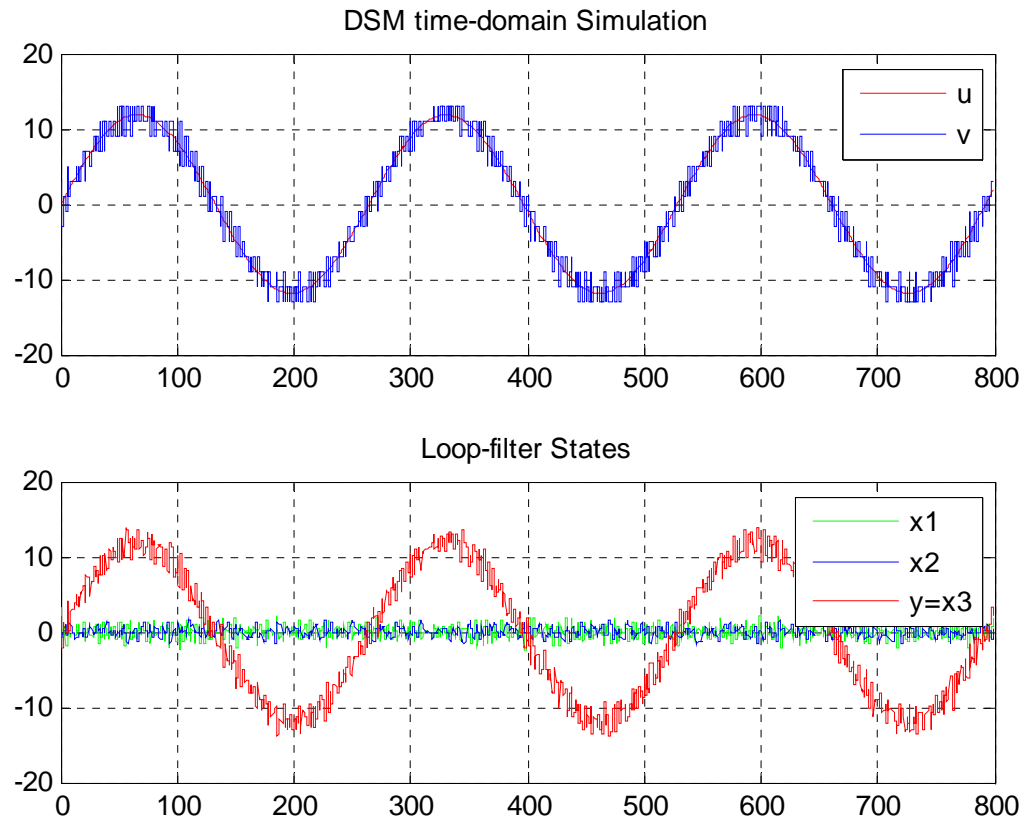
Estimating MSA using Risbo's Method contd.



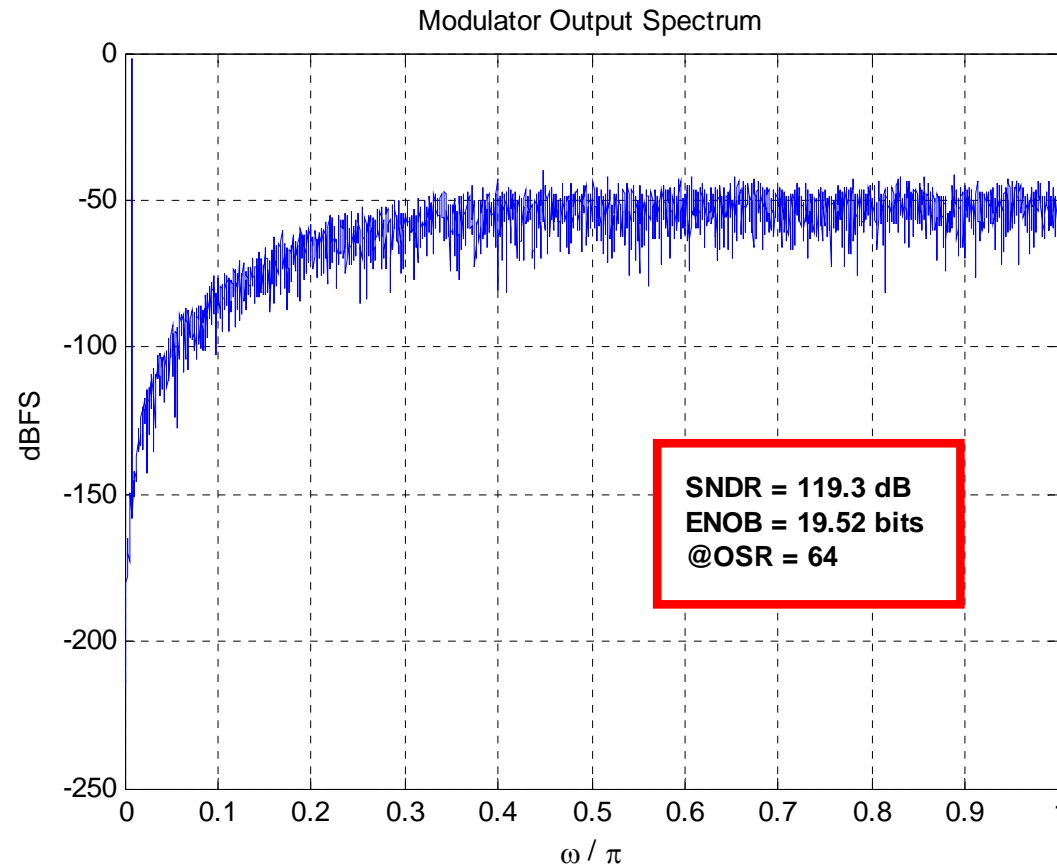
Estimating MSA using Risbo's Method contd.



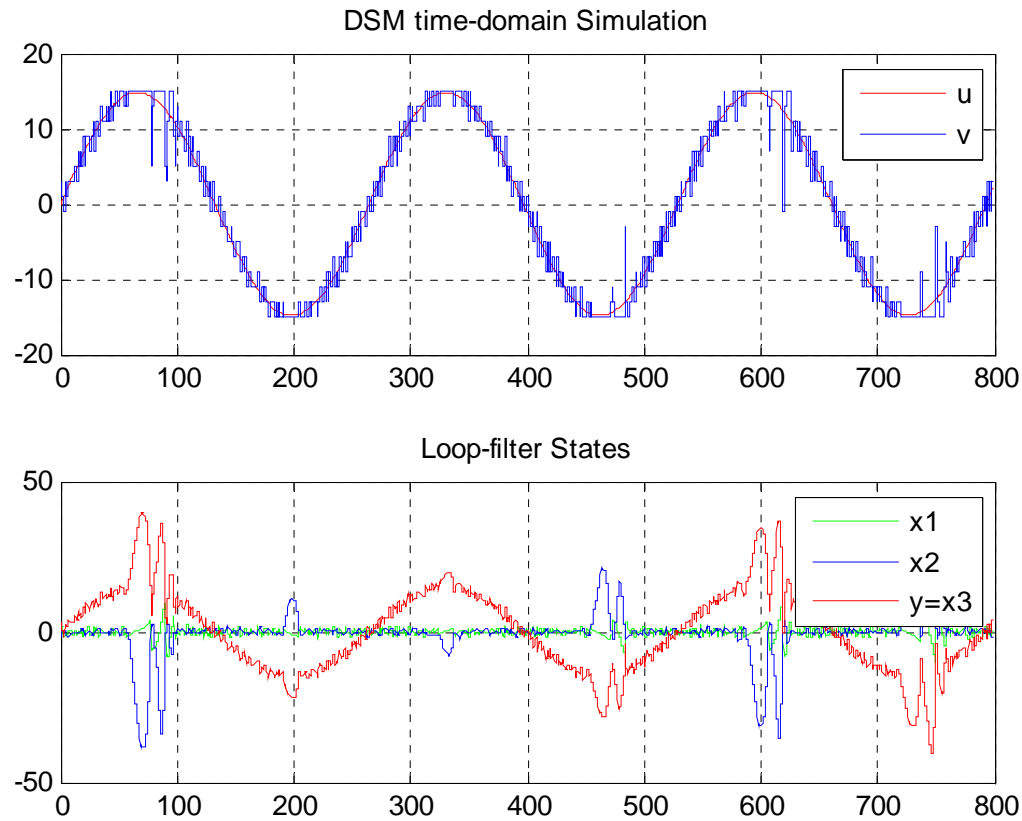
Simulation with input with MSA



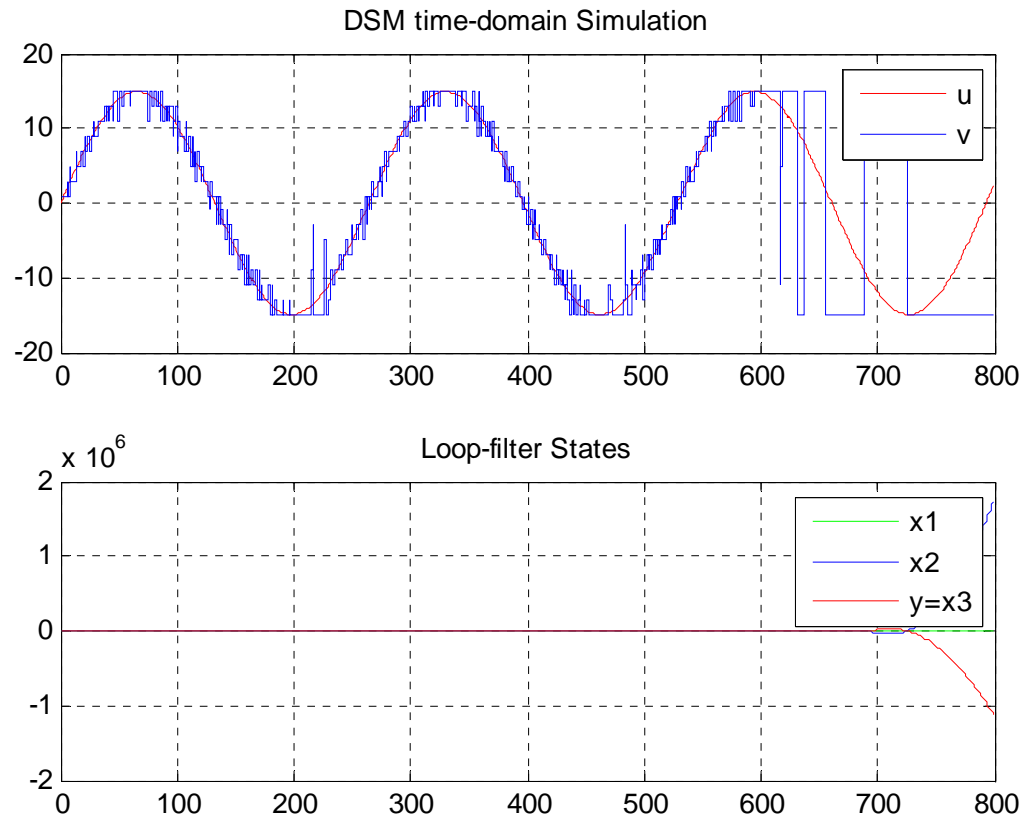
Simulated SNR with input with MSA



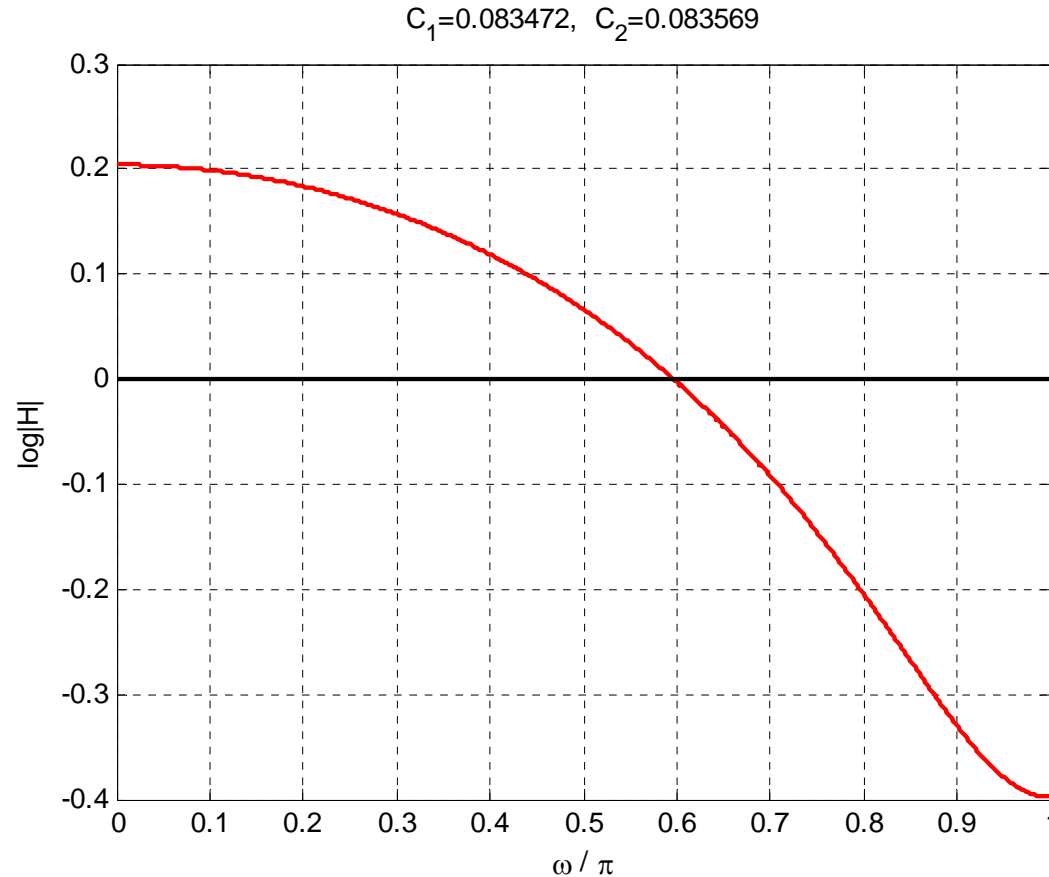
Simulation with input with $1.2 * \text{MSA}$



Simulation with input with $1.2 * MSA$



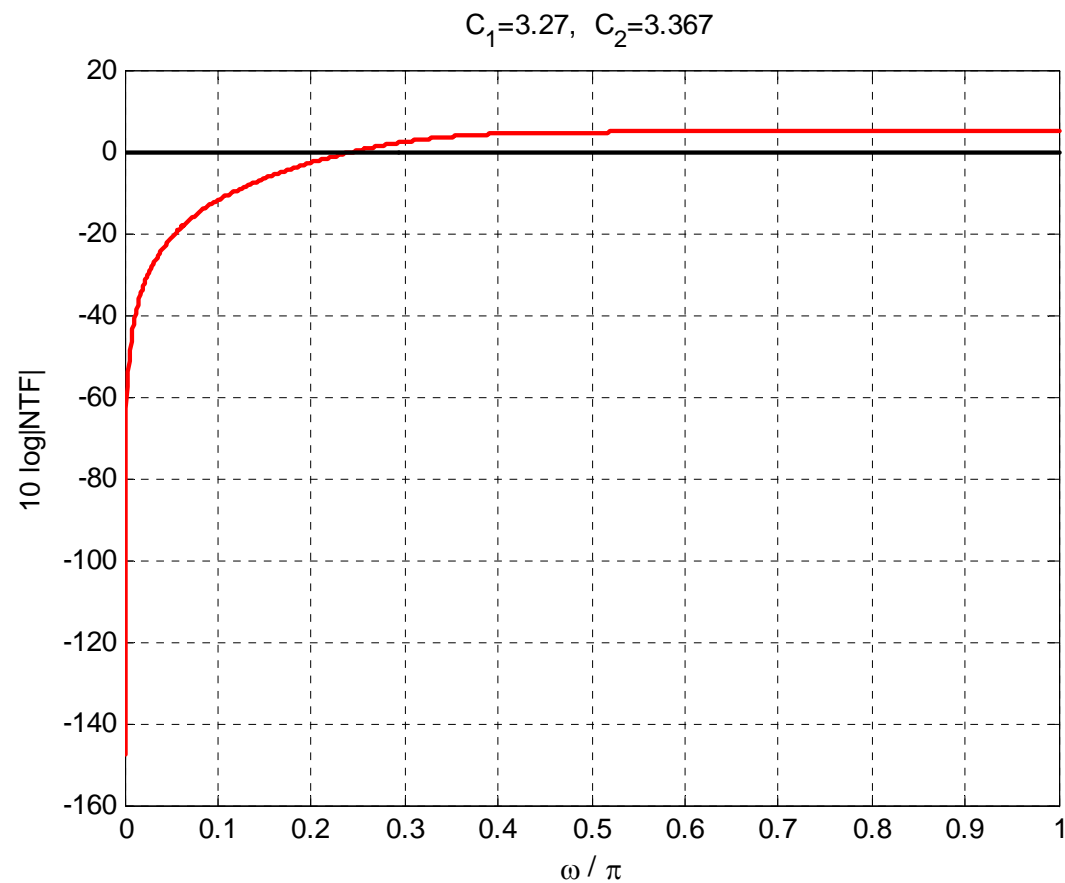
Bode Sensitivity Integral



Single pole/zero transfer function with pole/zero inside the unit circle.
Area above and below the 0-dB axis are equal.

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Bode Sensitivity Integral

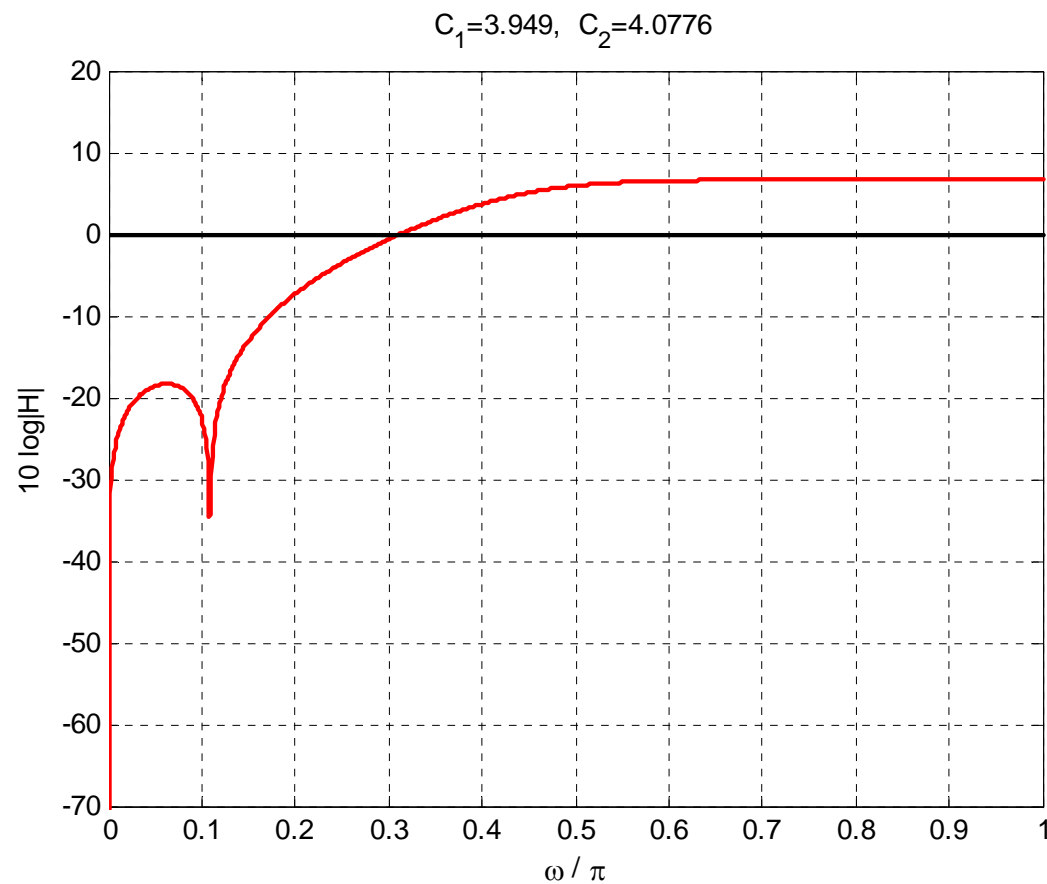


Butterworth NTF.

Area above and below the 0-dB axis are equal.

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Bode Sensitivity Integral

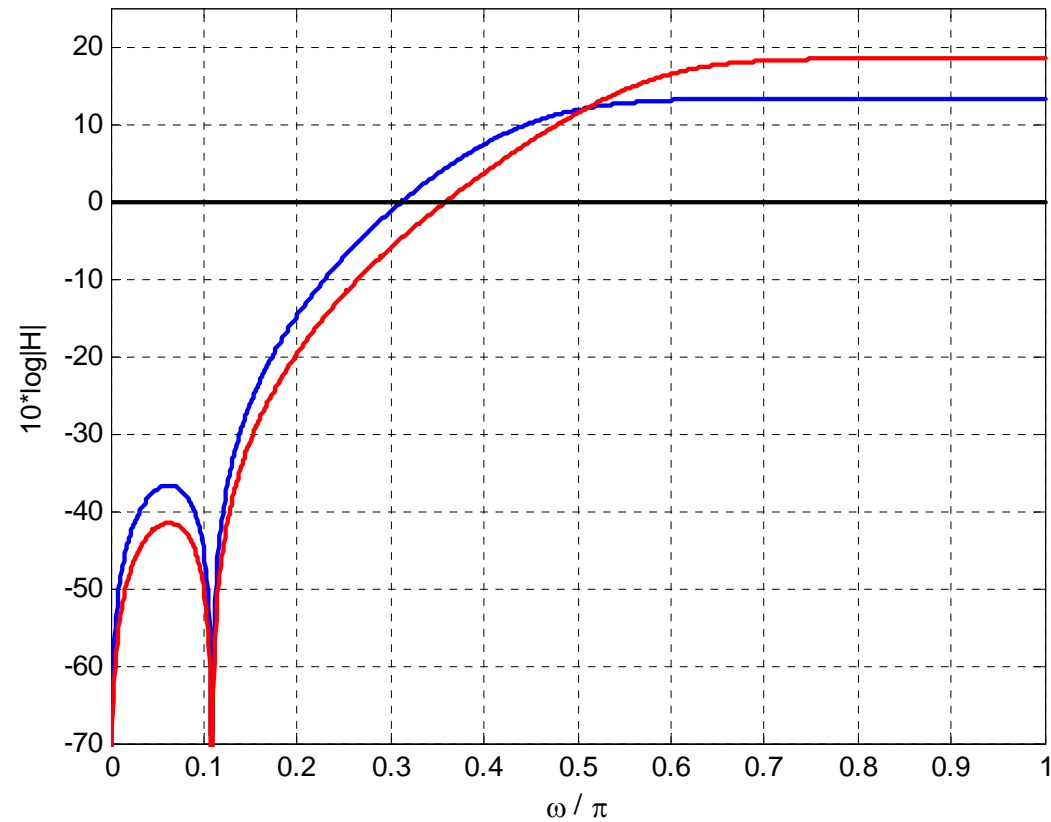


Inverse Chebyshev NTF.

Area above and below the 0-dB axis are equal.

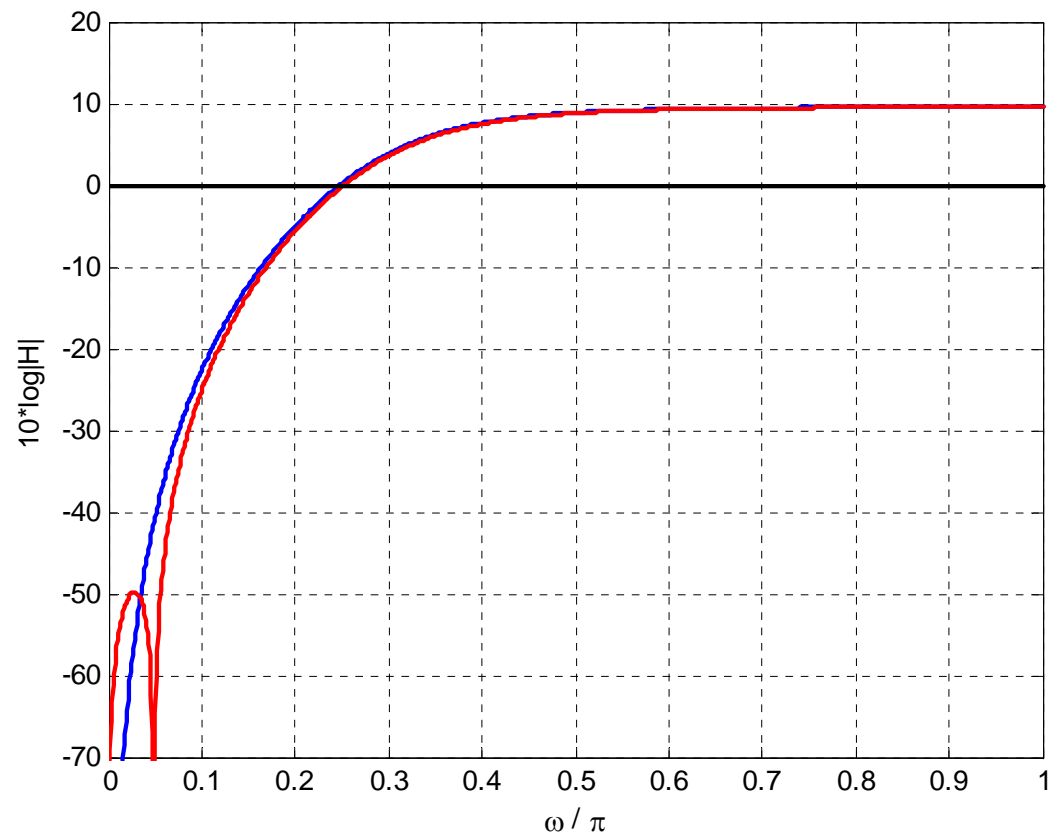
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Bode Sensitivity Integral



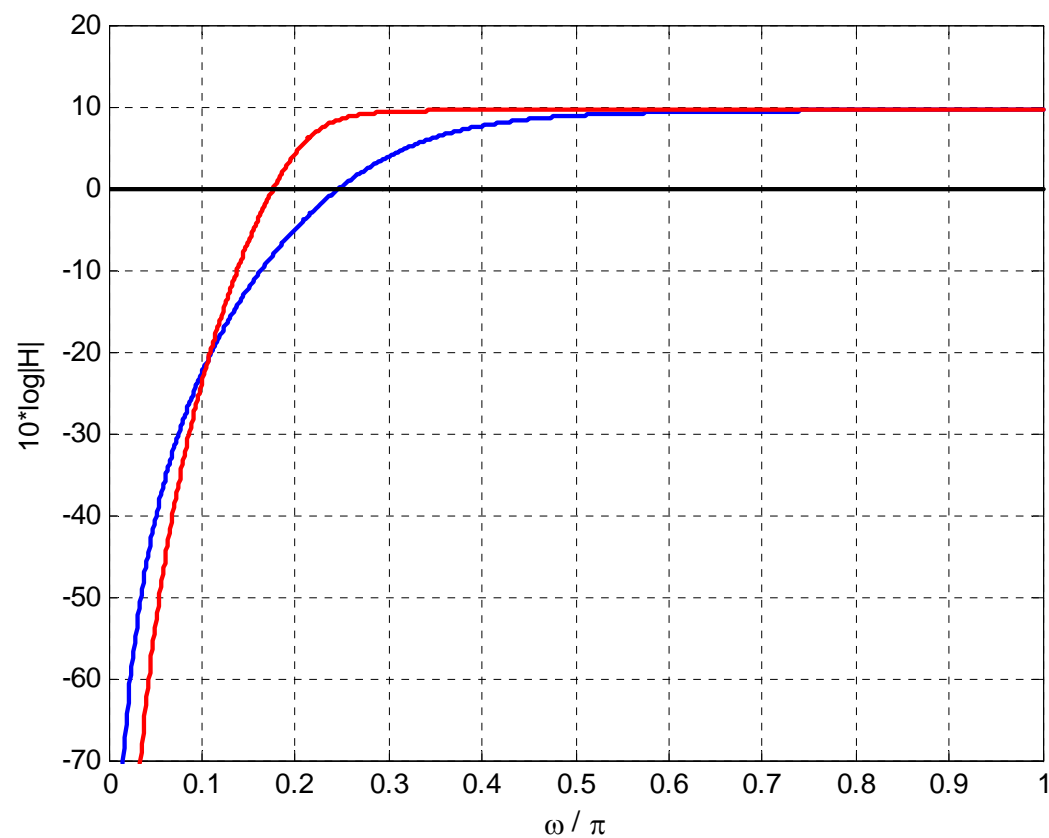
Better in-band performance results in worse out-of-band performance.

Bode Sensitivity Integral



Complex NTF zeros result in better in-band performance for the same OBG.

Bode Sensitivity Integral



Higher-order NTF results in better in-band performance for the same OBG.

References

- [1] R. Schreier, *Understanding Delta-Sigma Data Converters*, Wiley, 2005.
- [2] S. Pavan, N. Krishnapura, “Tutorial: Oversampling Analog to Digital Converters,” *21st International Conference on VLSI Design*, Jan. 4, 2008.
[Online]: <http://www.ee.iitm.ac.in/~nagendra/presentations/20080104vlsiconf/20080104vlsiconf.pdf>