

ECE 697 Delta-Sigma Converters Design

Lecture#1 Slides

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Course Outline

Instructor	: Vishal Saxena
Time	: Tuesday and Thursday, 6:30 to 7:45 PM
Course dates	: Tuesday, January 19 to Thursday, May 6
Location	: MEC 106
Holidays	: March 30 and April 1, Spring break.
Office	: MEC 202 F
Office hours	: Tue. & Thu., 5:00-6:00 pm or walk-in
E-mail	: vishalsaxena@u.boisestate.edu
Website	: <u>http://cmosedu.com/vsaxena/courses/ece697/s10/ECE697.htm</u>

Course Topics

- Data Conversion and spectral estimation fundamentals
- Delta-Sigma modulator (DSM) architectures, decimation filters
- Discrete-time (switched-capacitor) DSM design
 - ✓ System level design, noise budgeting, circuit optimization.
- Continuous-time (CT) DSM design
 - ✓ Effects of excess-loop delay and clock jitter in CT-DSMs
 - ✓ Tuning techniques for CT-DSMs.
- □ Flash ADCs and DACs employed in the CT-DSMs
- □ Bandpass and Complex DSMs.
- DAC mismatch error shaping
- Delta-Sigma DAC architectures



Prerequisites

Analog IC Design (ECE 511)

- ✓ Op-amps, biasing, small-signal analysis.
- Digital Signal Processing
 - ✓ Fourier, DTFT, Laplace, z-transforms, poles and zeros.
- Basic knowledge of circuit simulation using Spice/Spectre and Matlab scripting.
- □ Preparatory coverage will be provided on
 - ✓ Detailed noise and distortion analysis of circuits
 - ✓ Signal processing fundamentals



Textbook and References

- Understanding Delta-Sigma Converters Richard Schreier and Gabor Temes, Wiley-IEEE Press, 2005.
 - ✓ CT Sigma-Delta ADC Ortmanns
- Matlab Delta-Sigma Toolbox by R. Schreier available for download <u>online</u>. The toolbox manual is <u>here</u>.
- □ The complete reference list for delta-sigma modulators is available <u>here</u>.





Course Pedagogy, Grading and Policies

Combination of lecture notes, slides and simulation

- ✓ Lecture notes will be posted online
- ✓ Additional slides, Matlab code etc will also be posted.
- \checkmark Regular reading assignments from the textbook and referred papers.

□ Workload (Grading)

- Homeworks (30%): Weekly assignments combining Matlab and Spectre based design and simulation.
- ✓ Midterm Exam (20%)
- ✓ Project 1 (25%): Switched-capacitor delta-sigma modulator design.
- ✓ Project 2 (25%): Continuous-time delta-sigma modulator design.

Policies

- ✓ Maximize learning!
- \checkmark No plagiarism, late work and net surfing in class.



Data Converters



- □ Real world: Continuous-time, continuous-amplitude signals.
- Digital world: Discrete-time, discrete-amplitude signal representation.
- □ Interface circuits: ADC and DACs.
 - ✓ Varying speed and precision requirements.



Data Conversion Scenarios

Any application using a sensor and/or an actuator

- ✓ Wireless: RF Rx and Tx chain
- ✓ Twisted pair: ADSL modem
- ✓ Coaxial: Cable modem
- ✓ Serial/Optical links: 10G+ ADC for modulation and equalization
- ✓ Audio Recording: 24-bit stereo ADCs
- ✓ Audio players: stored data to speaker (audio DAC)
- ✓ HDD read channel: Magnetic disk to microprocessor
- Biomedical applications (e.g. sensing blood glucose level and actuating the insulin pump),.....
- □ Speed and resolution requirements vary with the application.



Analog to Digital Converter Architectures





Analog-to-Digital Converter (ADC)







Sampling Process

□ Refer to lecture notes.

Signals refraction
Fartier donies: for a feeriodie sugred g(t), with period to
g(t) =
$$\sum_{k=0}^{\infty} a_{k} e^{i 2\pi t R_{k} t}$$

where $a(R) = \int_{0}^{\infty} g(t) e^{i 2\pi t R_{k} t}$
fourier Transform:
 $x(t) = \int_{0}^{\infty} x(t) e^{i 2\pi t R_{k} t}$
 $r(t) = \int_{0}^{\infty} x(t) e^{i 2\pi t R_{k} t}$
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Q

Delta function (?)

$$\int_{-\infty}^{\infty} S(t) dt = 1$$

$$\chi(t) \cdot S(t) = \chi(o) \cdot S(t) \iff \text{picks the value of } t = 0$$

$$\chi(t) \cdot S(t-b) = \chi(t-b) = \chi(t-b)$$

$$\chi(t) \otimes S(t-b) = \chi(t-b)$$

$$S(t+c-b) = \chi(t-b)$$

$$S(t+c-b) = \chi(t-b)$$

ADL (Analog-to-Digital Converter)

N Digital output XIB Sitt or T/H Quan higes Discrete the Discriete (Quantized) Amplitude Continuous-time Continuous-Ampfitude Discrete-time Continuous amplitude Mainy continuous-time representation of signals to keep 5/H analysis simple. => y(t)=25(t)=>x(nTs) is held Ideal Sampling (impulse sampling) impulse train, pilt) $\gamma(t) = \gamma(t)$ x (+) Sompled signal y(t) = x(t). Z 8(t-nts) = x(t). p(t) => Y(f)= X(f) @ P(f)

How to find P(f)=?

3





But what about thermal/widdoand noise present at the input of the Sompler?



. Y Even if the signal was bondlimiked the noise will alias from kfs+[-B, B] to the baseband. X AAF teinids the suppresses the noise in the alias bands. AAF teinids the suppresses the noise in the alias bands. AAF to alias always a must before a dample.

* Ideal brickwall AAF is not realizable



>) Oversampling results in L) better alias rejection with the same AAF. L) lower order AAF for some amound of dias rejection.
>) Oversampling relaxes the requirements on AAF.
Oversampling vatio = #= = #= = 0.5R
2B