

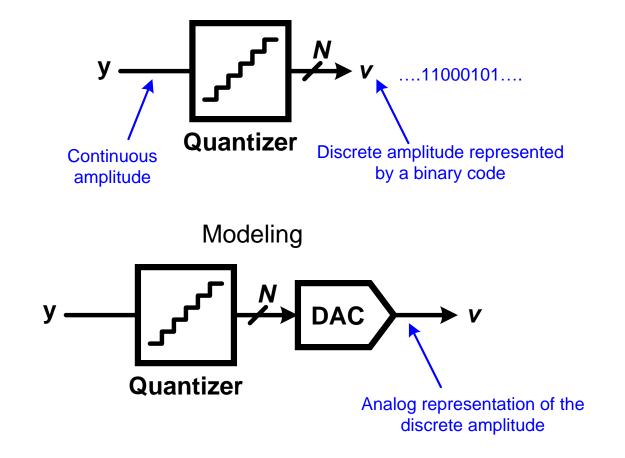
ECE615 Mixed-Signal IC Design

Lecture 2 Slides: Quantizers

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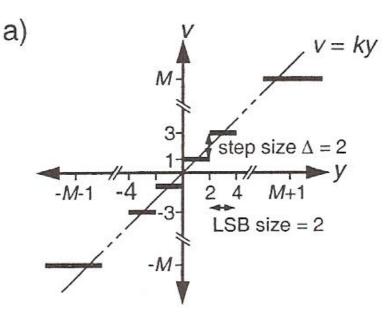
Quantizer

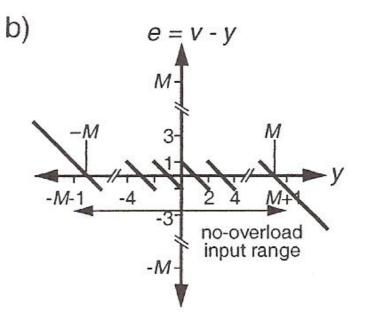






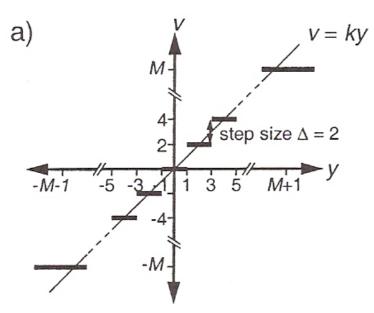
Mid-Rise Quantizer (even number of levels)

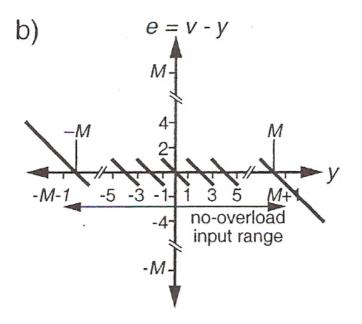




- \Box Step rising at y=0 (mid-rise).
- □ In this figure (DSM toolbox model), LSB= Δ = 2
- \Box M = Number of steps, (M is odd here)
 - Number of levels (nLev) = M+1, (even)
- □ Input thresholds: 0, ± 2 ,, $\pm (M-1)$.
- **D** Output levels: $\pm 1, \pm 3, \dots, \pm M$.

Mid-Tread Quantizer (odd number of levels)



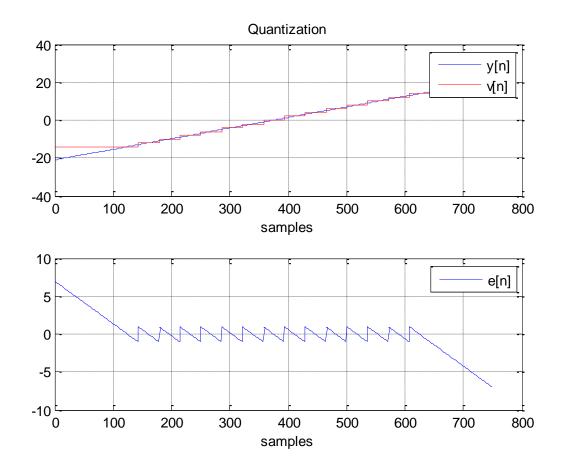


- \square Flat part of the step at y=0 (mid-tread).
- $\Box \text{ Here, LSB} = \Delta = 2$

 \square M = Number of steps, (M is even here)

- ✓ Number of levels (nLev) = M+1, (odd)
- Input thresholds: $0, \pm 2, \ldots, \pm (M-1)$.
- Output levels: $0, \pm 2, \pm 4, \dots, \pm M$.

Quantizer characteristics : Slow ramp input



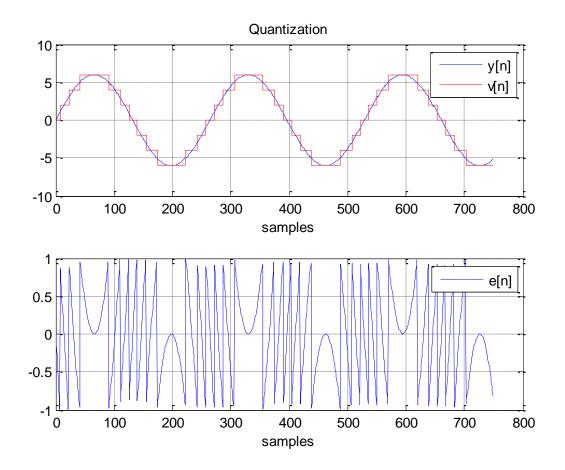
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File: Quantizer_ramp_input.m

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-5-

Quantizer characteristics : Sine input



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File: Quantizer_sine_input.m

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-6-