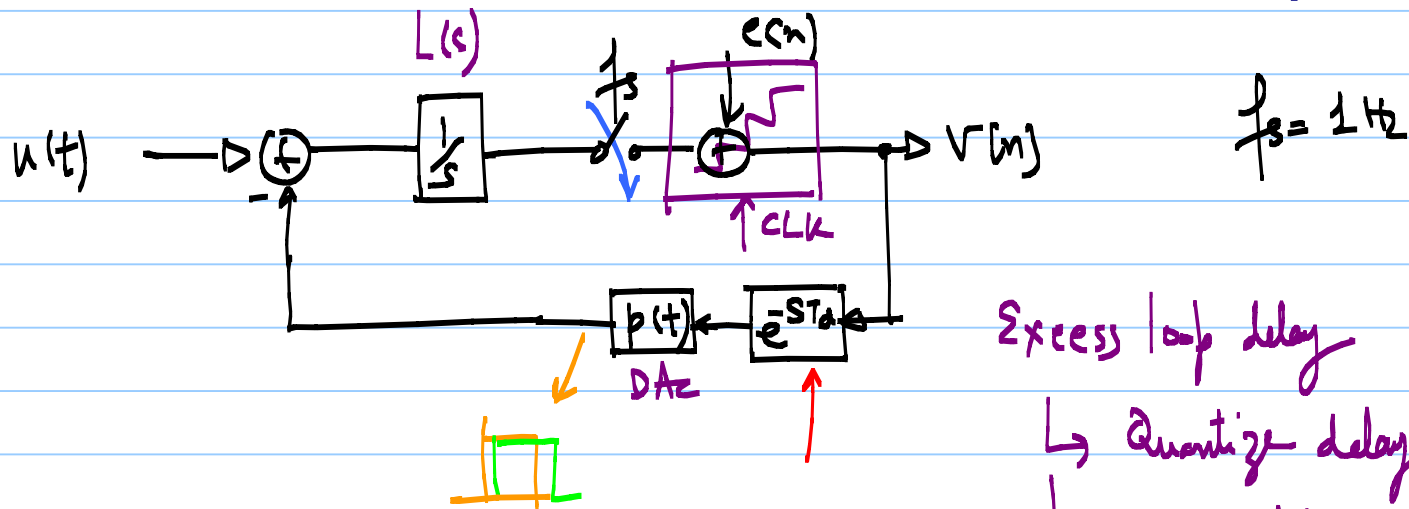


ECE 615 - Lecture 22

Note Title

11/19/2013

CT $\Delta\Sigma$ \rightarrow non-idealities \rightarrow Quantizer delay (\Rightarrow ELD)



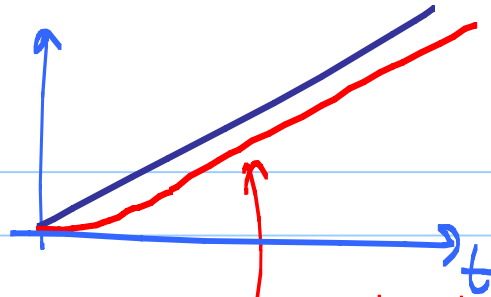
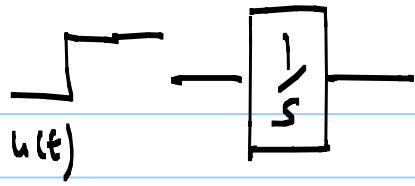
Excess loop delay

\hookrightarrow Quantize delay (T_d)

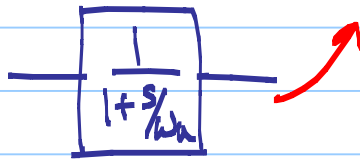
\hookrightarrow DAC delay

\hookrightarrow Finite BW of opamps in the loop-filter

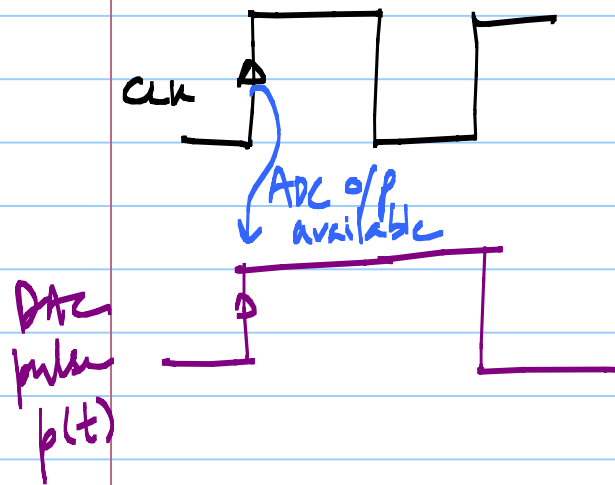
Aside



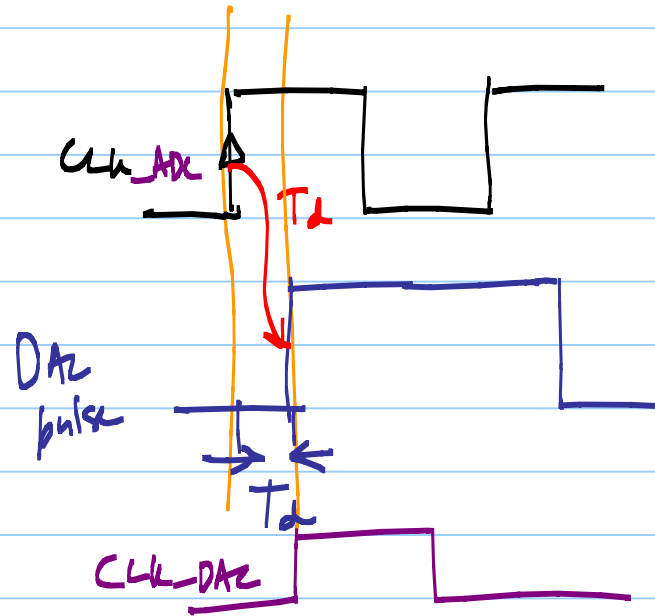
↑
SLD due to the
loop filter.



Ideal case
(No delay)



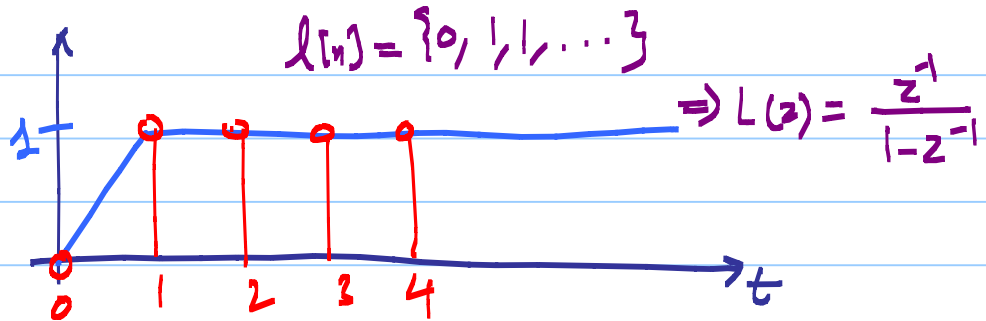
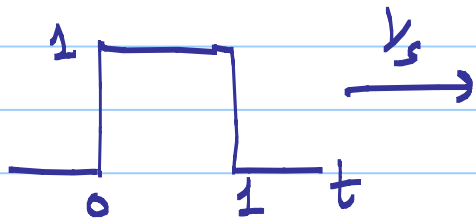
Real case



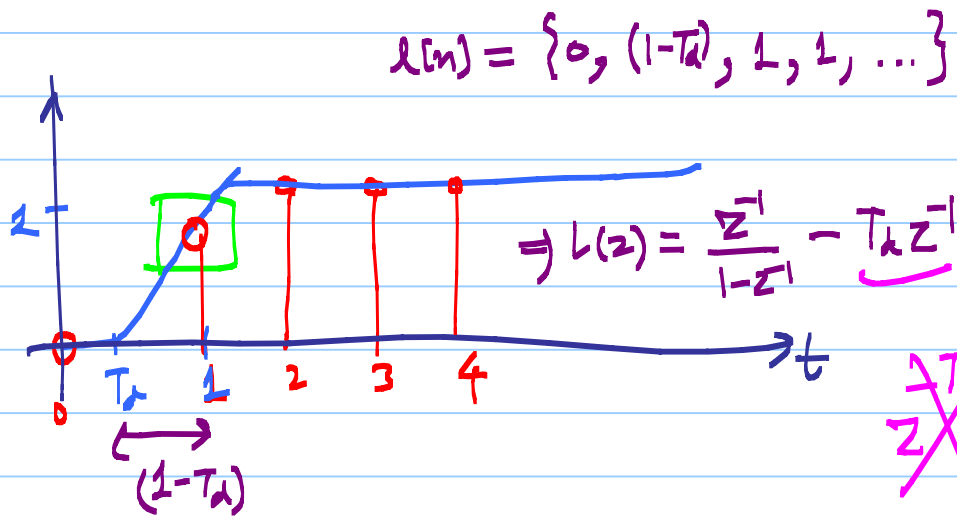
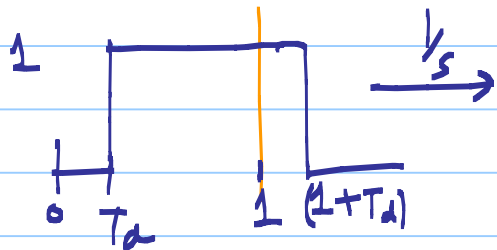
\Rightarrow Max delay in the loop \Rightarrow NTF will be changed as $L(z)$ changes

$$f_s = 1 \text{ Hz}$$

IDEAL CASE



with ELD



With ELD

$$NTF(z) = \frac{1}{1+L(z)} = \frac{1}{1 + \frac{z^{-1}}{1-z^{-1}} - T_d z^{-1}}$$

$$= \frac{(1-z^{-1})}{1 - T_d z^{-1} (1-z^{-1})} = \frac{(1-z^{-1})}{1 - T_d z^{-1} + T_d z^{-2}}$$

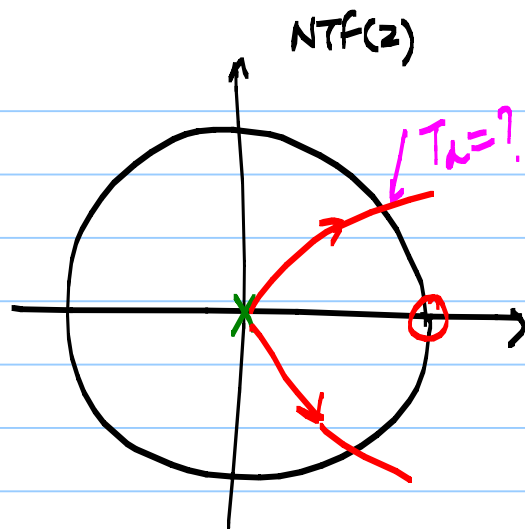
← without ELD

* for low frequency, $\omega \approx 0 \Rightarrow z \approx 1$

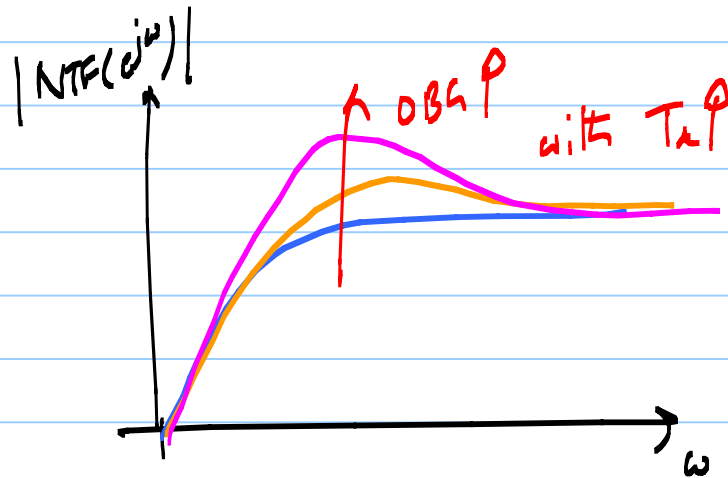
$$NTF(z) \Big|_{z=1} \approx (1-z^{-1})$$

* poles of the NTF are at

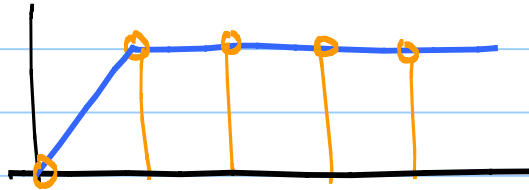
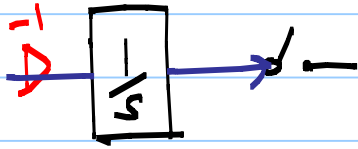
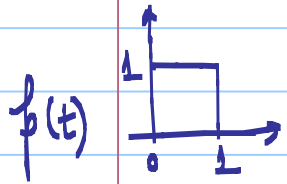
$$p_{1,2} = \frac{T_d \pm \sqrt{T_d^2 - 4T_d}}{2}$$



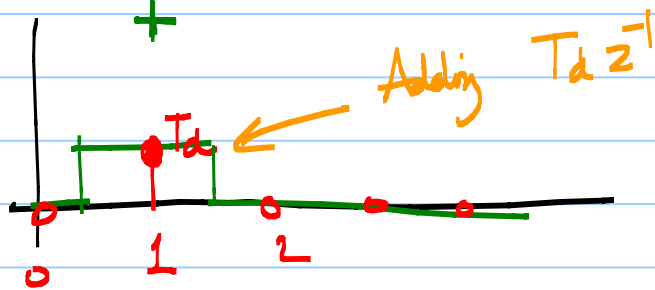
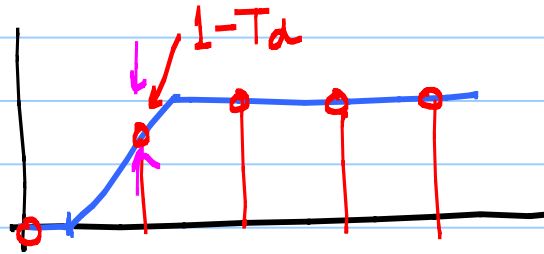
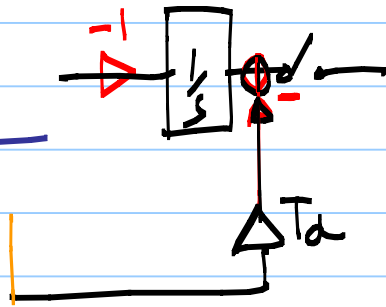
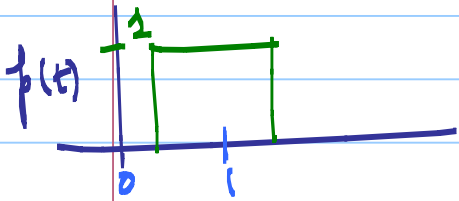
$$= \frac{T_d}{2} \left[1 \pm \sqrt{1 - \frac{4}{T_d}} \right]$$



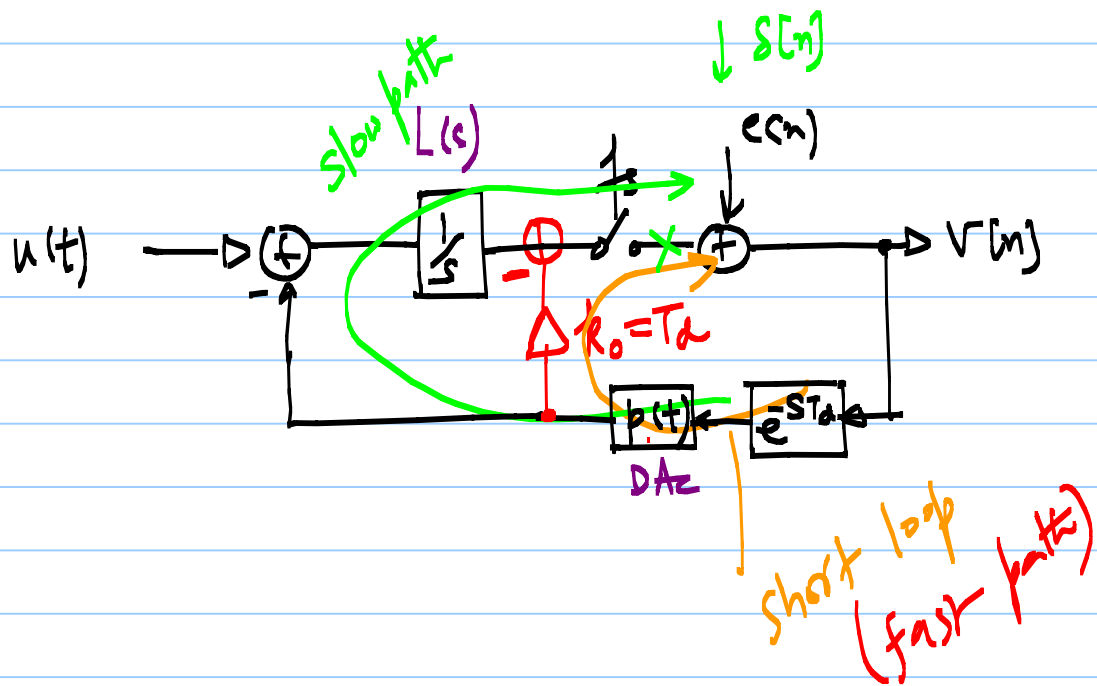
Ideal



With ELD

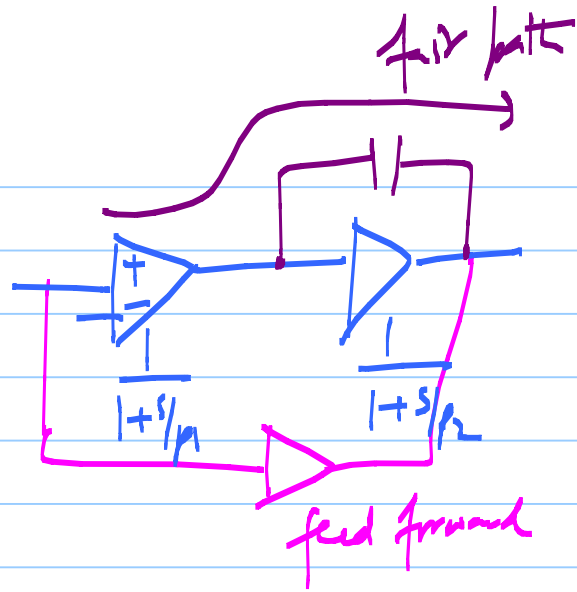


"LED compensation scheme"

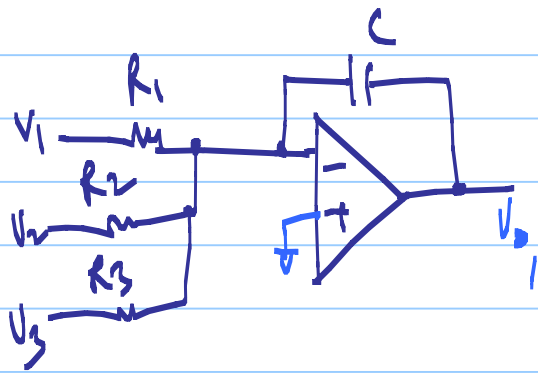


$$L(z) = \left(\frac{z^1}{1-z^1} - \cancel{T_d z^{-1}} \right) + \cancel{T_d z^{-1}} = \frac{z^1}{1-z^1} \Rightarrow L(z) \text{ is fixed} \\ \Rightarrow \text{NTF is fixed}$$

ASIDE



short loop \Leftrightarrow fast path



$$V_0 = \frac{1}{sC} \left[\frac{V_1}{R_1} + \frac{V_2}{R_2} + \frac{V_3}{R_3} \right]$$