

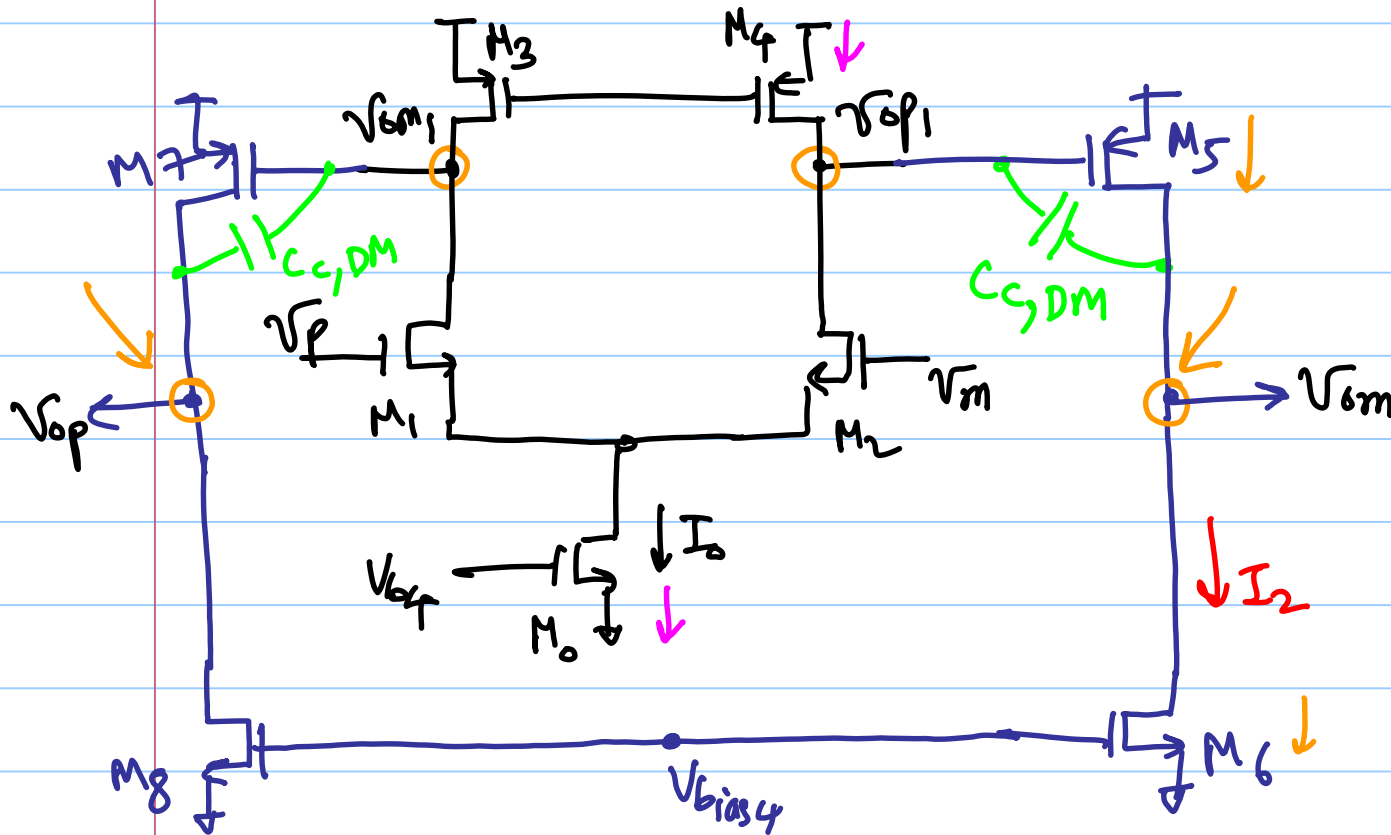
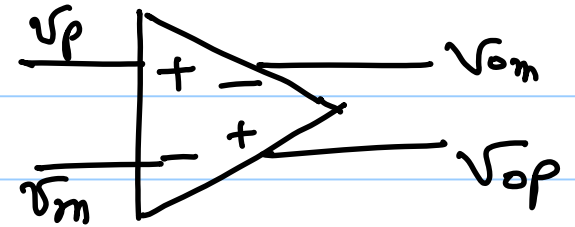
ECE 614 - Lecture 4

Note Title

9/4/2014

- * Resistive averaging CM-detector
- * dual diff-pair based CM-detector

Two-stage fd opamp

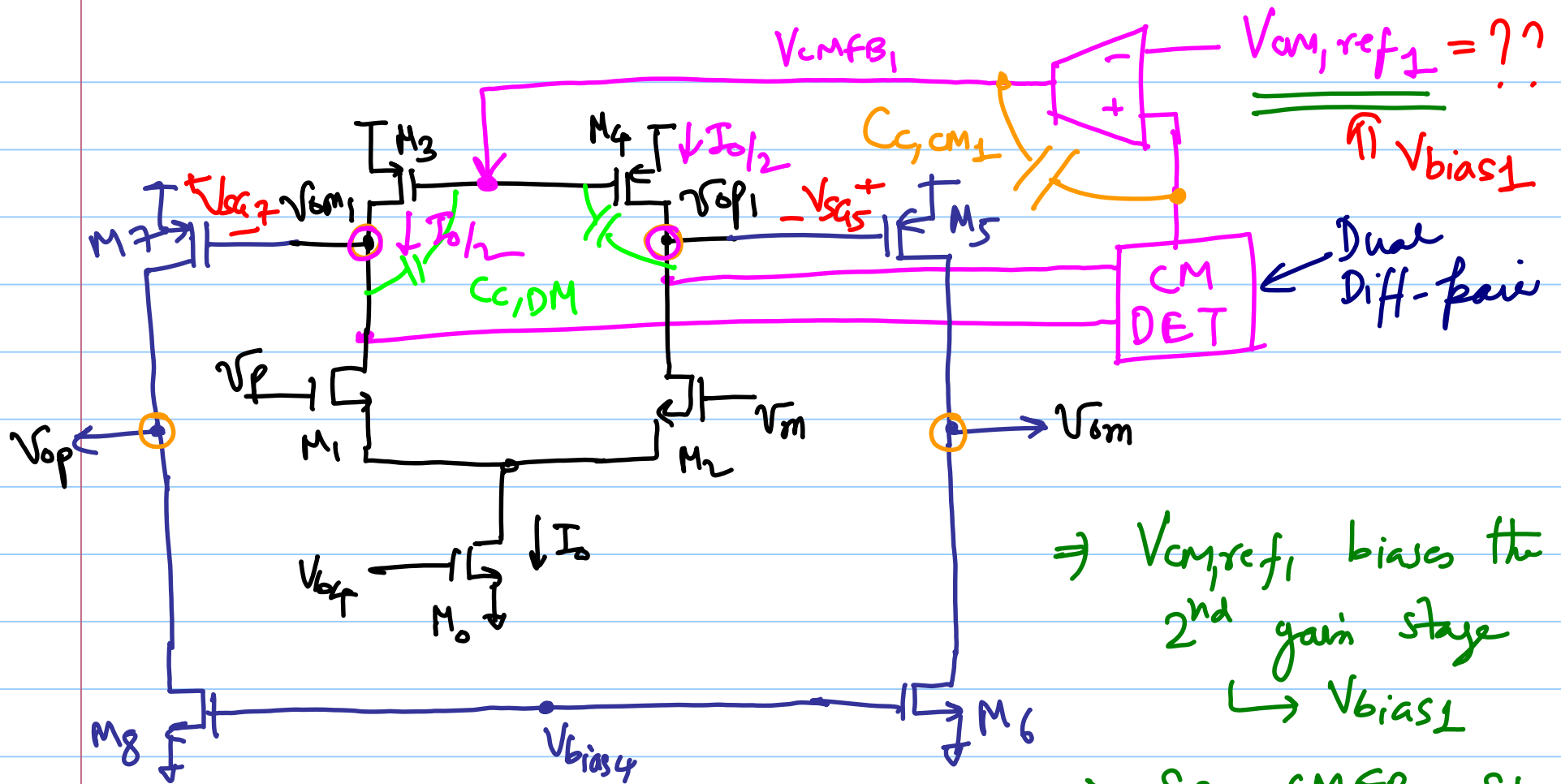


We have
Two sources to
adjust

$M_3 \& M_4 \rightarrow$ current
equal to $\frac{I_0}{2}$

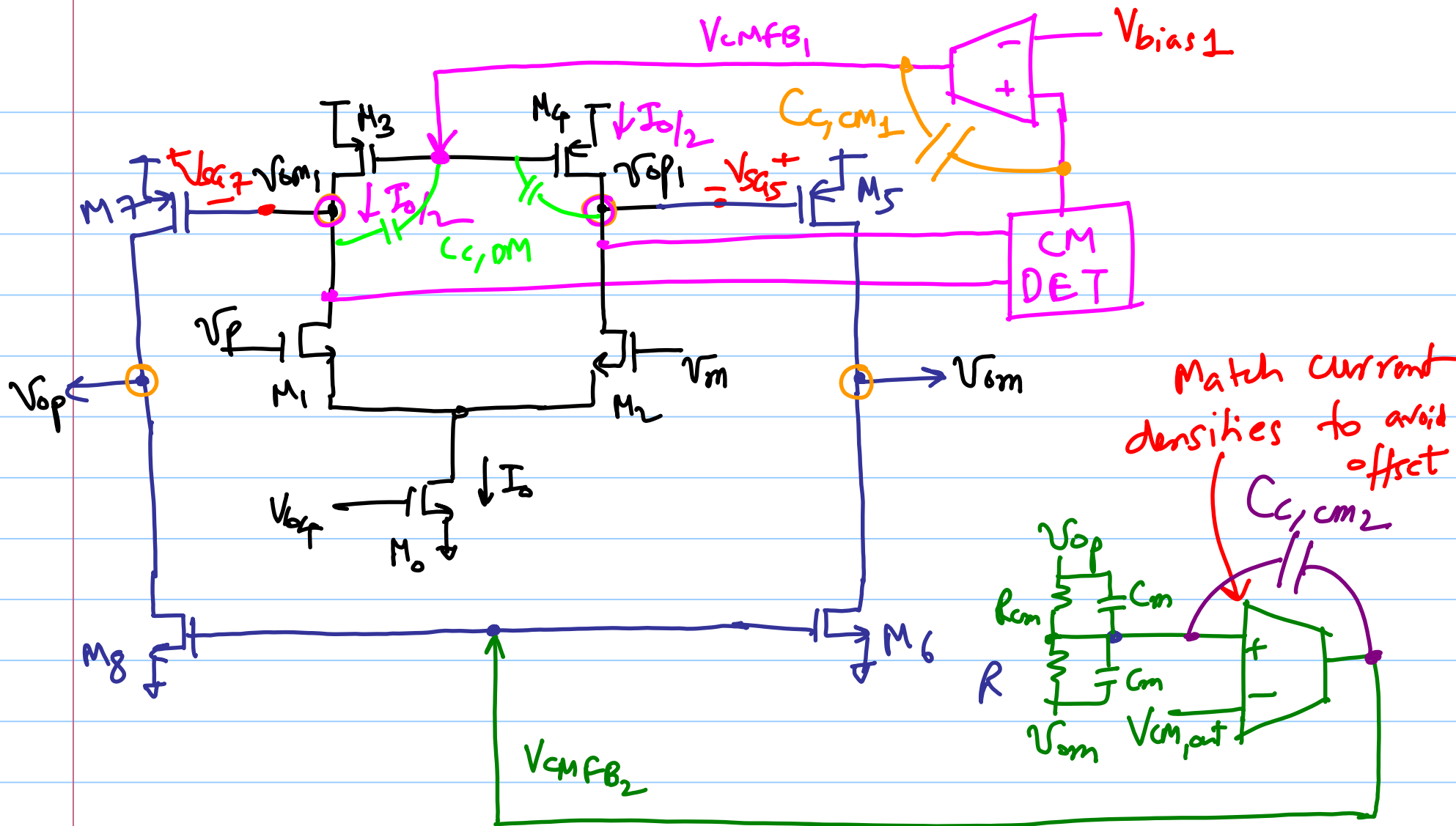
$M_6 \& M_8 \leftrightarrow$ current
in $M_5 \& M_7$

- Two options:
- ① We use a single point of control
 - ② Two separate CMFB loops for each of the nodes

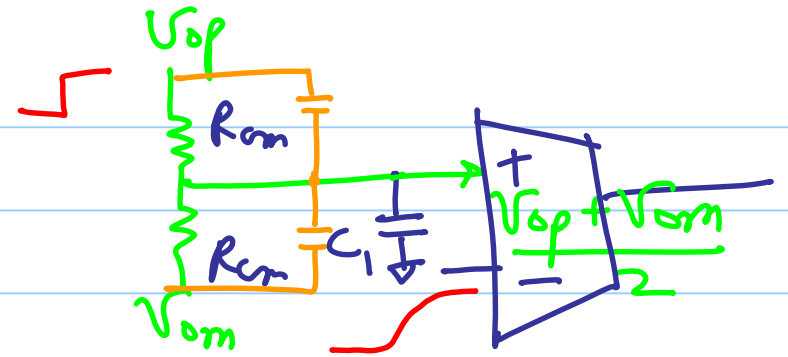


$\Rightarrow V_{cm,ref1}$ biases the 2nd gain stage
 $\hookrightarrow V_{bias1}$

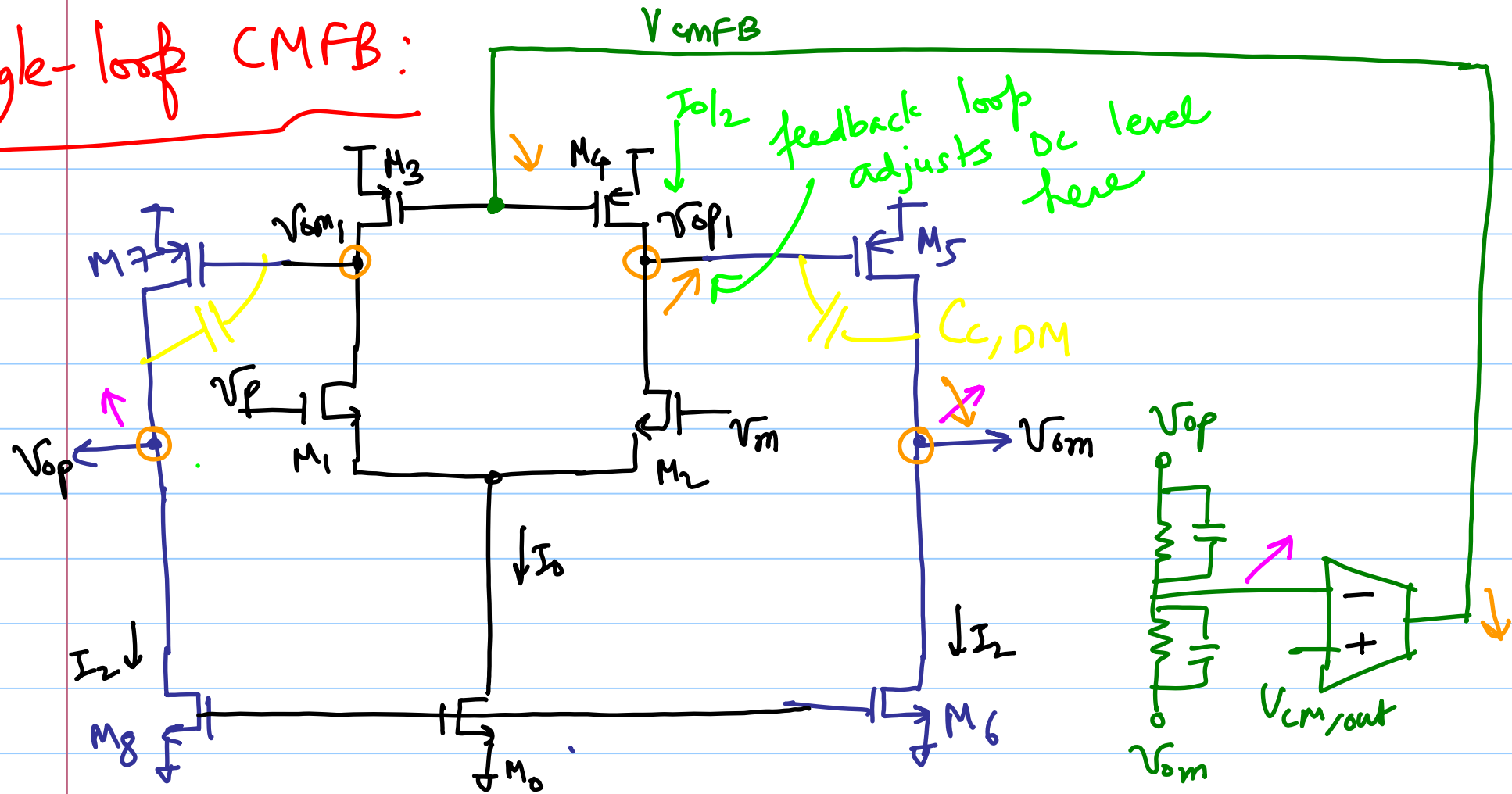
$\Rightarrow$ Size CMFB1 s.t. there is no systematic offset.



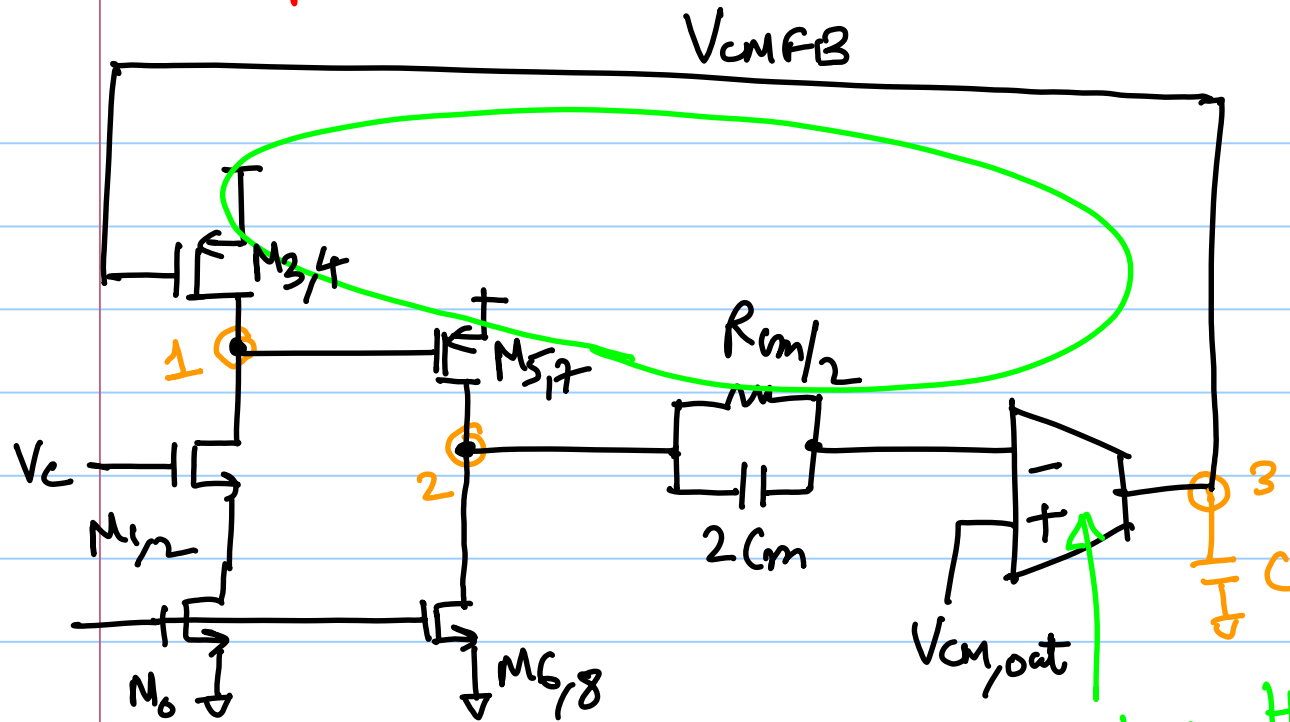
Control M_5 & M_8 s.t the current is equal to I_{M_5} & M_7



Single-loop CMFB:



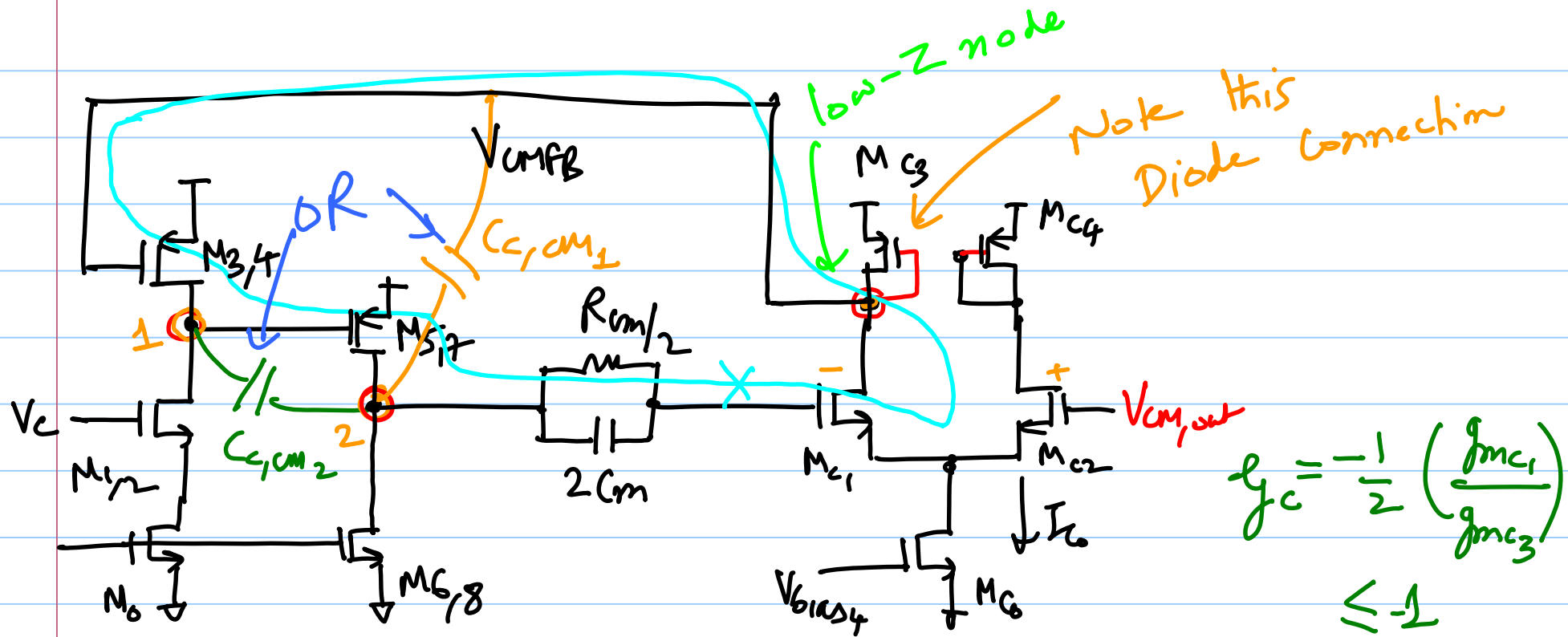
CM picture



* 3 low-frequency poles
 ↳ complex compensation

* how to get rid of this 3-pole situation

kill the gain of this error Amp??



* Can use the differential path C_c for compensating the CMFB loop itself.