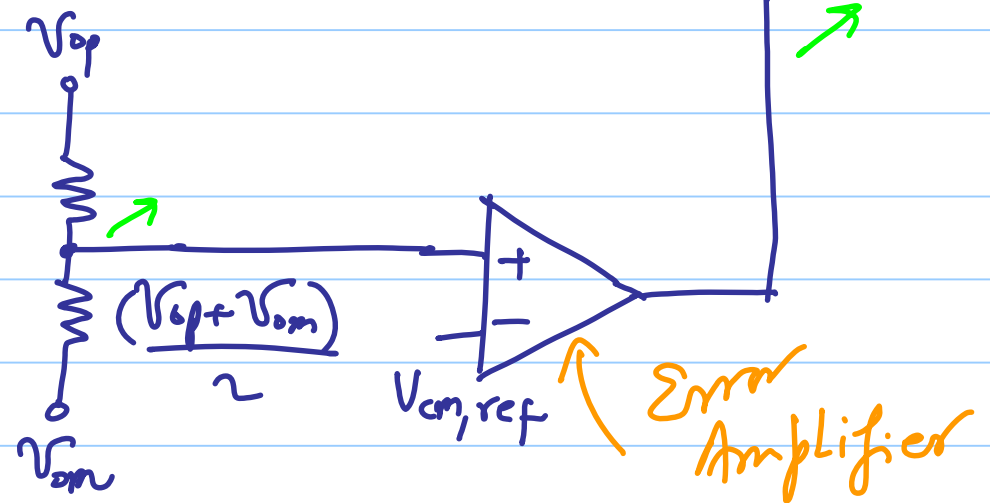
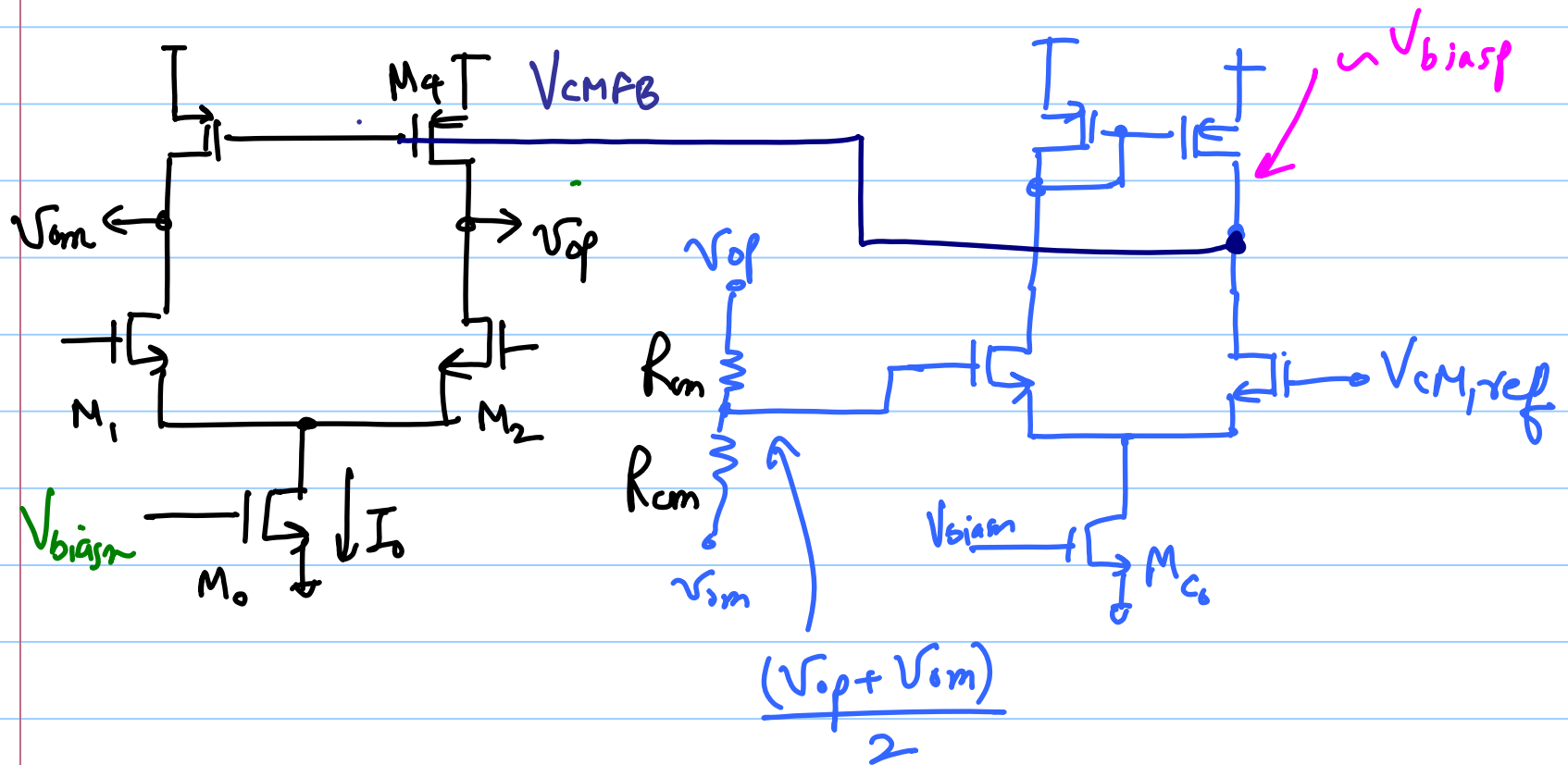
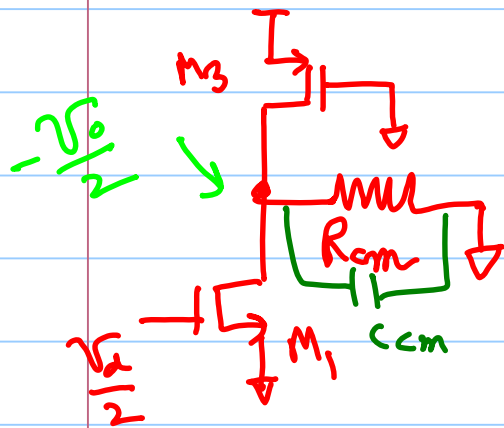
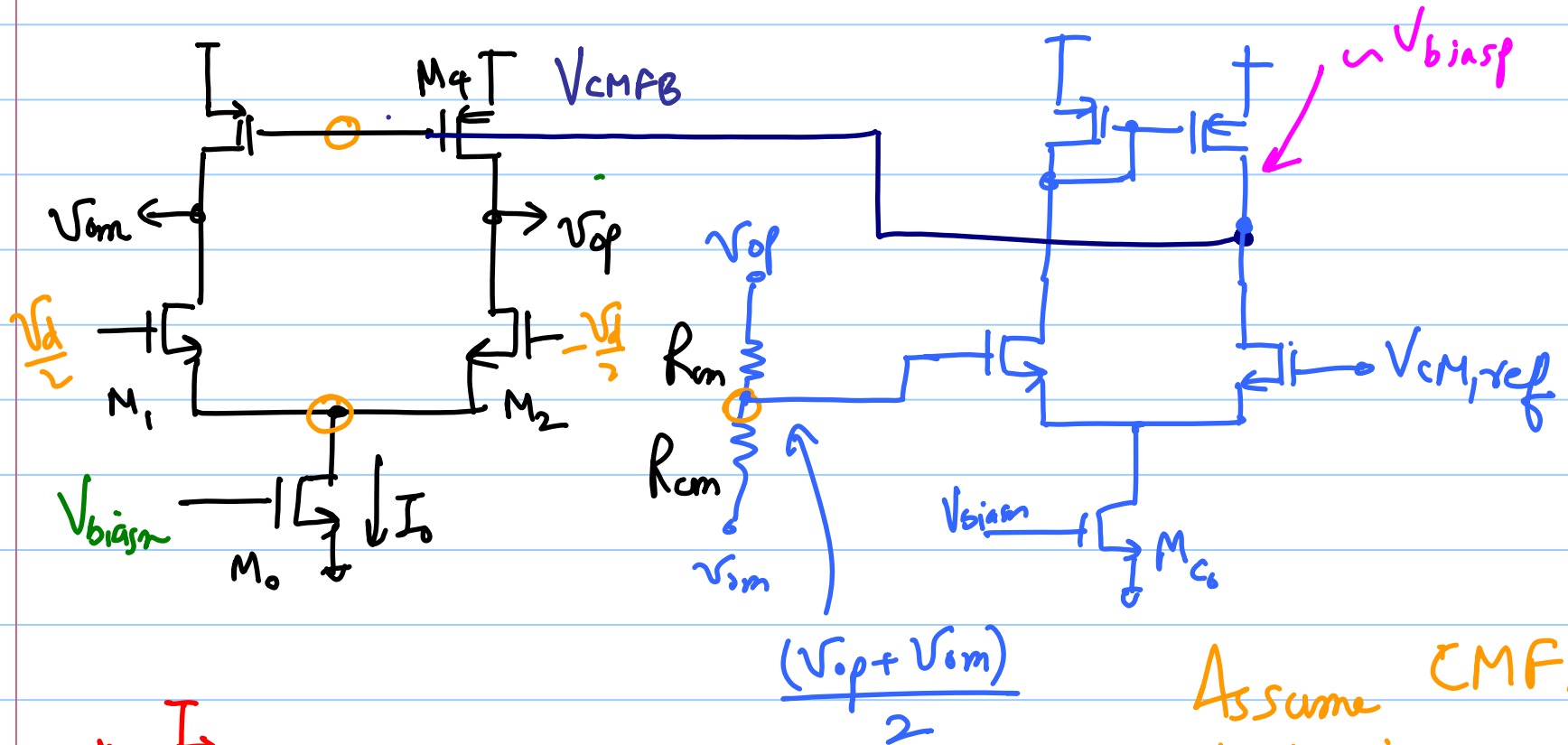


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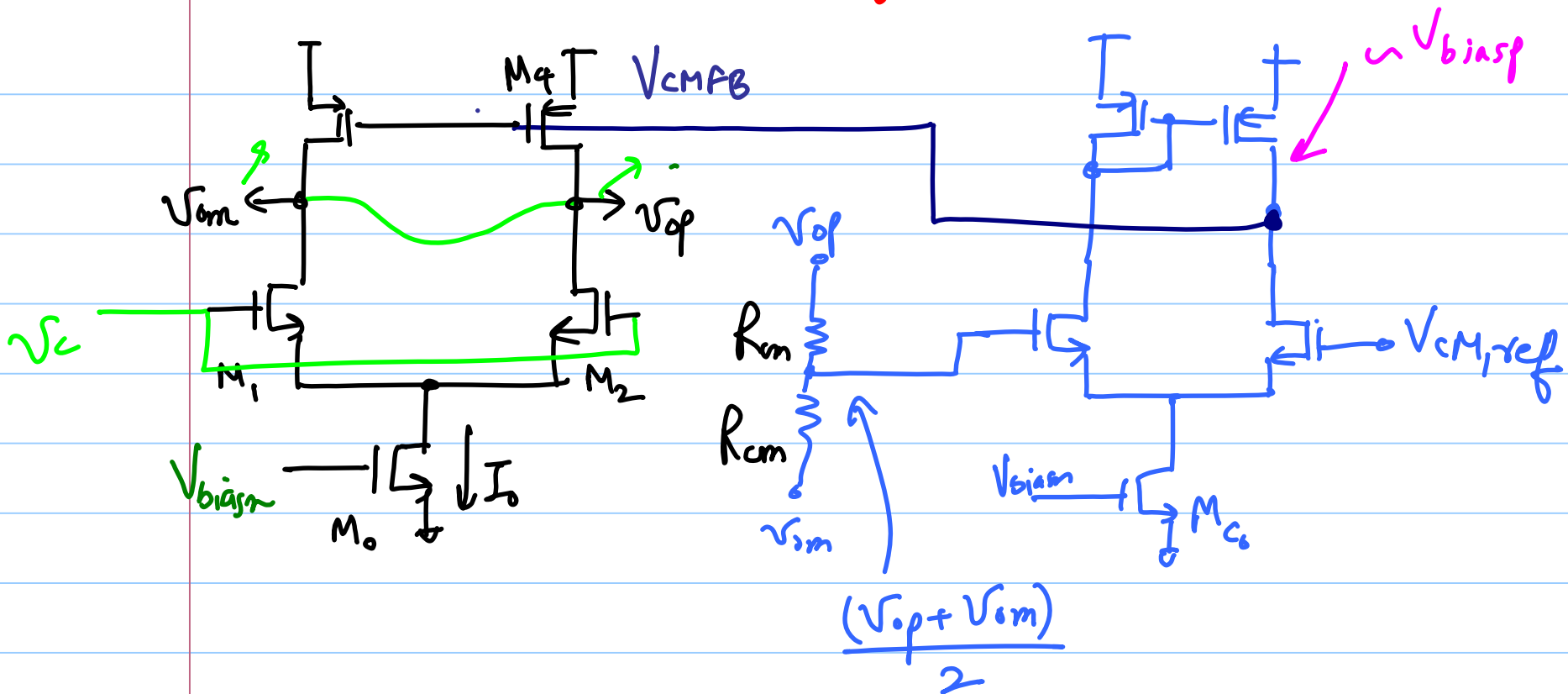
Differential Half Circuit:



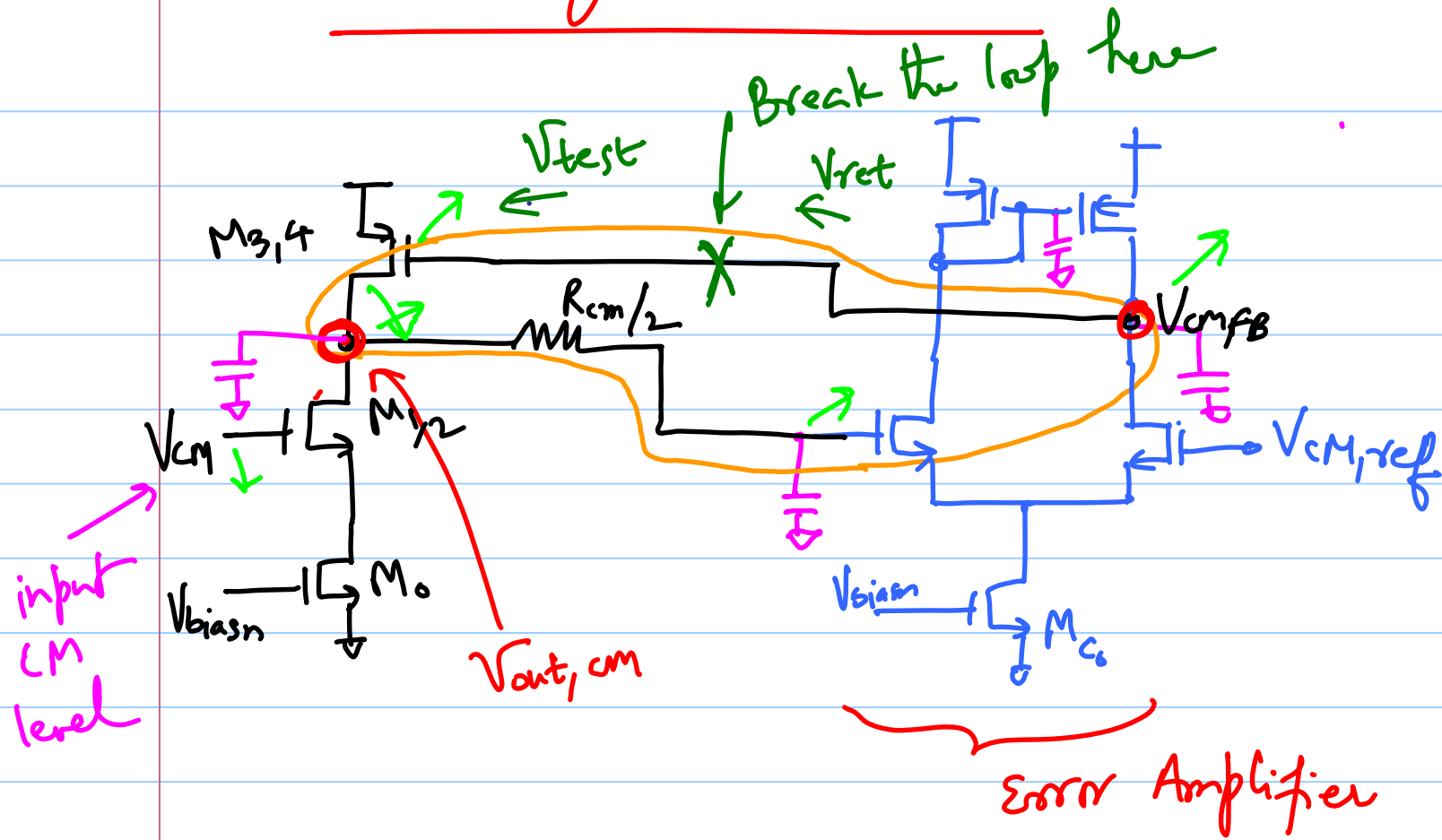
$$\frac{v_{op}}{v_{id}} = g_{m1} (r_{o1} || r_{o3} || R_{cm})$$

Assume CMFB loop is stable

Common-mode Equivalent Circuit

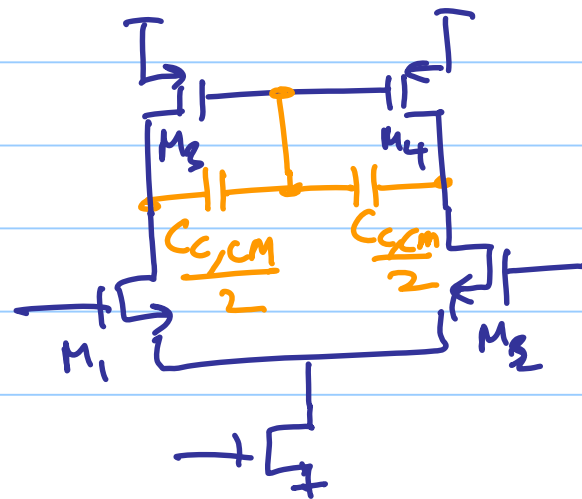
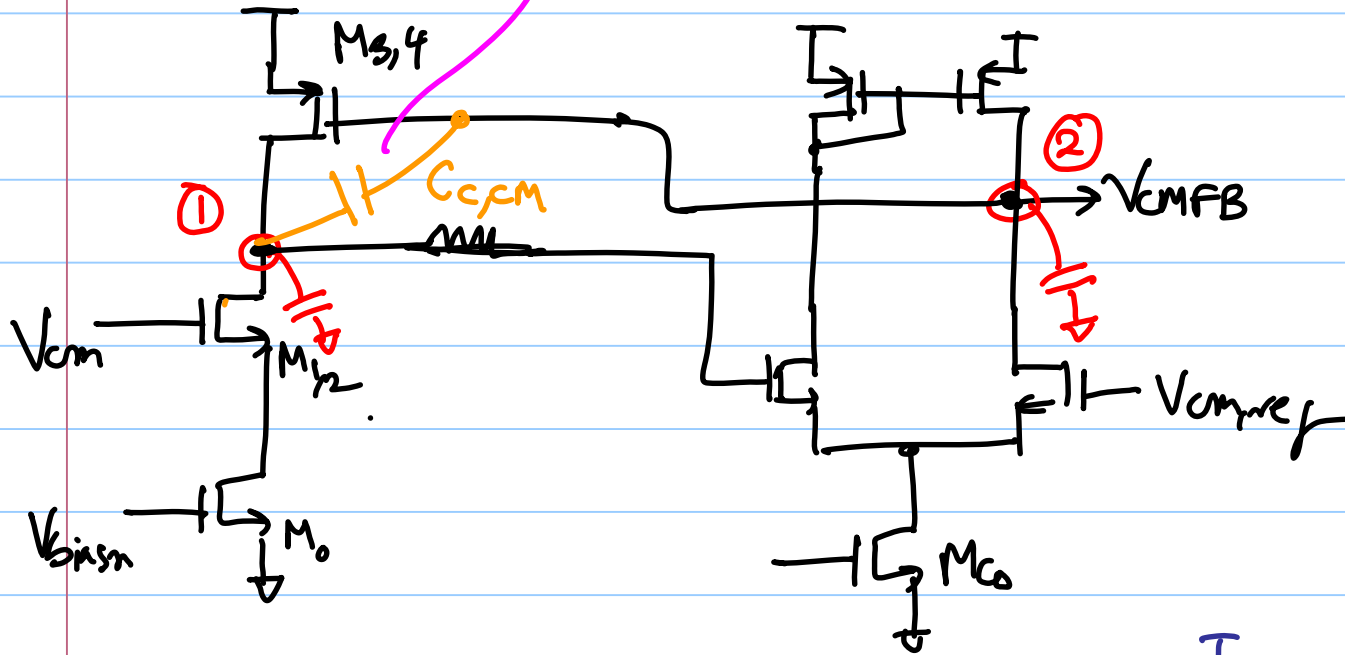
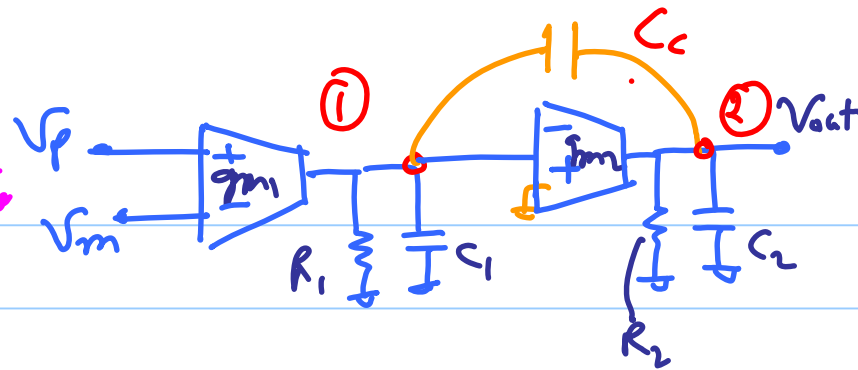


CM Equiv Circuit

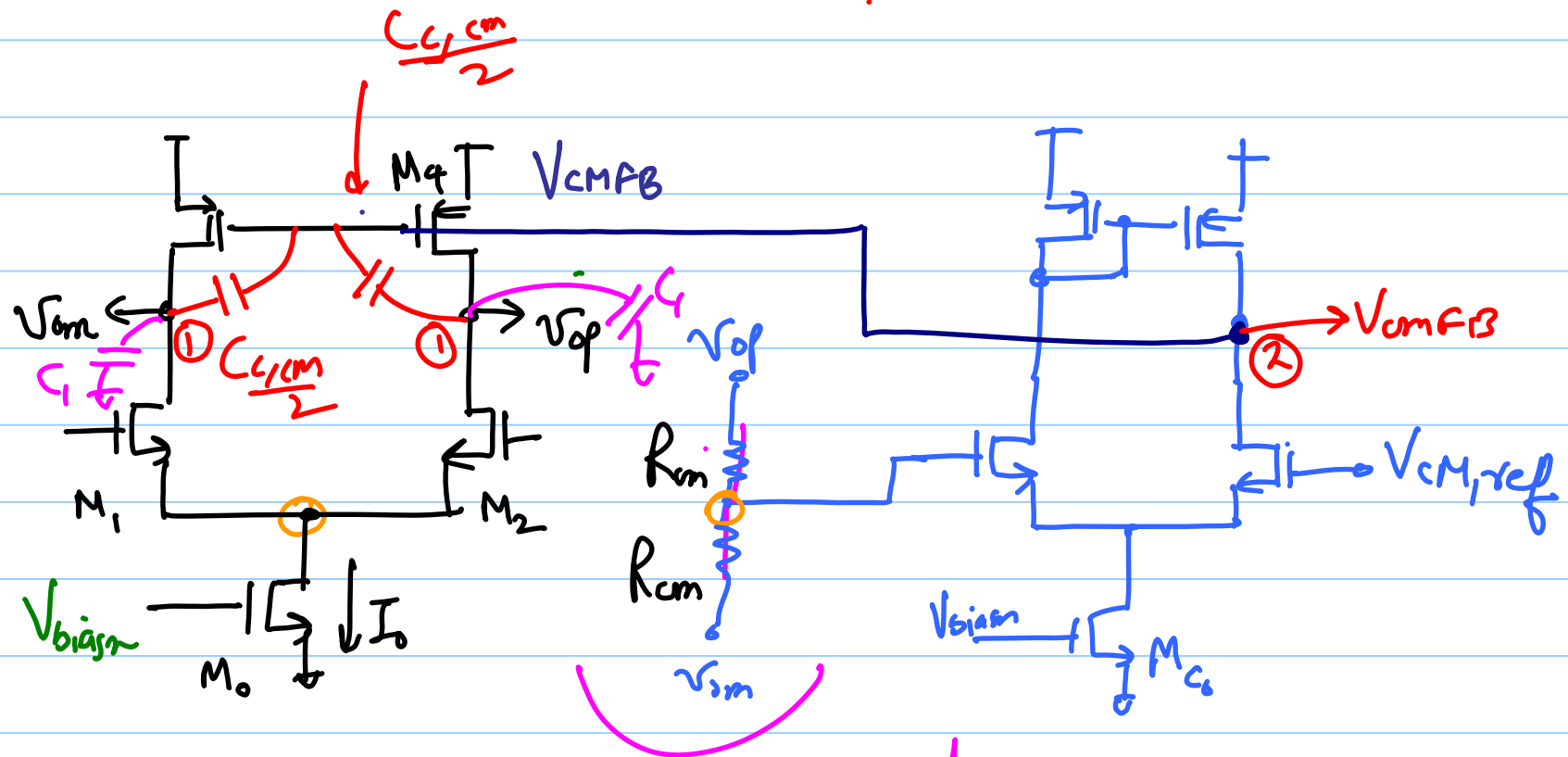


Ignoring $R_{cm}/2$

compensation cap for the CMFB circuit



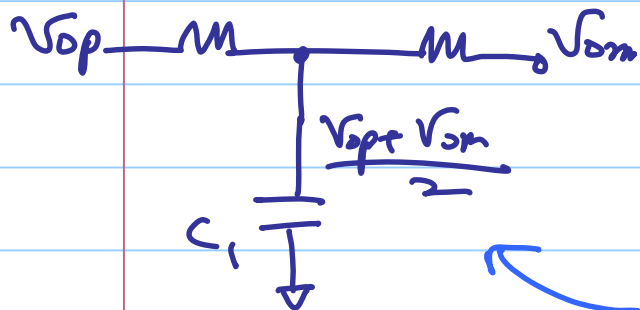
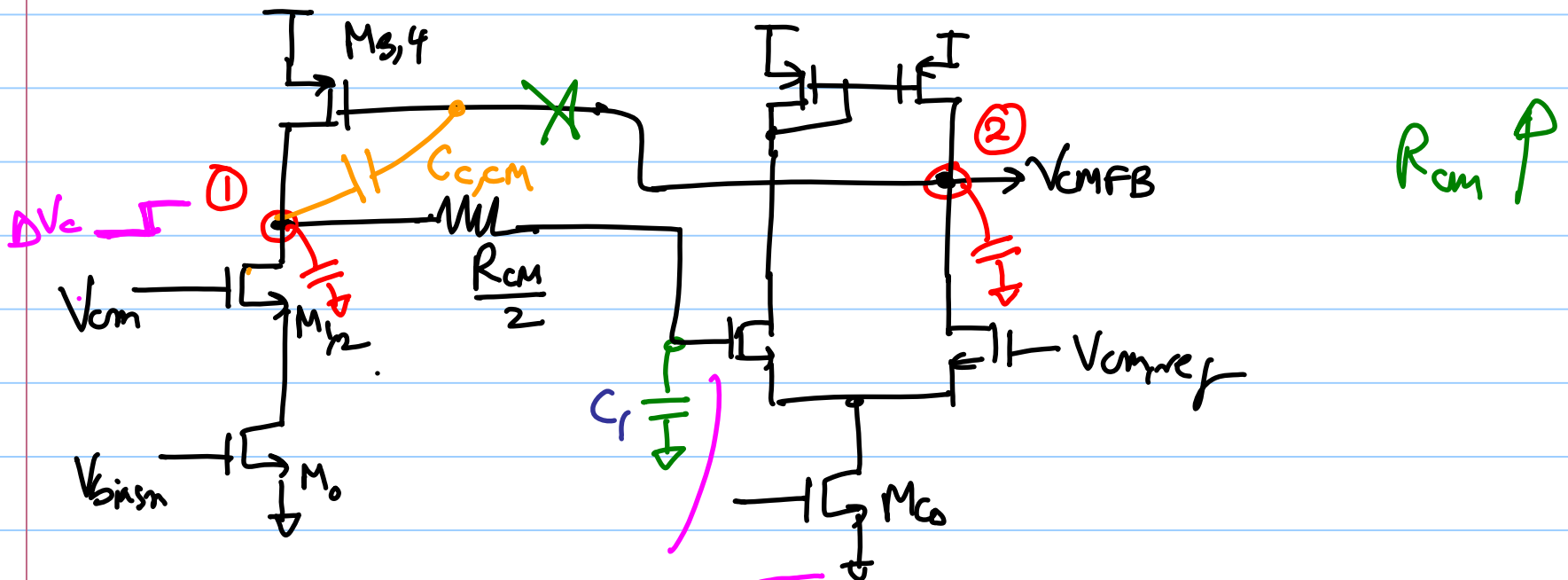
The overall circuit



* $C_{g,cm}$ loads the differential picture

we ignored this!

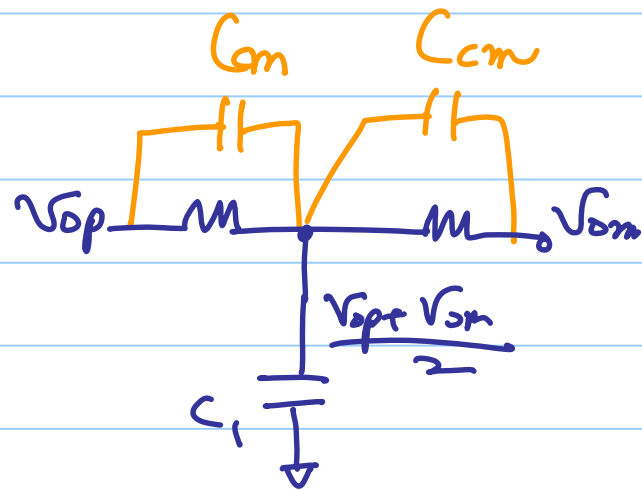
* Lets place $\frac{R_{cm}}{2}$ back into the CMFB circuit.



↑ excess delay in the loop

works at smaller frequencies
but at larger frequencies
→ phase lag ⇒ delay

* we may have instability in the CMFB loop as the CM detector doesn't "work" at high frequencies



← responds at high frequencies

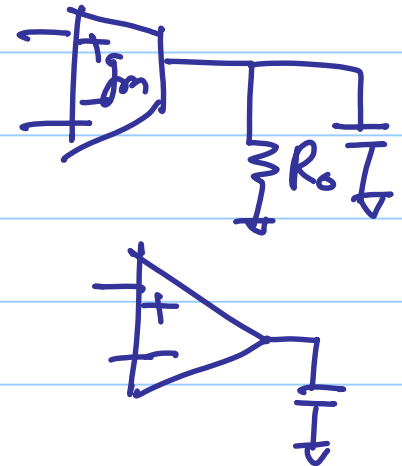
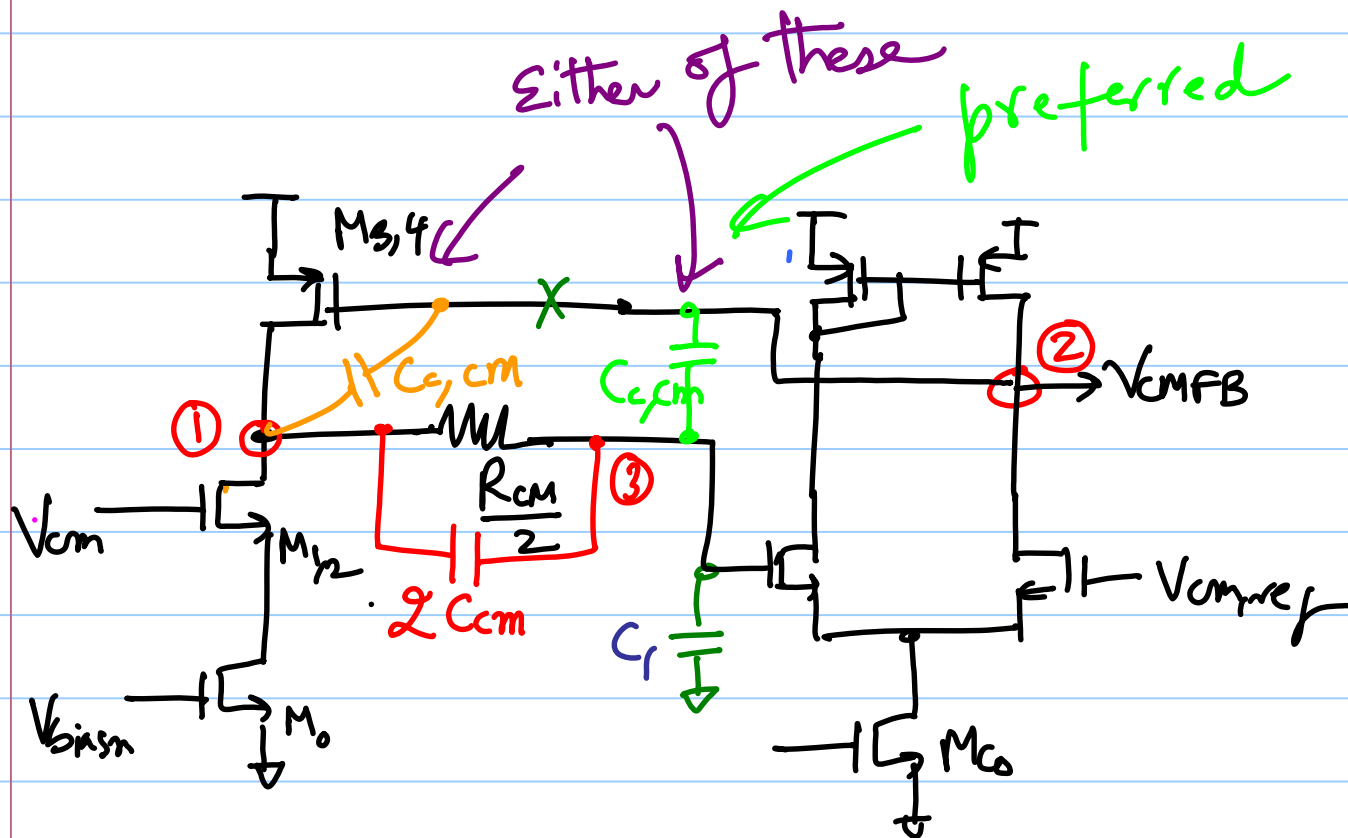
at high frequencies, the CM-detector output

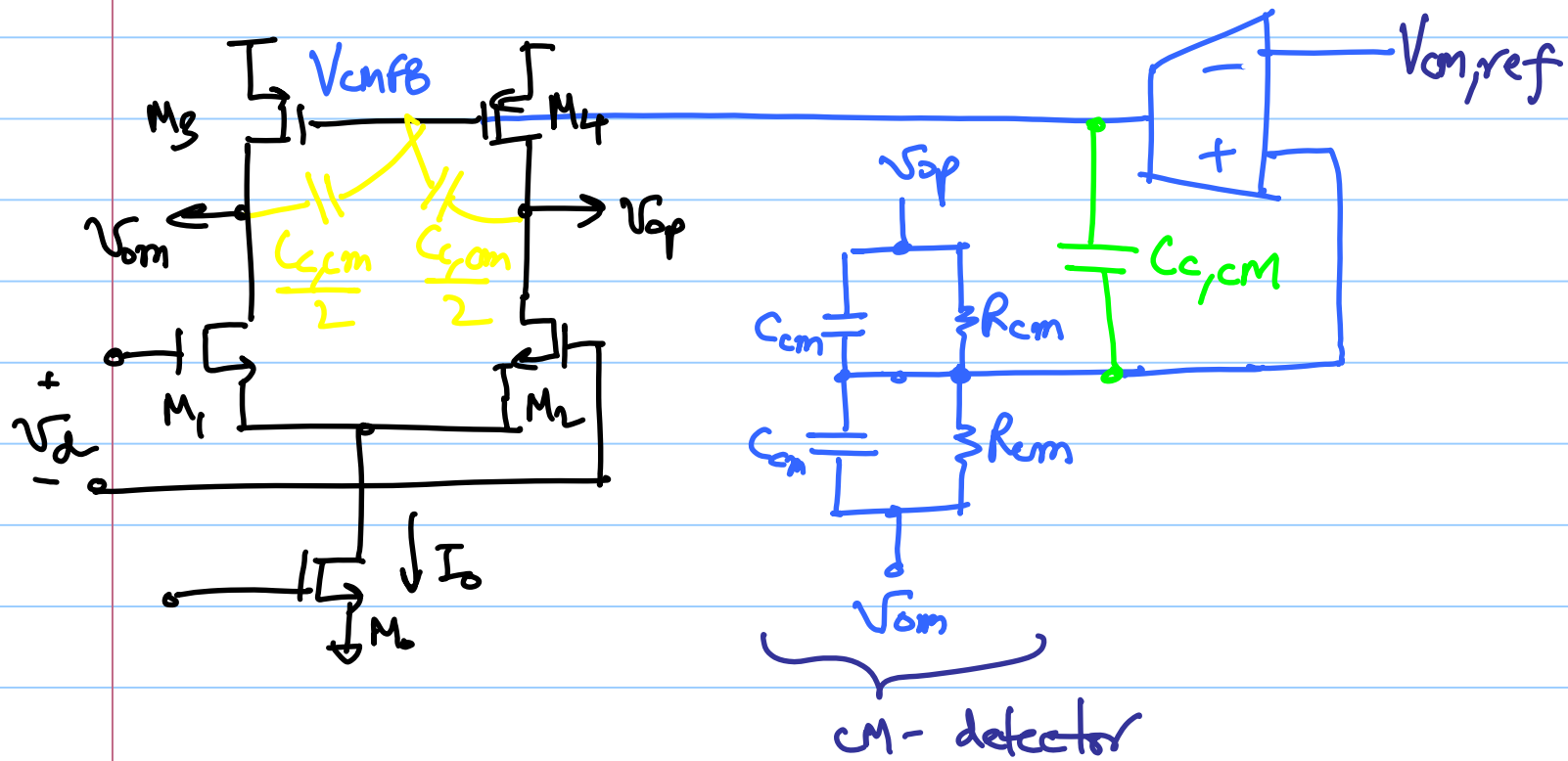
$$V_{op} \left(\frac{C_{cm}}{2C_{cm} + C_1} \right) + V_{om} \left(\frac{C_{cm}}{2C_{cm} + C_1} \right)$$

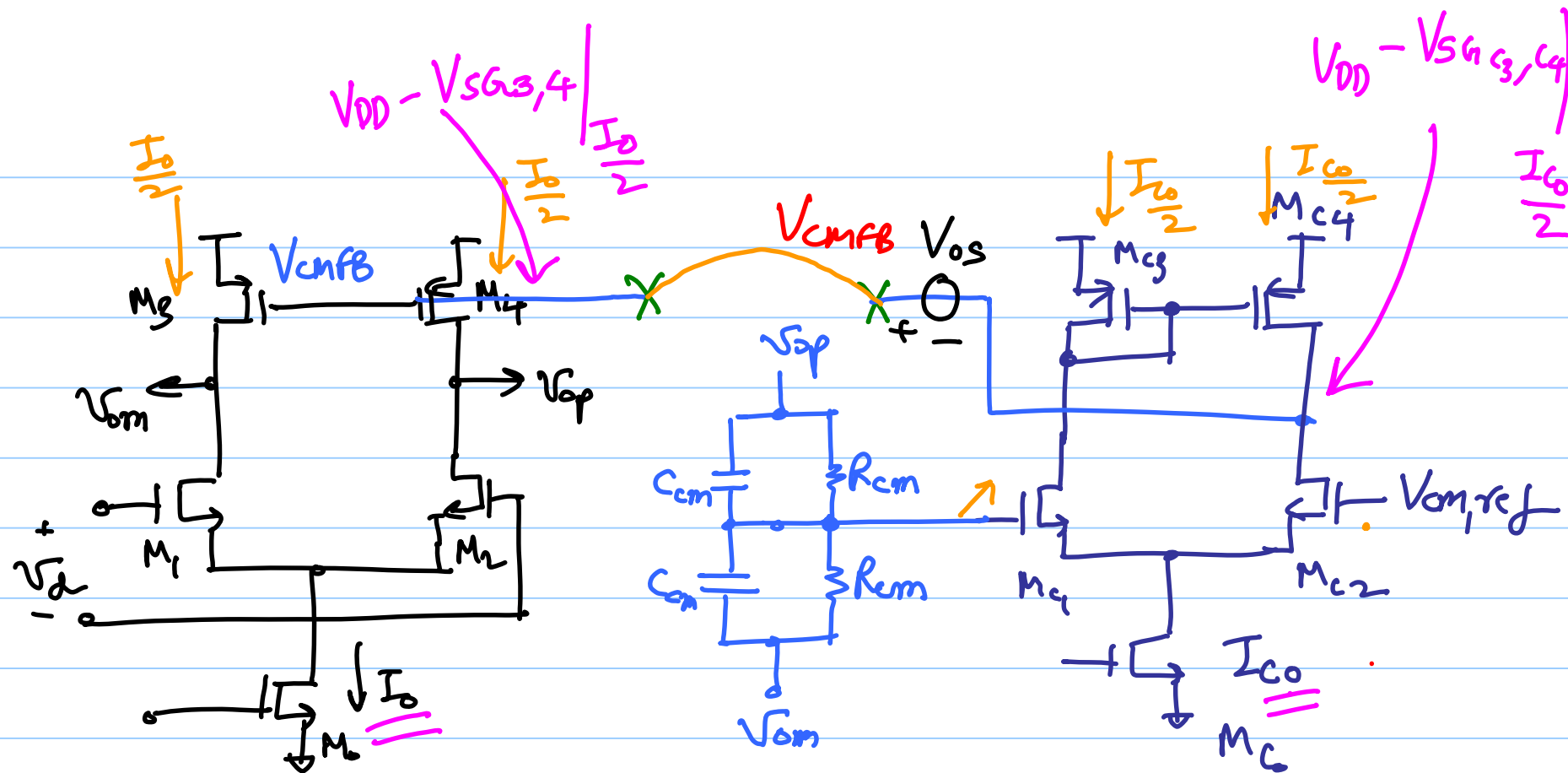
$$= \left(\frac{V_{op} + V_{om}}{2} \right) \left(\frac{2C_{cm}}{2C_{cm} + C_1} \right) \leftarrow \text{parasitic cap causes attenuation}$$

but no phase lag at high frequencies

Typical $C_{\text{cm}} \subseteq C_1$







When the CMFB loop stabilizes
the V_{cmfb} goes from

$$\left(V_{DD} - V_{SG_{3,4}} \left| \frac{I_{co}}{2} \right. \right) \text{ to } \left(V_{DD} - V_{SG_{3,4}} \left| \frac{I_o}{2} \right. \right)$$

$$\left| \left(\frac{V_{op} + V_{om}}{2} \right) - V_{CM,ref} \right| \rightarrow \frac{\left(V_{DD} - V_{SG_{3,4}} \right) \left| \frac{I_{a2}}{2} \right| - \left(V_{DD} - V_{SG_{3,4}} \right) \left| \frac{I_{a1}}{2} \right|}{\text{loop-gain}}$$

↑ "system offset"