

Homework 2

ECE 614 — Advanced Analog IC Design (Fall 2014)

Due on Tuesday, Sep 30, 2014

Problem 1: For each of the example common-mode feedback loop designs shown in the *CMFB Examples* handout: Explain the operation of the CMFB loops in both the opamp stages (can use hand-sketches), and comment on their relative advantages and disadvantages. Bookmark these discussions for your next assignments.