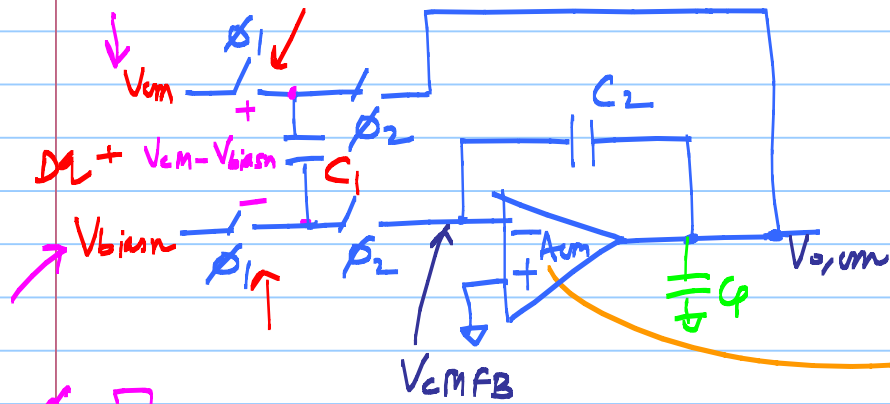


# ECE 614 - Lecture 8

Note Title

9/20/2012

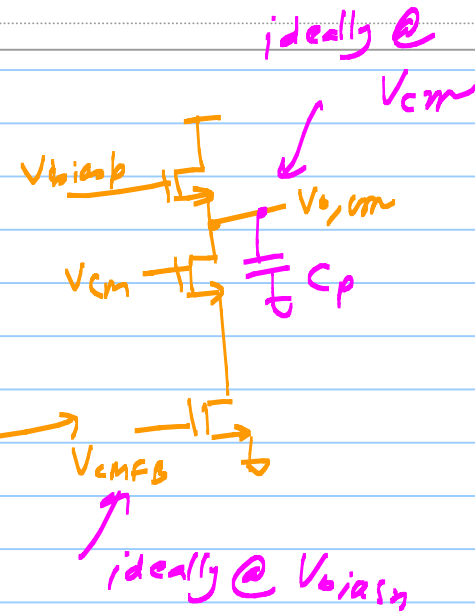
CM Equivalent Circuit



$\phi_1$

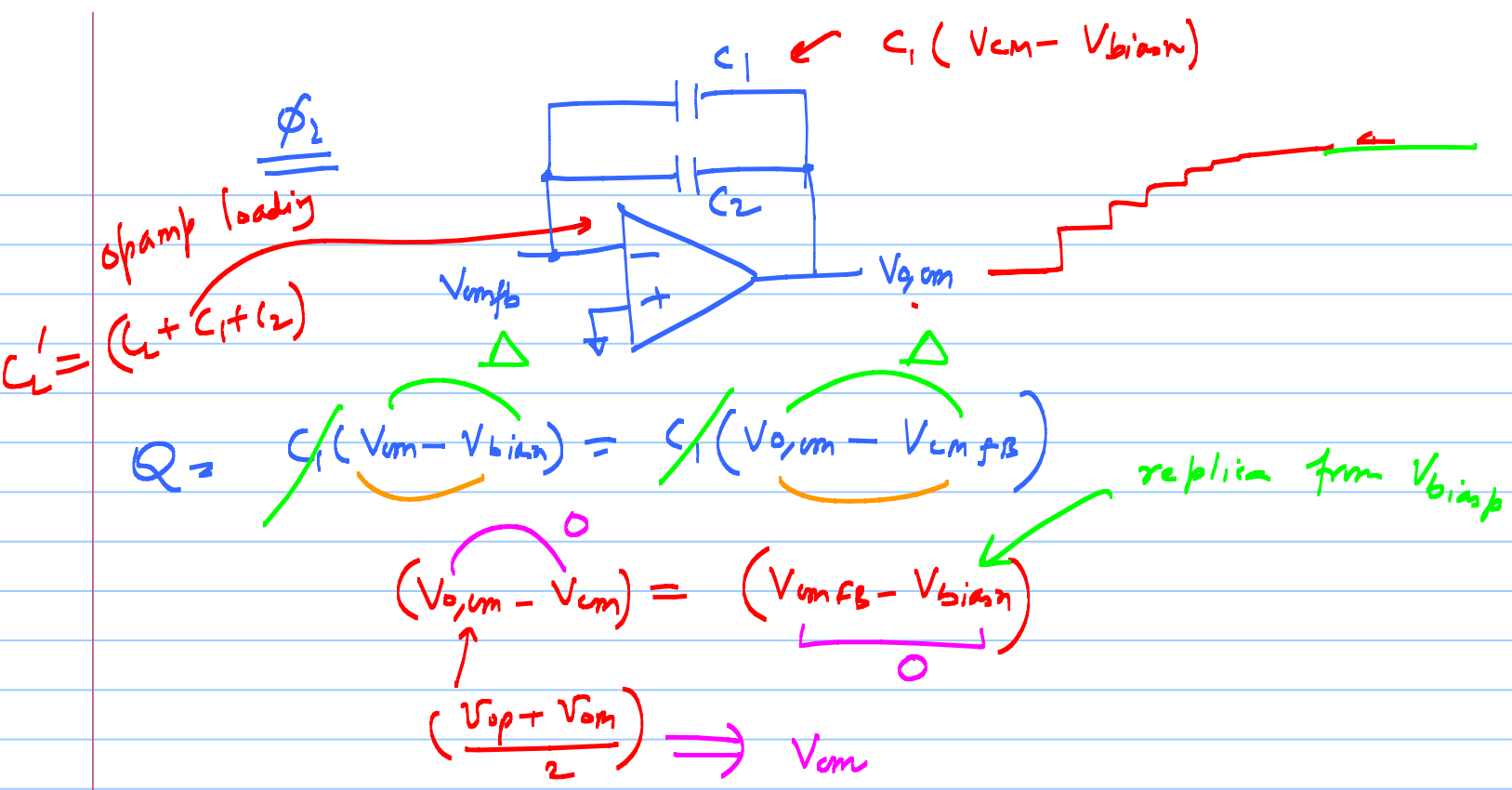
$\phi_2$

$$Q = C_1 (V_{cm} - V_{biasn})$$



ideally @  $V_{cm}$

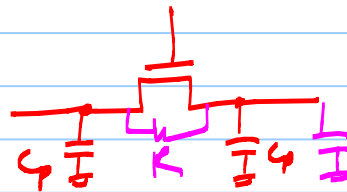
ideally @  $V_{biasn}$



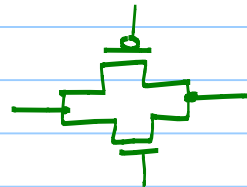
(+) o/p voltage swing is not limited by CM-detector  $C_2$   
 Caps don't load the o/p and kill the gain

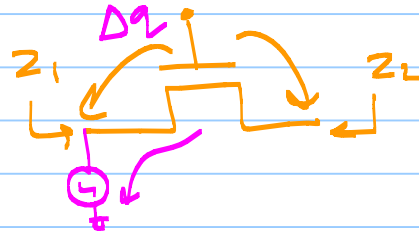
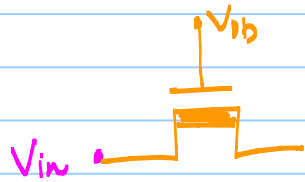
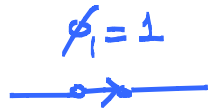
$$* (C_1 + C_2) < 1\% \text{ of } C_L$$

(+) looks like single-pole system  
 "DT"



$$R_C \ll T/2$$





"charge injection"

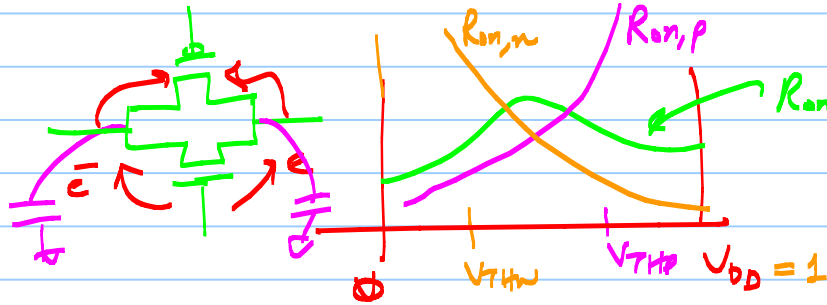
"error"

→ DC

→ distortion

$\Delta q \propto f(V_{in})$

$\Delta q \propto f(V_{in})$



$\Rightarrow W_b \Rightarrow R_{on} \uparrow$

$Z = R_{on} C_i$

$\Rightarrow W \uparrow \Rightarrow \Delta q \uparrow$

\* make  $\Delta q$  independent of  $V_{in} = V_{in,p} - V_{in,m}$

\* TG

\* fully-differential circuit

\* Mos transistor switches inject charge into  $C_1 \rightarrow \Delta q$

$$\Rightarrow C_1(V_{cm} - V_{biasn}) = C_1(V_{o,cm} - V_{cm,FB}) + \Delta q$$

$$\Rightarrow \underbrace{(V_{cm} - V_{o,cm})}_{\downarrow \frac{V_{op} + V_{om}}{2}} = \underbrace{(V_{biasn} - V_{cm,FB})}_0 + \underbrace{\frac{\Delta q}{C_1}}_{\substack{\text{offset in} \\ \text{o/p cm-level} \\ \text{increase } C_1}}$$

$$V_{o,cm}[\infty] = \frac{V_{cm} + \left[ \left(1 + \frac{C_{p1}}{C_1}\right) (V_{CMFB} - V_{biasn}) \right] - \frac{\Delta q_{L1}}{C_1} + \frac{(I_{j1} + 2I_{j2})T}{2C_1}}{1 + \left(\frac{1}{A_m}\right) \left(1 + \frac{C_{p1}}{C_1}\right)}$$

$$Dr \Rightarrow 1 + \left(\frac{1}{A_m}\right) \left(1 + \frac{C_{p1}}{C_1}\right)$$

$A_m \rightarrow \text{large}$

No  $C_2$  !

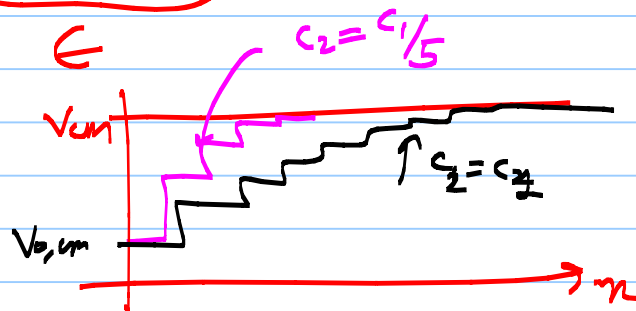
$$Nr \approx \left[ 1 - \frac{1}{A_m} \cdot \left(1 + \frac{C_{p1}}{C_1}\right) \right]$$

DT

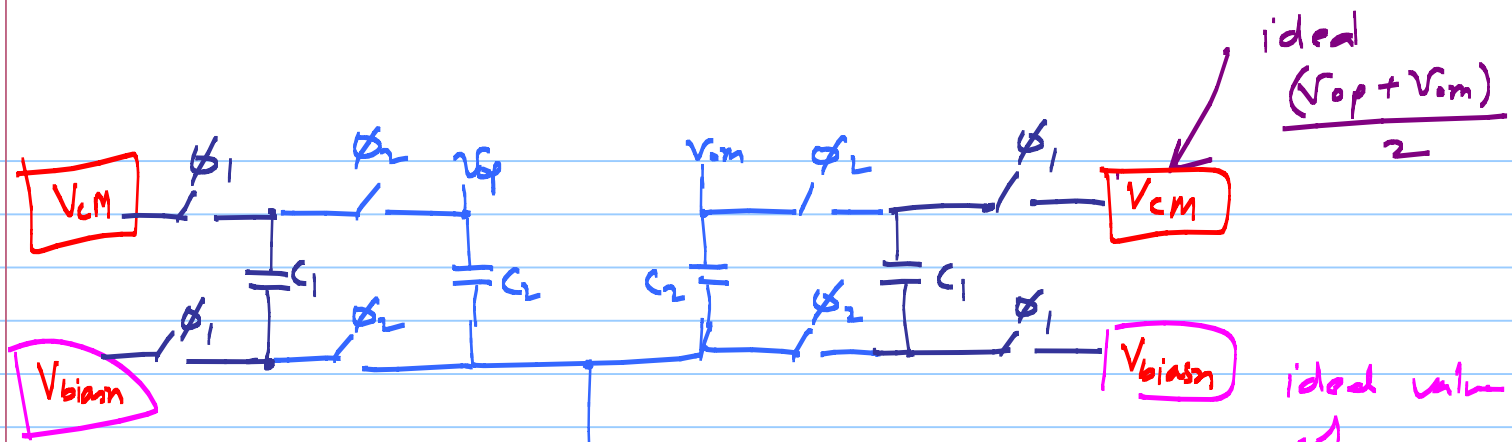
settling time  $\Rightarrow t_s \propto \frac{C_1}{C_2}$

$C_1 = 5 \text{ to } 10 \times \text{ of } C_2$

$$\boxed{\begin{matrix} C_2 = 10 \text{ fF} \\ C_1 = 50 \text{ fF} \end{matrix}}$$



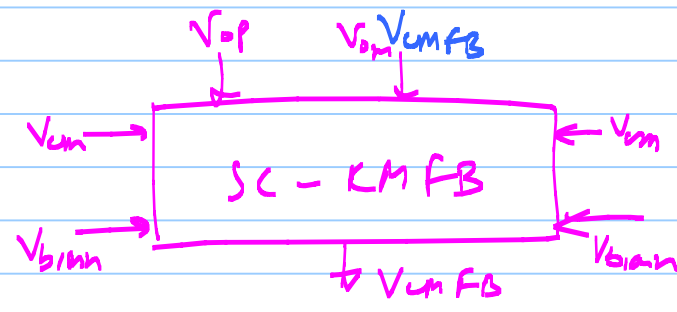




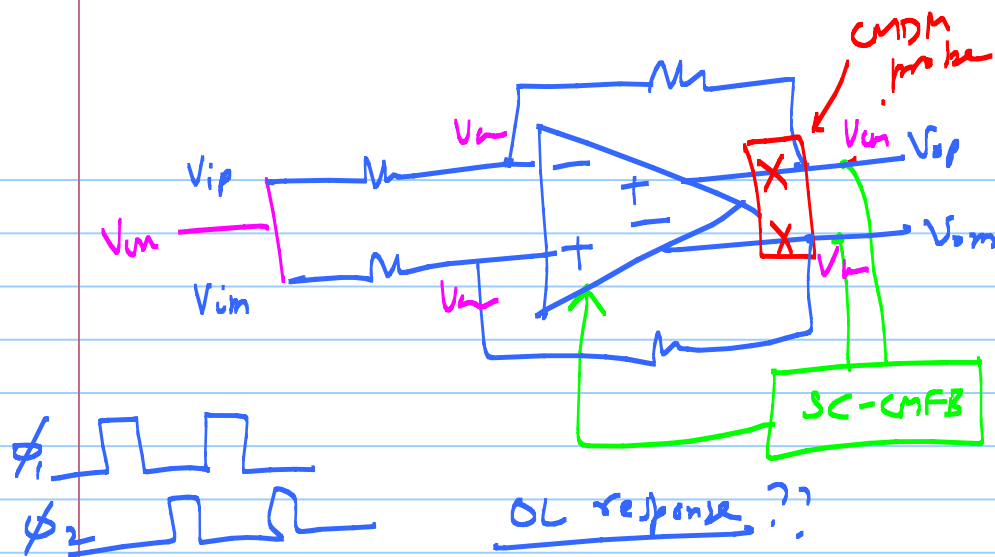
ideal  

$$\frac{(V_{op} + V_{om})}{2}$$

ideal value  
 of  
 $V_{cmFB}$



See CMOS Book



\* Can I use SC-CMFB here ??

↳ need symmetric loading of opamp.

in both phases -  $(C_1 + C_2)$

\* NO DC operating point here!

↳ have a periodic steady state

Spectral RF  $\rightarrow$  \* PSS  $\leftrightarrow$  . op

PSTB  $\leftrightarrow$  STB

PAC, PxF, Pnoise, PDIST

# Noise

Random process

