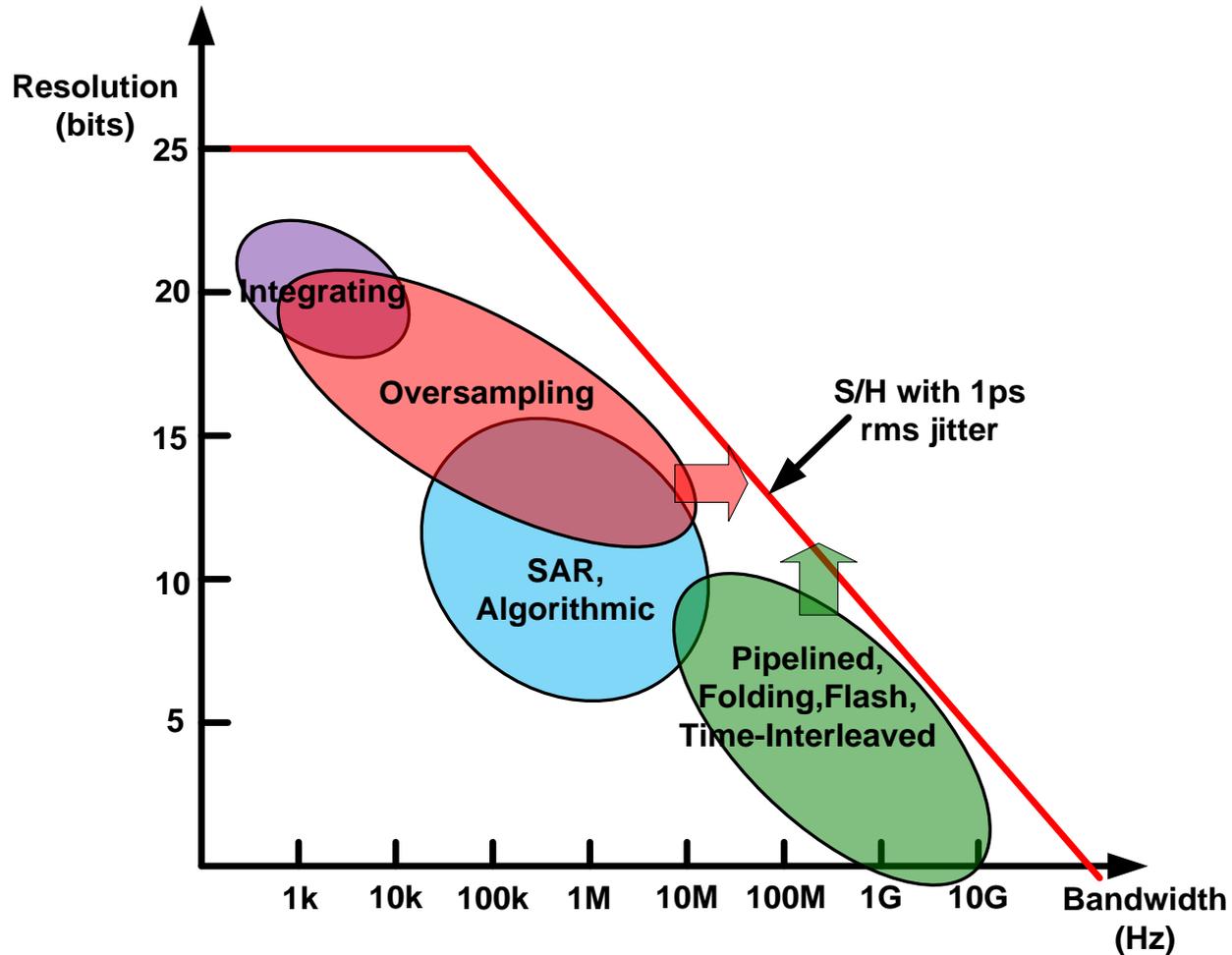


Delta-Sigma Analog-to-Digital Converters

Brief Overview

Vishal Saxena, Boise State University
(vishalsaxena@boisestate.edu)

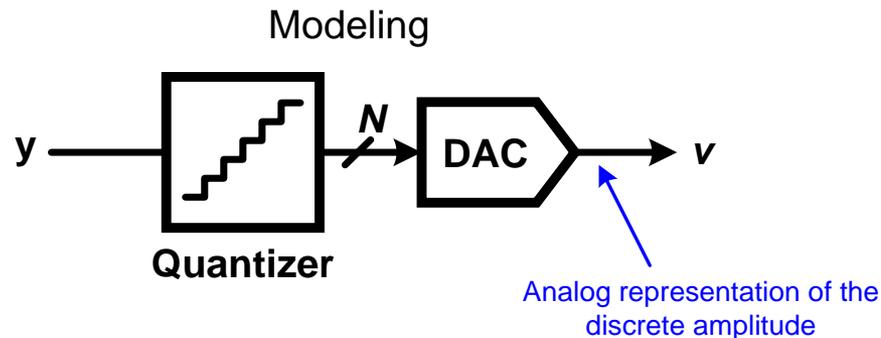
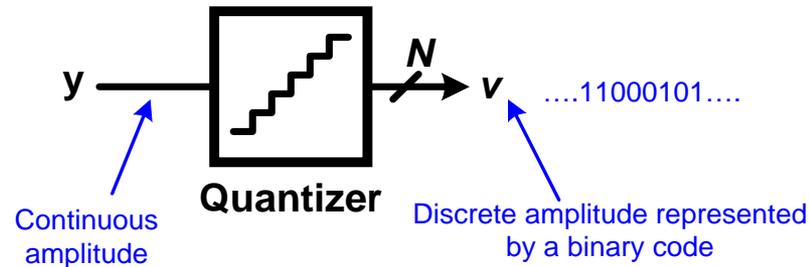
Analog to Digital Converter Architectures



Why larger overlap on SAR and OS?

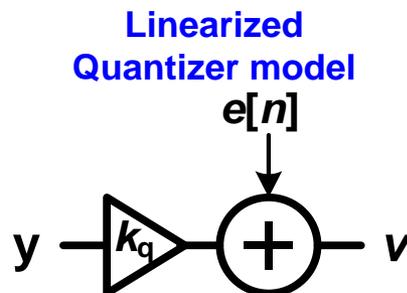
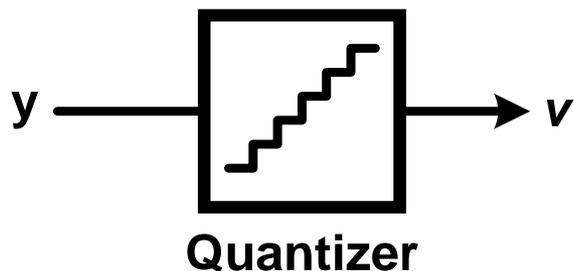
- ❑ Sometimes oversampling modulators are preferred over SAR. Why?
- ❑ Nyquist rate SAR converters can't support complete bandwidth due to stringent requirements on Anti-aliasing Filters.
- ❑ Consider a data converter with 250 ksps output rate.
- ❑ For oversampled modulator $OSR=64$, input is sampled at 16 MHz.
- ❑ Nyquist rate SAR samples the input at 500 kHz.
- ❑ Requirements on anti-aliasing filter in the second case is very hard compared to oversampling case.

Quantization Basics



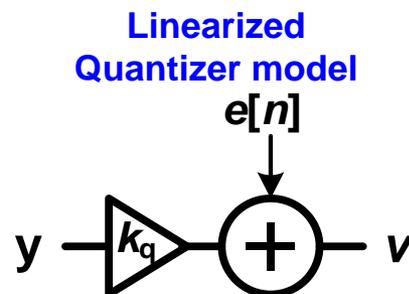
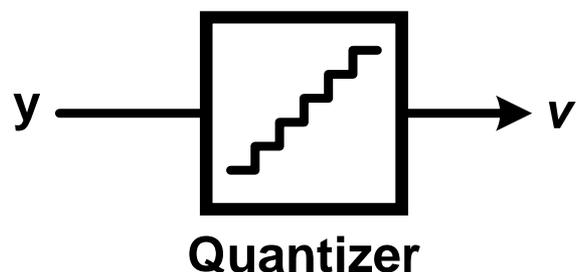
- ❑ Quantization error/noise: $e[n] = v[n] - y[n]$
- ❑ Least significant bit (LSB): Δ
- ❑ Quantizer has non-linear input-output characteristics
- ❑ Exact analytical modeling is difficult
 - Use a simplified model

Quantizer Modeling



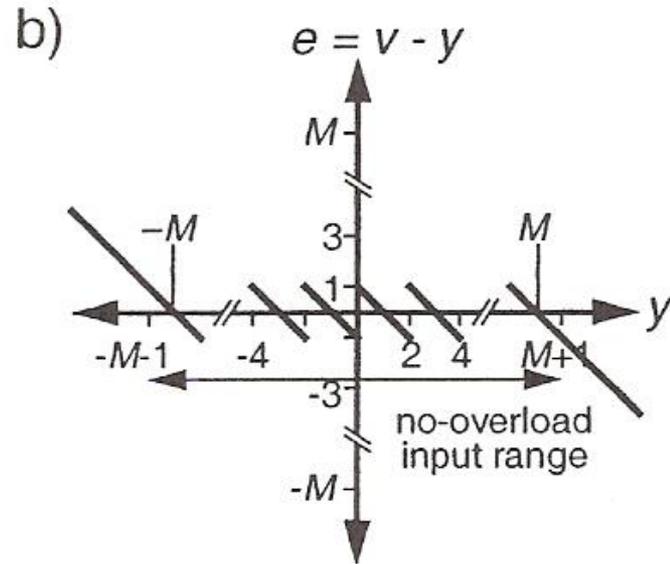
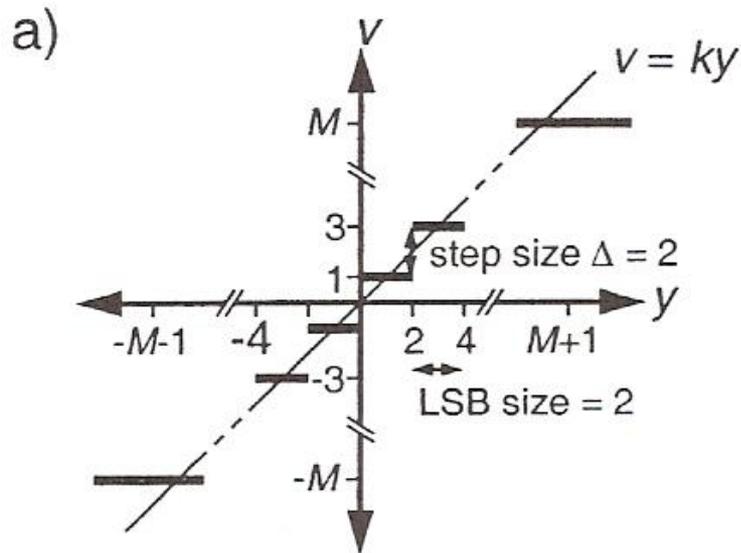
- Quantization noise modeling can be simplified with the assumptions
 - Input (y) stays within the no-overload input range
 - $e[n]$ is uncorrelated with the input y
 - Spectrum of $e[n]$ is white
 - Quantization noise is uniformly distributed
- Linearized quantizer model
 - AWUN noise
- Quantization noise power: $\sigma_e^2 = \frac{\Delta^2}{12}$
- $SQNR = 6.02 \cdot N + 1.76$

Quantizer Modeling



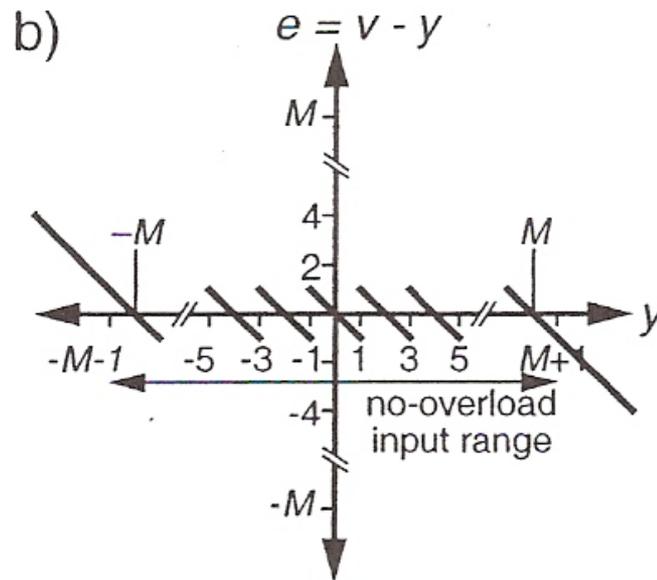
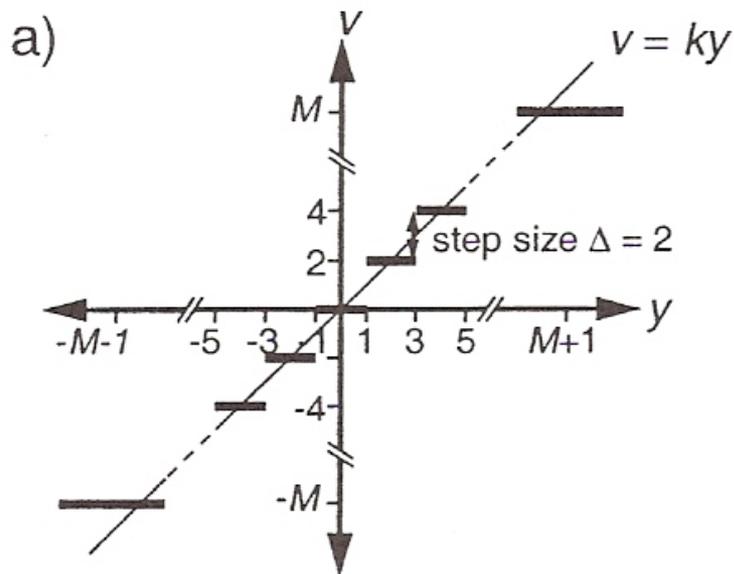
- ❑ Linear quantizer model holds true for large number of quantization levels and when the input is varying fast
- ❑ Linear quantizer model breaks down when
 - Input (y) is not varying fast
 - Input (y) is periodic with a frequency harmonically related to f_s
 - Quantizer overload
- ❑ With quantizer overload, the effective gain of the quantizer drops as the input amplitude increases

Mid-Rise Quantizer (even number of levels)



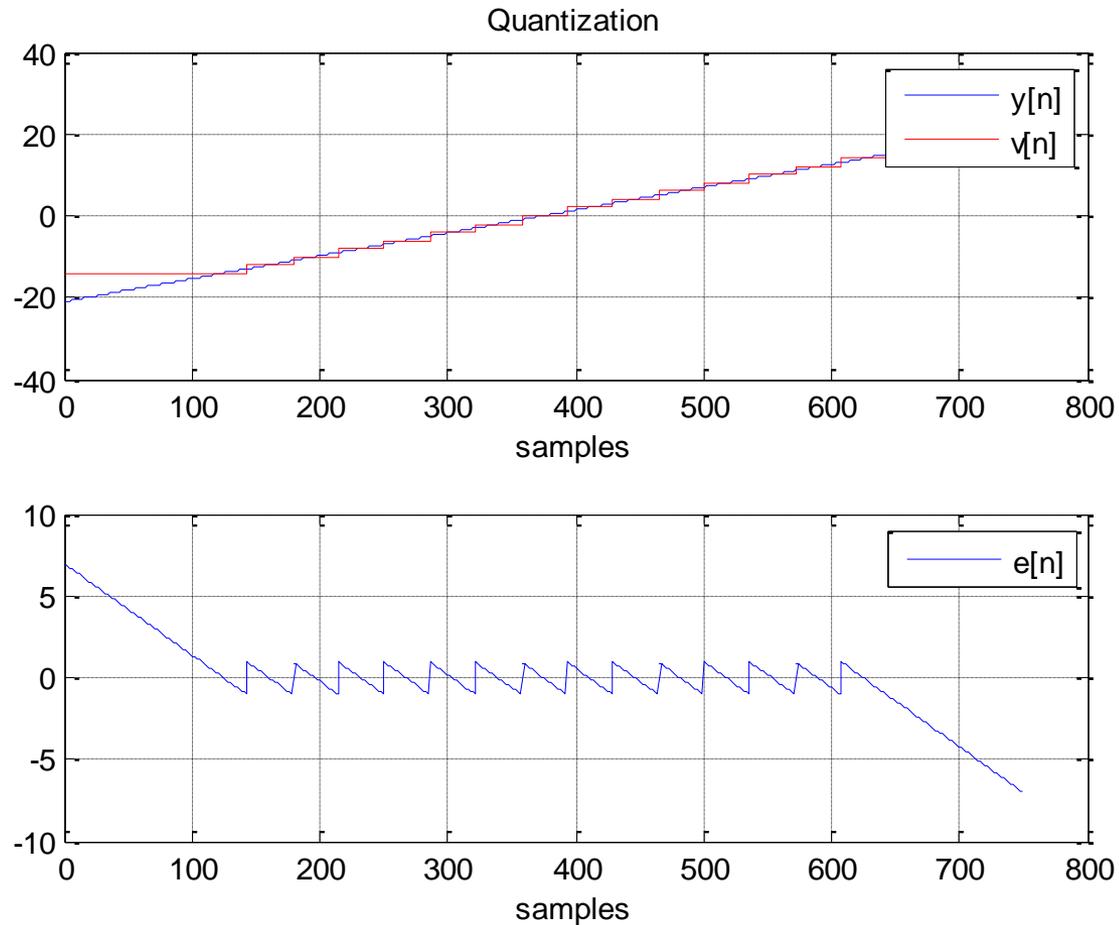
- ❑ Step rising at $y=0$ (mid-rise).
- ❑ In this figure (DSM toolbox model), $LSB = \Delta = 2$
- ❑ M = Number of steps, (M is odd here)
 - Number of levels ($nLev$) = $M+1$, (even)
- ❑ Input thresholds: $0, \pm 2, \dots, \pm(M-1)$.
- ❑ Output levels: $\pm 1, \pm 3, \dots, \pm M$.

Mid-Tread Quantizer (odd number of levels)

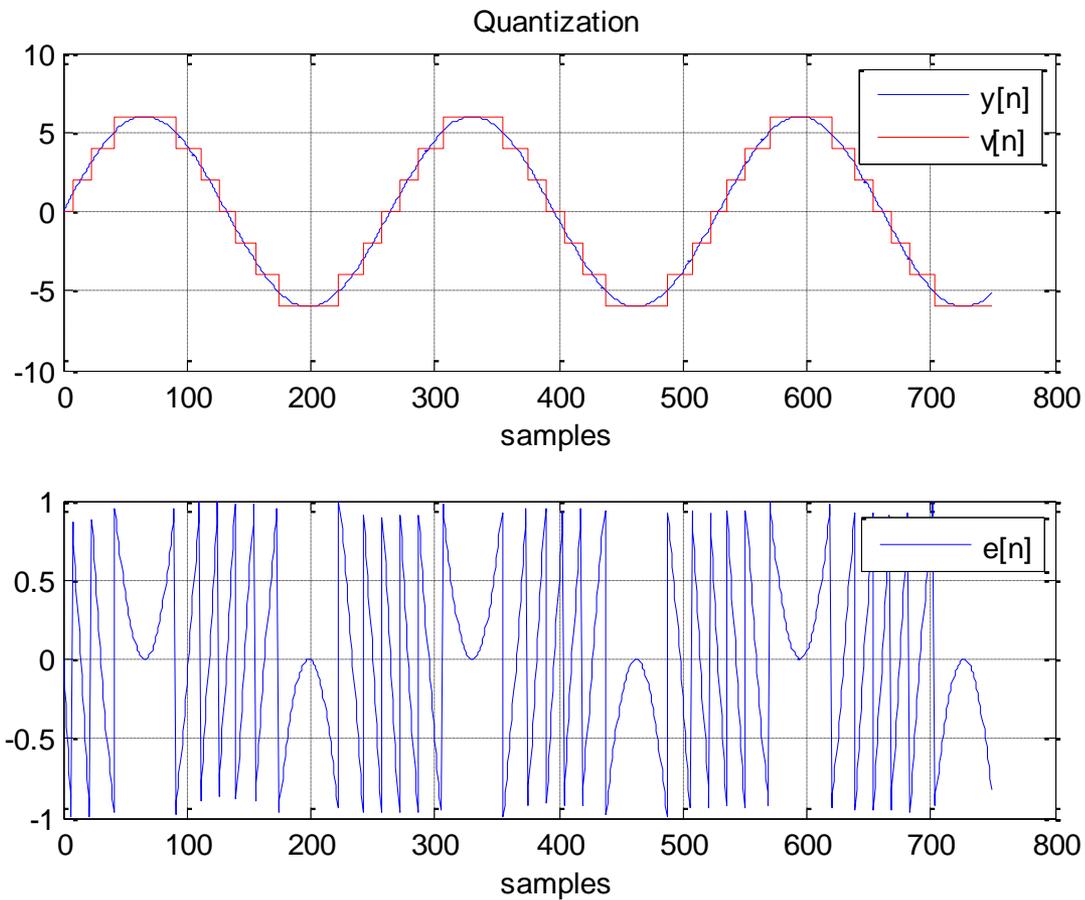


- ❑ Flat part of the step at $y=0$ (mid-tread).
- ❑ Here, $\text{LSB} = \Delta = 2$
- ❑ $M = \text{Number of steps}$, (M is even here)
 - $\text{Number of levels (nLev)} = M+1$, (odd)
- ❑ Input thresholds: $0, \pm 2, \dots, \pm(M-1)$.
- ❑ Output levels: $0, \pm 2, \pm 4, \dots, \pm M$.

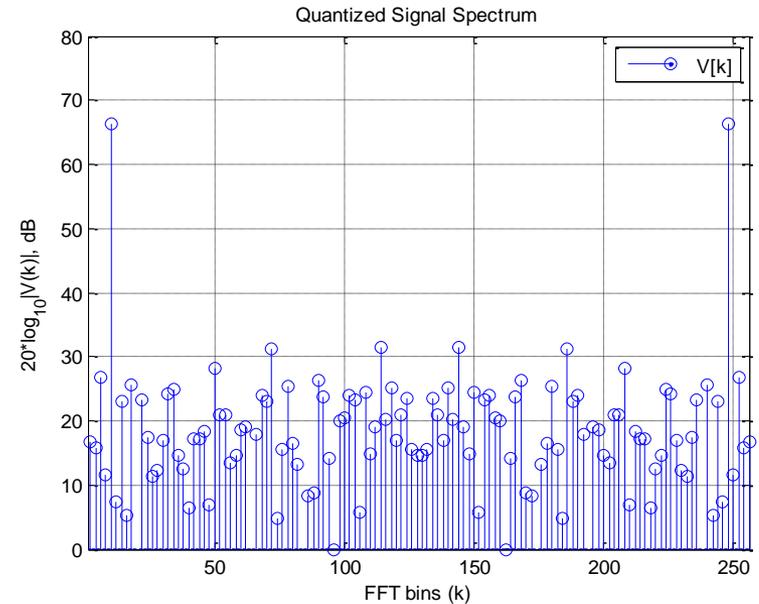
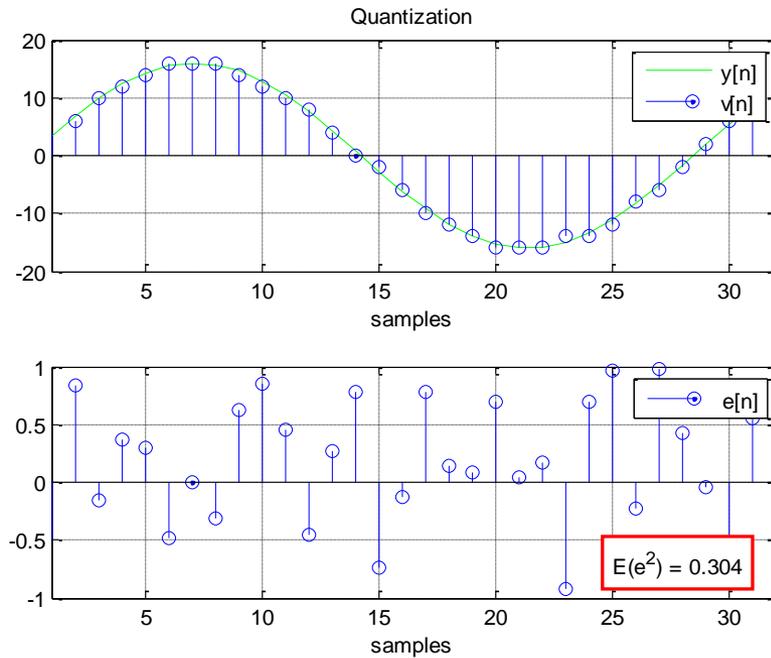
Quantizer Characteristics : Slow ramp input



Quantizer Characteristics : Sine input



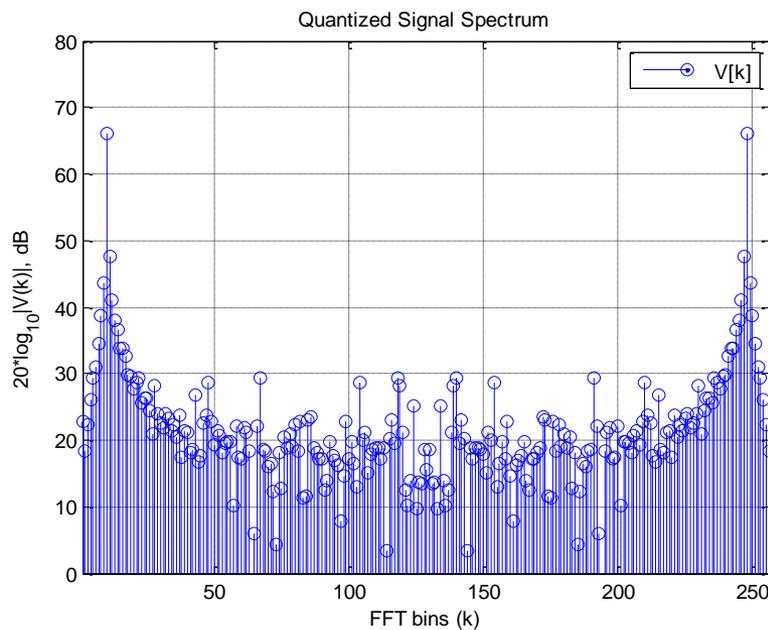
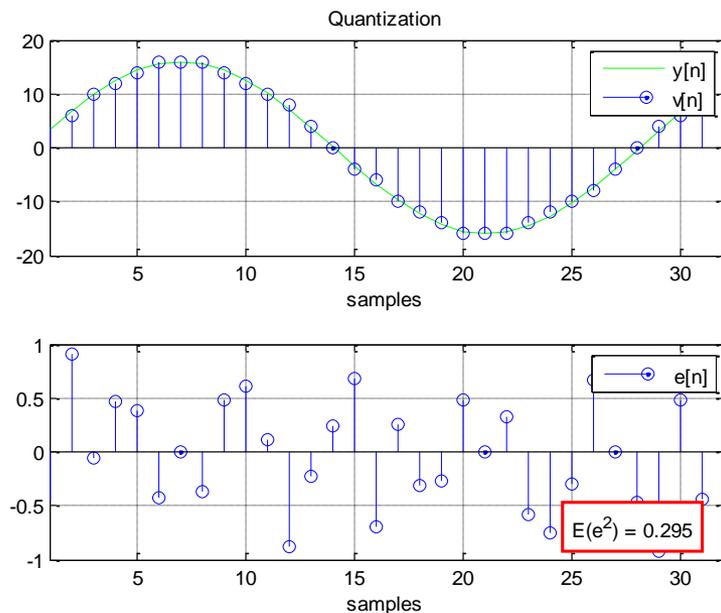
Quantization Noise : Example 1



$n_{\text{Lev}}=17, \Delta=2, f_{\text{in}}/f_{\text{s}} = 9/256 :$

• $E(e^2) = 0.304 \approx \Delta^2/12$

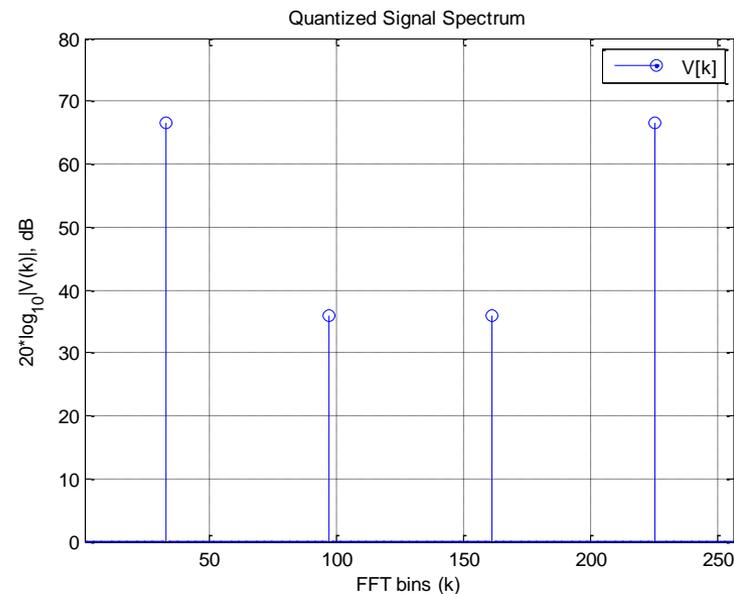
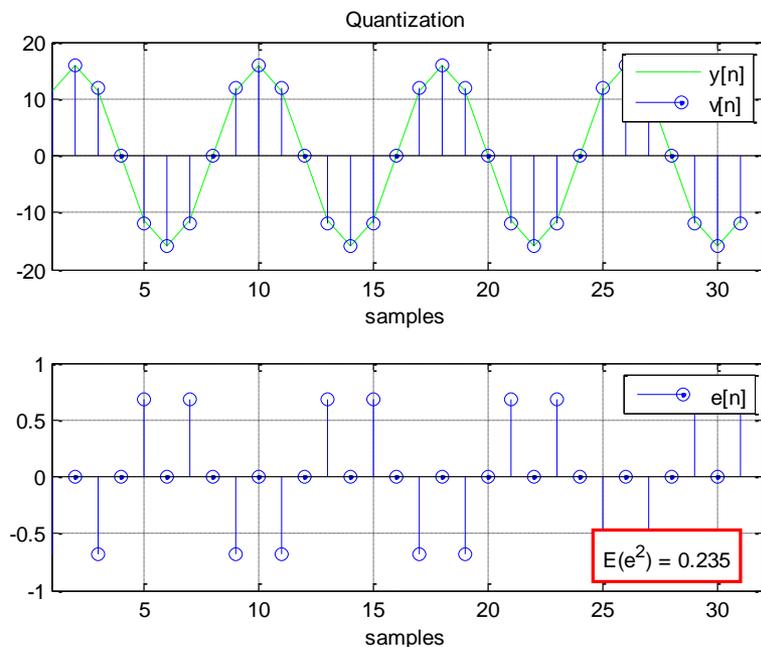
Quantization Noise : Example 1 contd.



$n_{\text{Lev}}=17, \Delta=2, f_{\text{in}}/f_s = 9.1/256 :$

- $E(e^2) = 0.295 \approx \Delta^2/12$
- Notice the FFT leakage.

Quantization Noise : Example 1 contd.



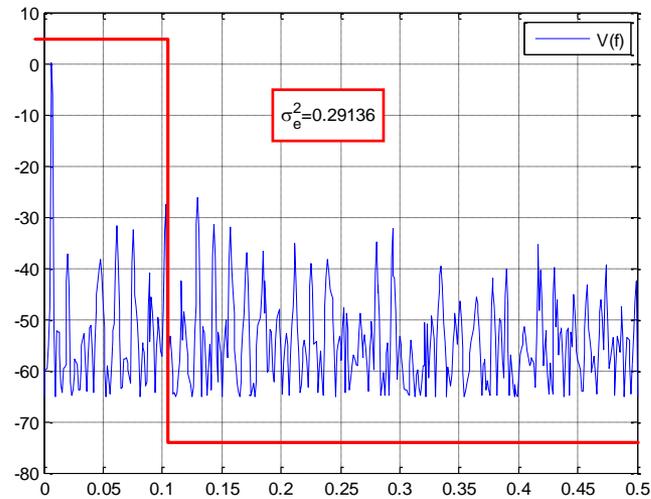
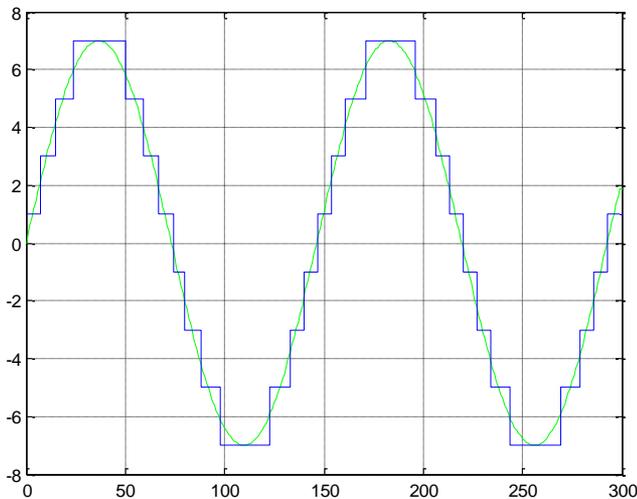
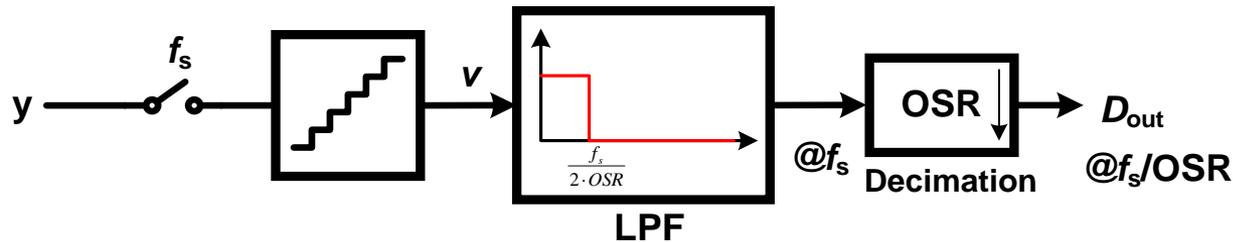
$n_{Lev}=17, \Delta=2, f_{in}/f_s = 32/256 = 1/8 :$

• $E(e^2) = 0.235 < \Delta^2/12$

• Quantization *noise* approximation not valid

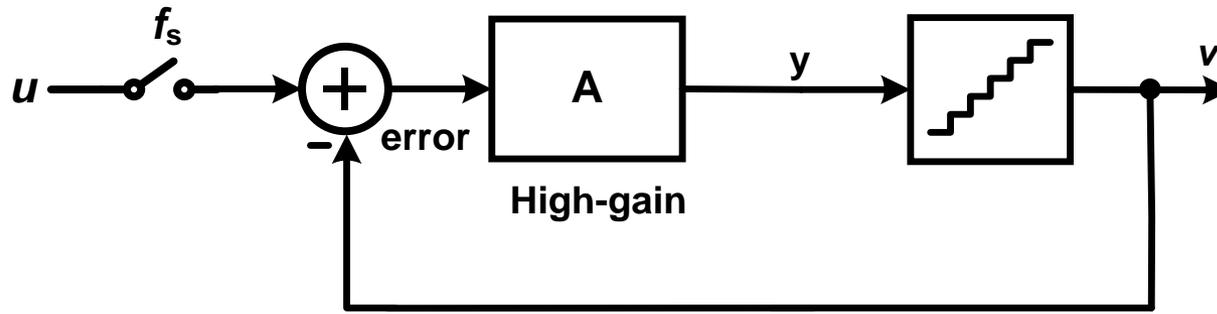
Oversampling and Noise-Shaping

Oversampling



- ❑ Oversampling ratio (OSR)
- ❑ Conversion bandwidth: $f_B = f_s / 2 \cdot OSR$
- ❑ $SQNR = 6.02 \cdot N + 1.76 + 10 \cdot \log_{10}(OSR)$
- ❑ 0.5 bits increase in resolution per doubling in OSR

Oversampling with Feedback

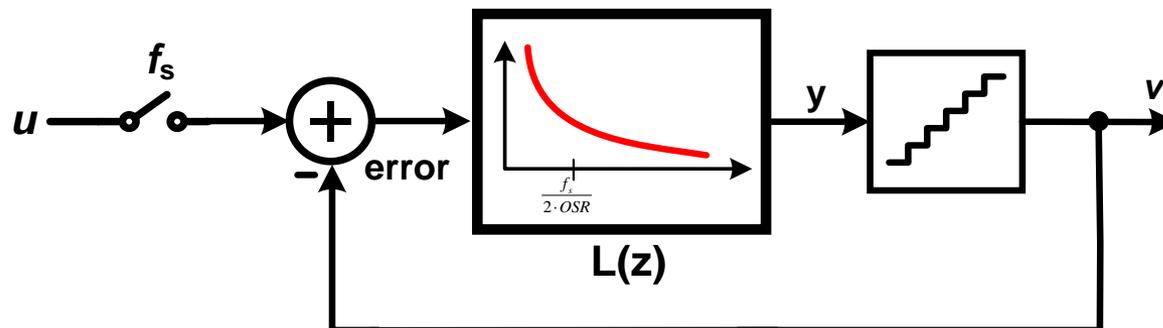


- ❑ Can we use feedback with high loop-gain ($A \cdot k_q$) to reduce the error $e = |u - v|$?
- ❑ Since quantizer output can not be equal to the input

$$A \cdot k_q \rightarrow \infty \Rightarrow e = |u - v| \rightarrow \infty$$

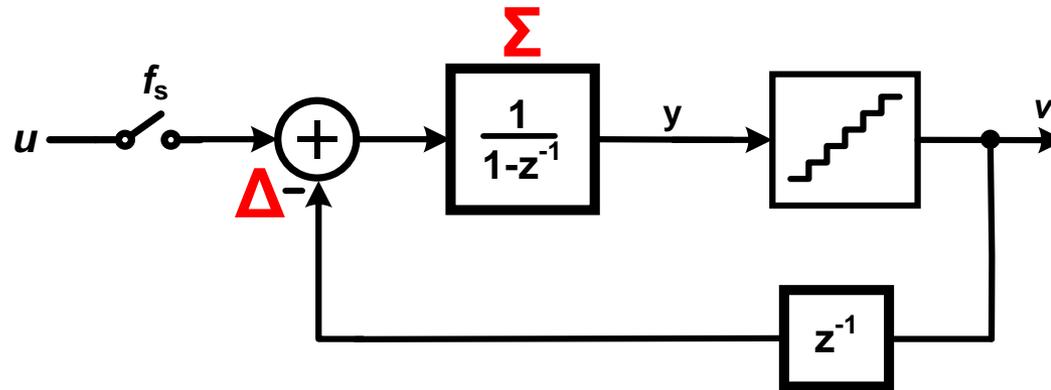
- ❑ The loop will be unstable as the error gets unbounded

Oversampling with Feedback



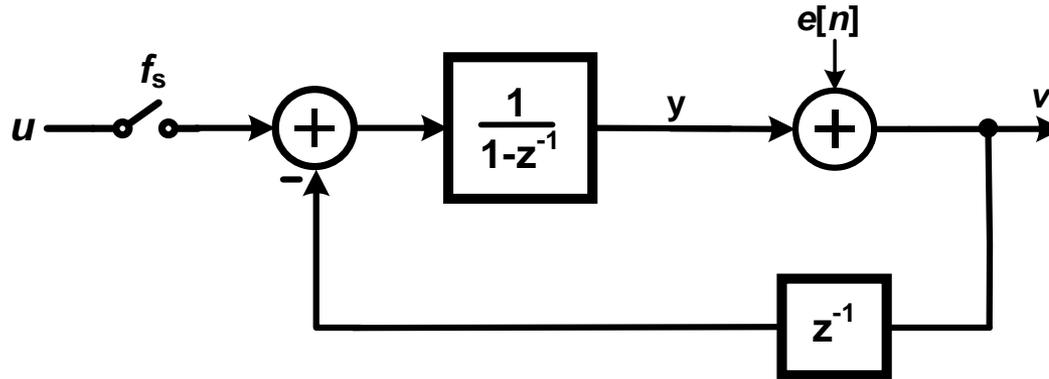
- ❑ Use large loop-gain in the signal band and small loop-gain at higher frequencies
- ❑ At low frequencies $e = |u - v| \rightarrow 0$
- ❑ At high frequencies, low loop-gain stabilizes the loop
- ❑ $L(z)$ is the loop-filter
- ❑ This feedback arrangement is called a (noise) **modulator**

First-order Modulator



- Differencing (Δ) followed by an accumulator (Σ)
 - $\Delta\Sigma$ modulator
- At low frequencies $e = |u - v| \rightarrow 0$

First-order Noise Shaping

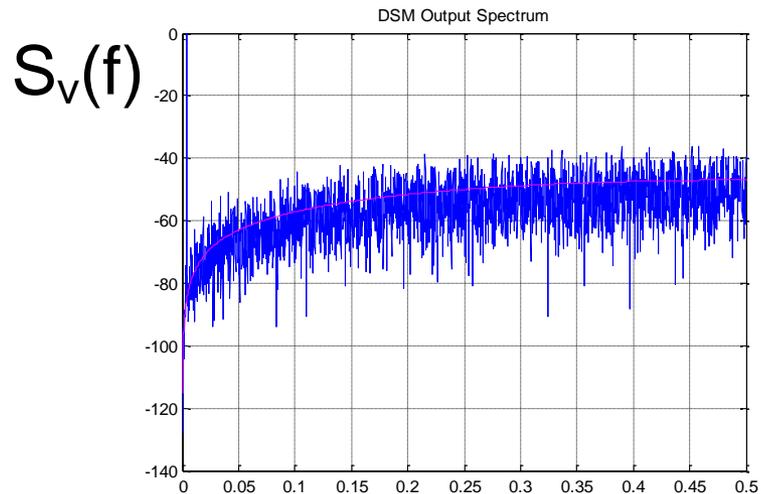
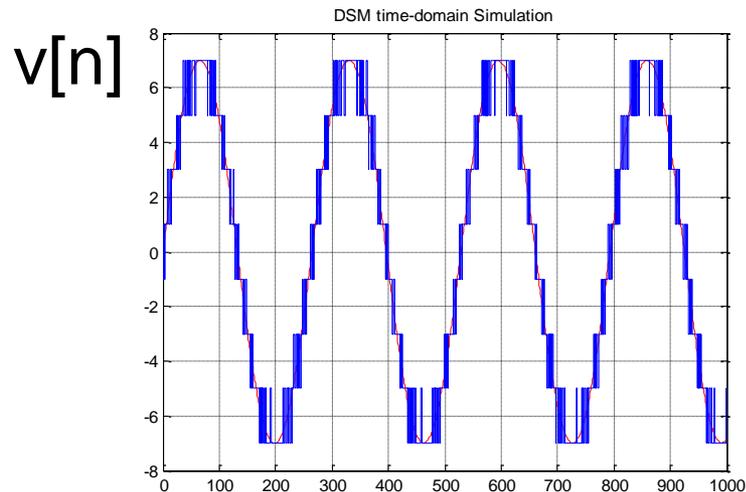
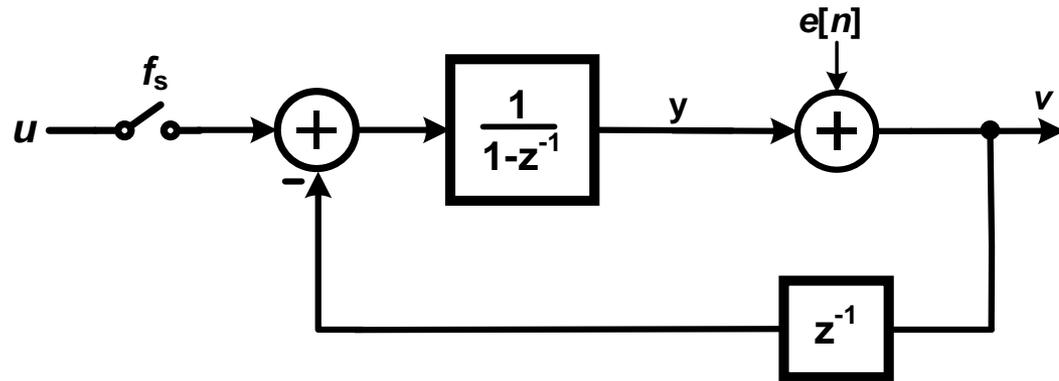


- Linearized model for the modulator

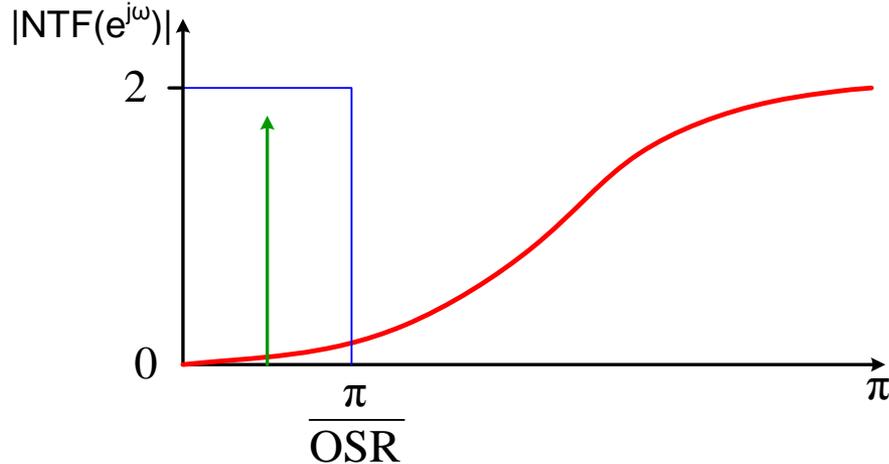
$$V(z) = \underbrace{z^{-1}}_{\text{STF}} U(z) + \underbrace{(1 - z^{-1})}_{\text{NTF}} E(z)$$

- Noise transfer function (NTF)
 - $(1-z^{-1})$: first-order differentiator
 - High-pass shaping of quantization noise
- Signal transfer function (STF)
 - Unit delay

First-order $\Delta\Sigma$ Modulator



First-order $\Delta\Sigma$ Modulator SQNR



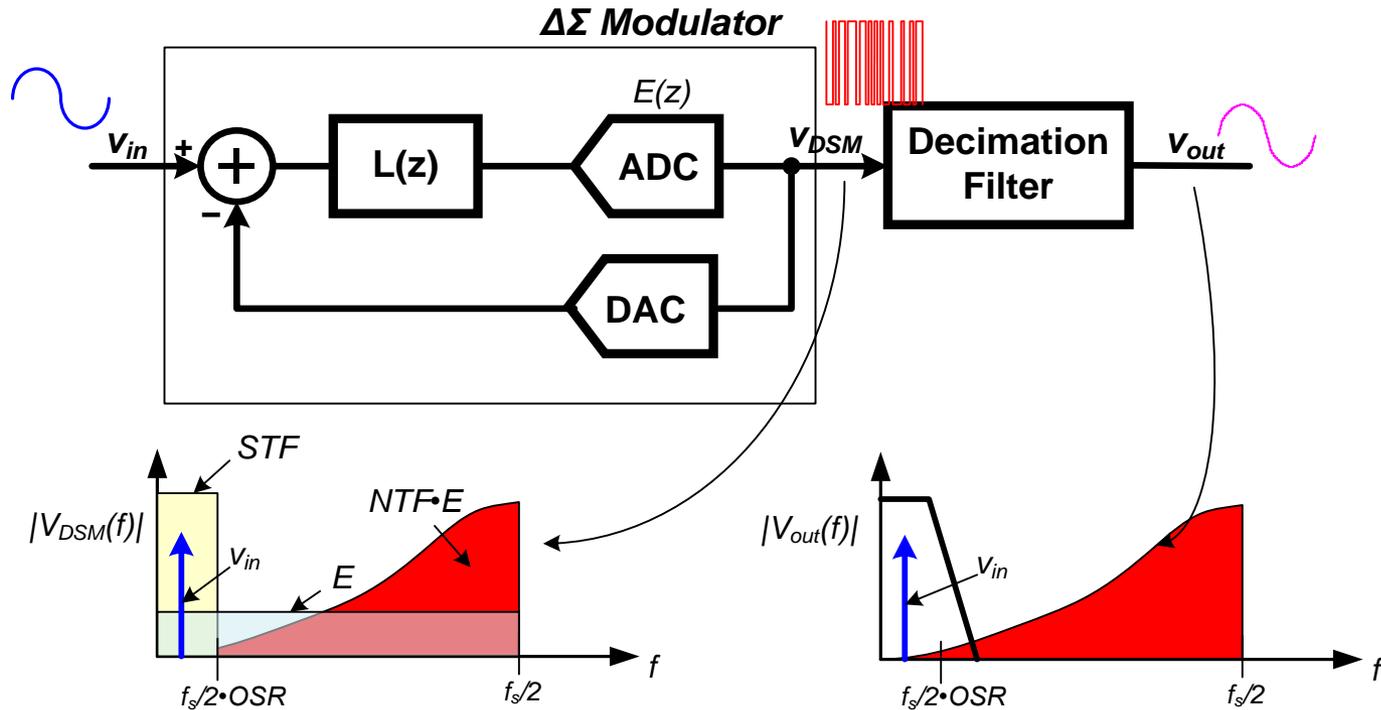
- In-band quantization noise (IBN)

$$\frac{\Delta^2}{12} \cdot \frac{\pi^2}{3} \cdot OSR^{-3}$$

- $SQNR = 6.02 \cdot N - 3.4 + 30 \cdot \log_{10}(OSR)$
- 1.5 bits increase in resolution per doubling in OSR
- Out-of-band noise is filtered out using a digital filter

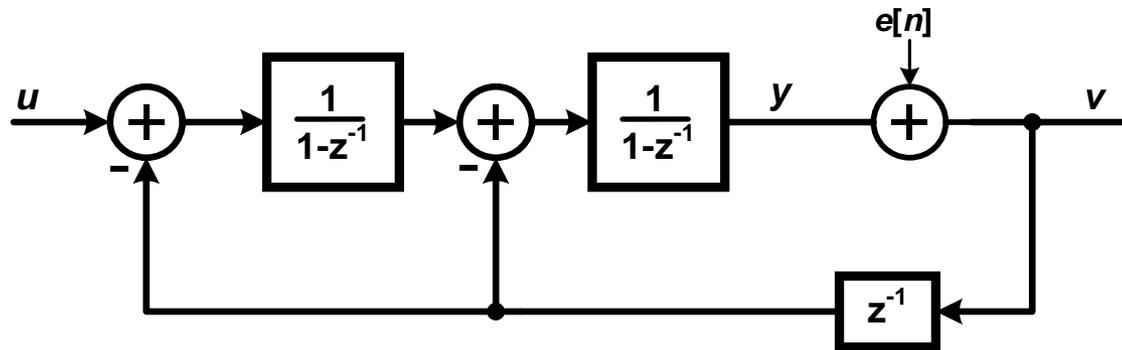
$$\begin{aligned}
 IBN &= \int_0^{\frac{\pi}{OSR}} S_v(e^{j\omega}) \cdot d\omega \\
 &= \frac{\Delta^2}{12} \int_0^{\frac{\pi}{OSR}} |NTF(e^{j\omega})| \cdot d\omega \\
 &= \frac{\Delta^2}{12} \int_0^{\frac{\pi}{OSR}} 4 \sin^2(\omega/2) \cdot d\omega \\
 &\approx \frac{\Delta^2}{12} \int_0^{\frac{\pi}{OSR}} \omega^2 \cdot d\omega \\
 &= \frac{\Delta^2}{12} \cdot \frac{\omega^3}{3} \Bigg|_0^{\frac{\pi}{OSR}} \\
 &= \frac{\Delta^2 \pi^2}{36 \cdot OSR^3}
 \end{aligned}$$

Delta-Sigma ($\Delta\Sigma$) ADC



- ❑ Use oversampling ($f_s = 2 \cdot OSR \cdot BW$) to shape the quantization noise out of the signal band
- ❑ Use low-resolution ADC and DAC to higher much higher resolution
- ❑ Digitally filter out the out-of band shaped (modulated) noise
- ❑ Trades-off SQNR with oversampling ratio (OSR)

Second-Order Noise Shaping

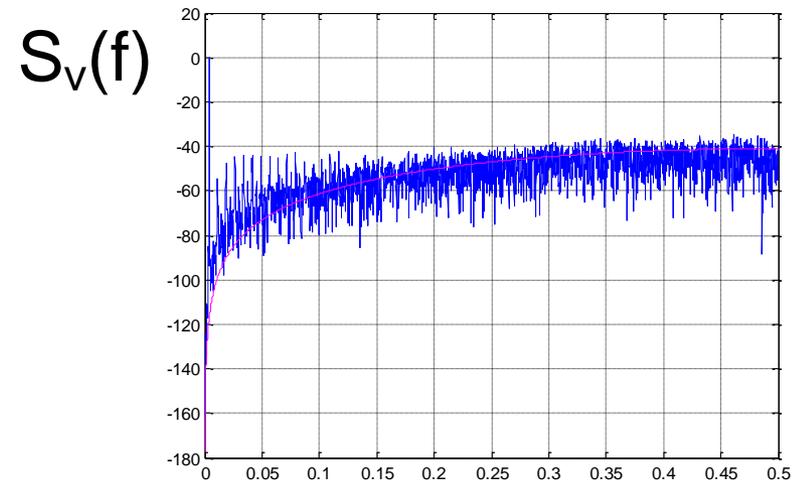
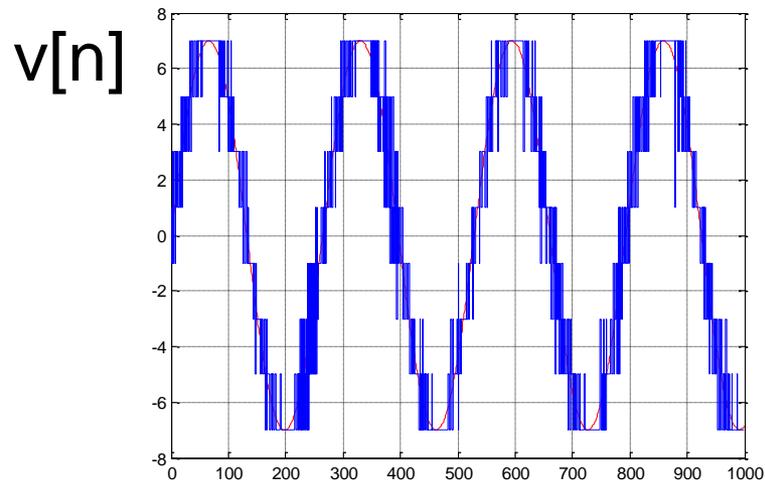
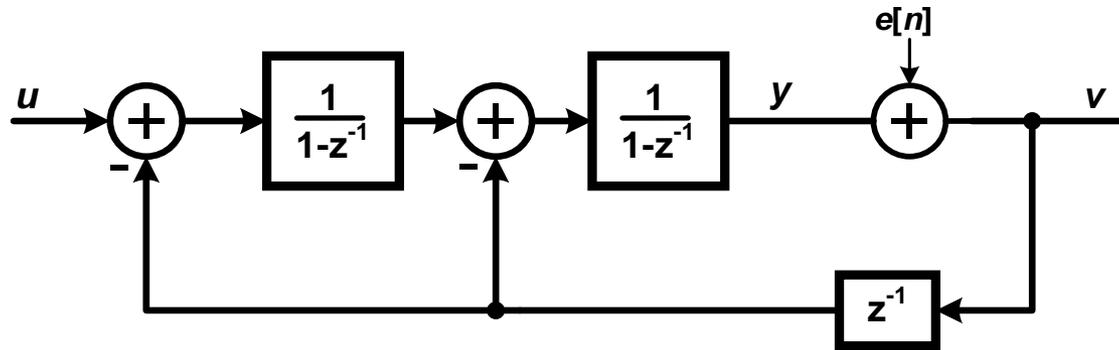


- Linearized model for the modulator

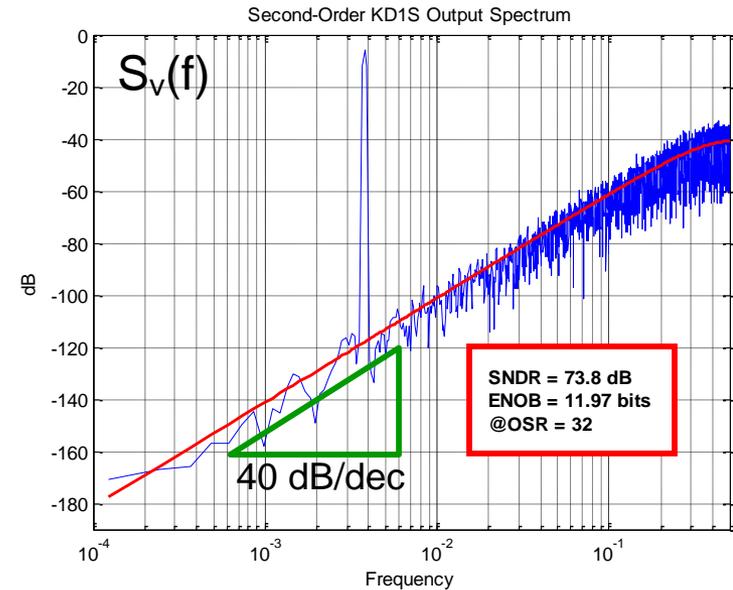
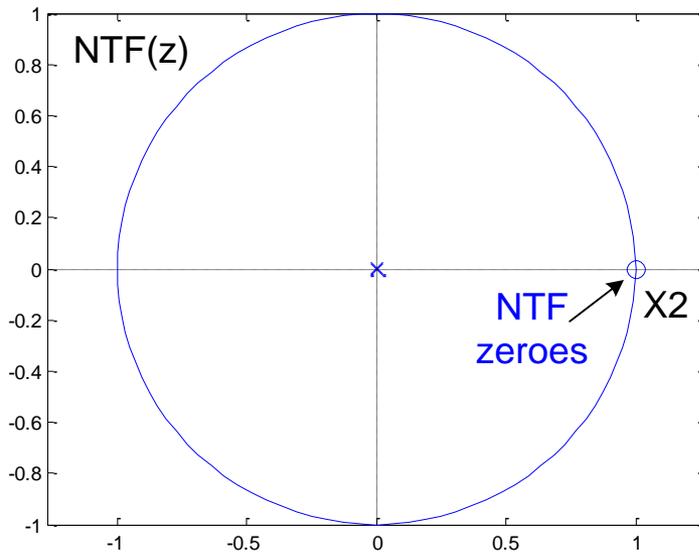
$$V(z) = \underbrace{z^{-1}}_{\text{STF}} U(z) + \underbrace{\left(1 - z^{-1}\right)^2}_{\text{NTF}} E(z)$$

- Second-order noise-shaping
- In-band quantization noise (IBN): $\frac{\Delta^2}{12} \cdot \frac{\pi^4}{5} \cdot \text{OSR}^{-5}$
 - 2.5 bits increase in resolution per doubling in OSR
- Can be extended to higher orders

Second-order $\Delta\Sigma$ Modulator



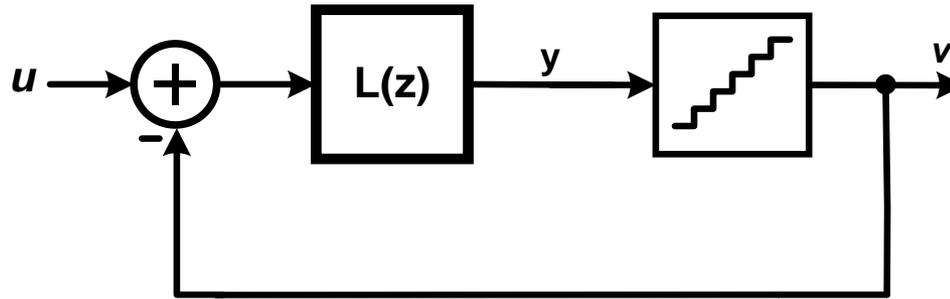
Second-order $\Delta\Sigma$ Modulator



- $NTF(z) = (1-z^{-1})^2$
 - Two zeroes at DC
 - Out-of-Band Gain (OBG) (i.e gain at $\omega \approx \pi$) = 4

Higher-Order $\Delta\Sigma$ Modulators

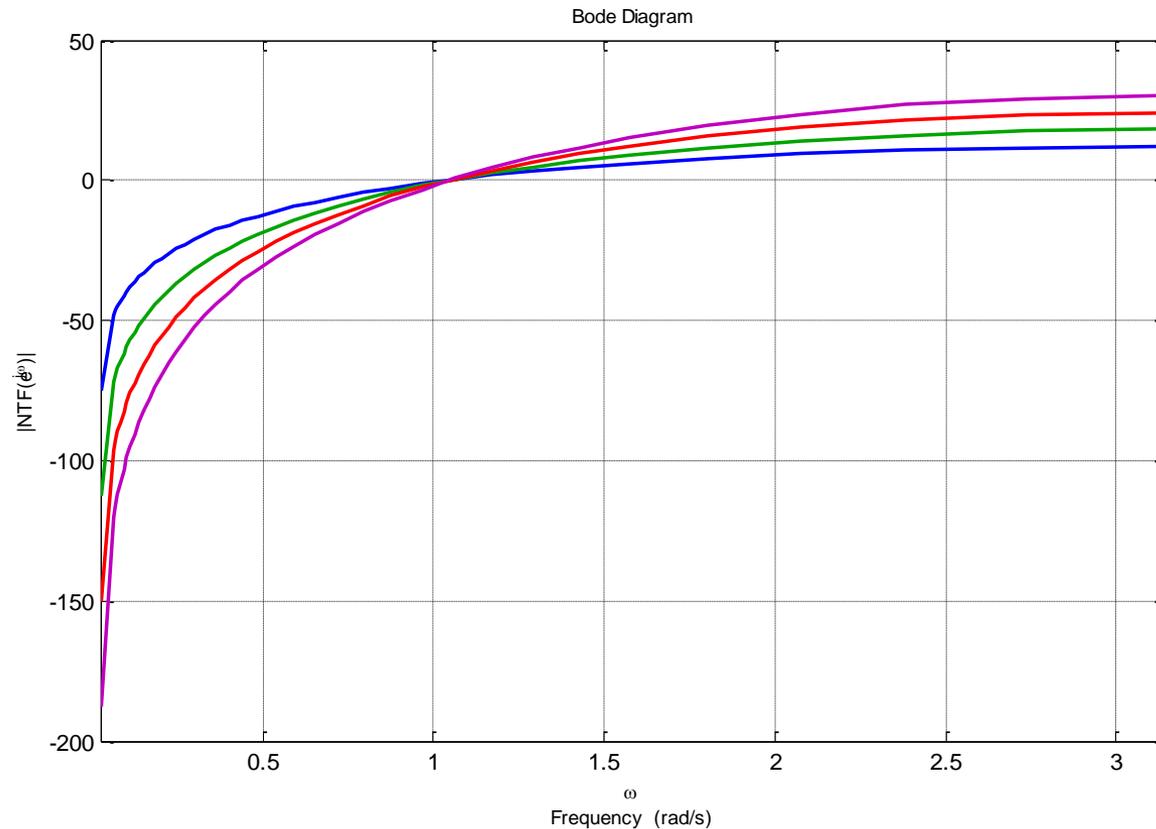
Higher-Order NTFs



$$V(z) = \underbrace{\frac{L(z)}{1+L(z)}}_{\text{STF}} U(z) + \underbrace{\frac{1}{1+L(z)}}_{\text{NTF}} E(z)$$

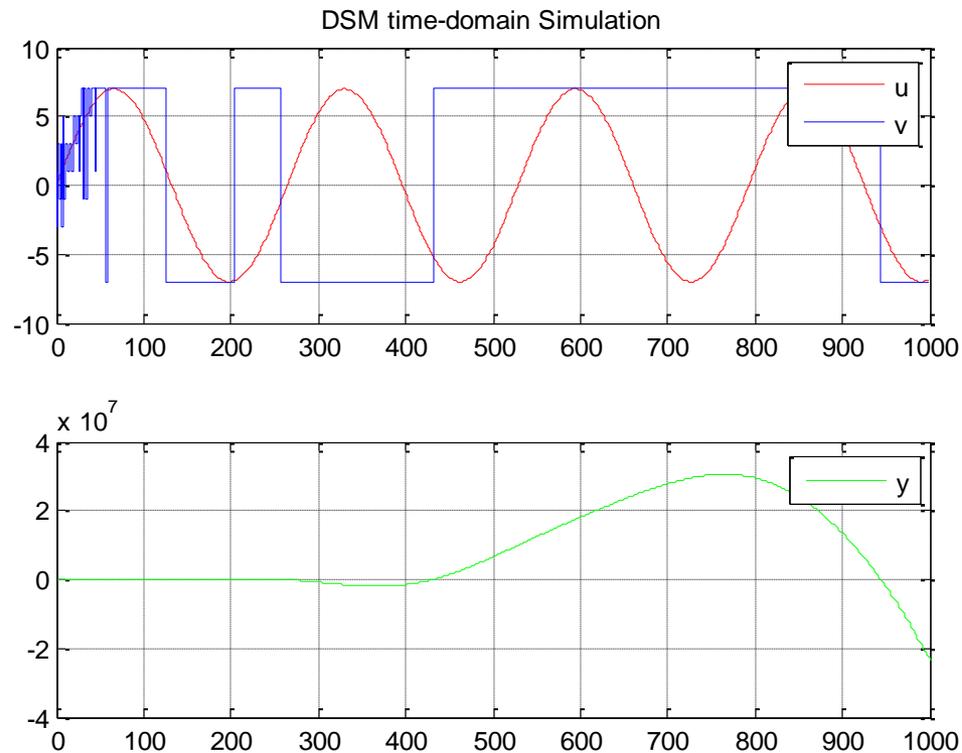
- Higher order noise shaping
 - Reduced in-band noise, higher SQNR
- For $NTF=(1-z^{-1})^{-N}$, in-band noise (IBN): $\frac{\Delta^2}{12} \cdot \frac{\pi^{2N}}{(2N+1)} \cdot OSR^{-(2N+1)}$
 - Ideally $(N+1/2)$ bits increase in resolution per doubling in OSR

Higher-Order NTFs



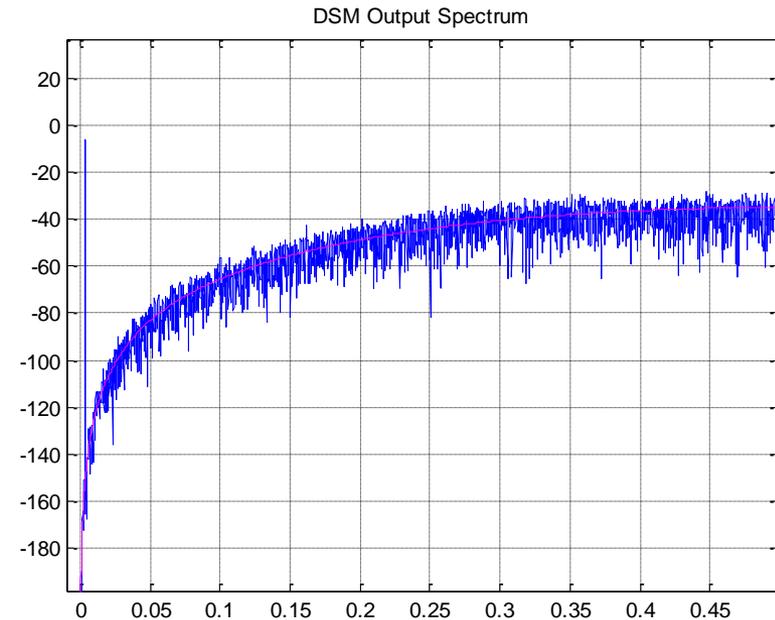
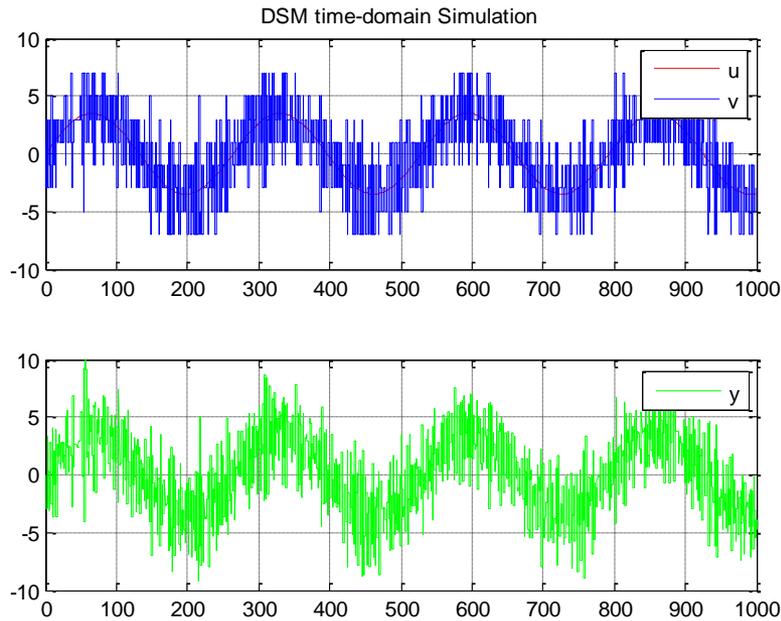
- NTF gain increases at high frequencies (around $\omega \approx \pi$)
- Can we go on increasing the order?

Third-order $\Delta\Sigma$ Modulator Example



- ❑ $NTF(z) = (1-z^{-1})^3$
- ❑ OBG = 8, Full-scale input.
- ❑ Unstable after few samples (look at quantize input (y) blowing up!).
 - Signature for $\Delta\Sigma$ instability
 - Worst case for a single-bit quantizer.

Third-order $\Delta\Sigma$ Modulator Example



- ❑ Stable for 50% of full-Scale amplitude
- ❑ Signal dependent stability
 - Need to develop intuition for modulator stability
 - Reference: Stability theory from the Yellow Bible of delta-sigma

Systematic NTF Design

- ❑ NTFs of the form $(1-z^{-1})^N$ have stability issues
 - The OBG (2^N) are too high
- ❑ A larger OBG causes more wiggling at the quantizer input
 - This saturates the quantizer for even smaller inputs
 - Irrecoverable quantizer saturation causes loop instability
- ❑ For high-OBG the maximum stable (input) amplitude (MSA) is small
- ❑ The stability is worse for low quantizer resolutions
- ❑ Thus we need to reduce OBG while maintaining high in-band noise shaping

Systematic NTF Design Procedure

- Introduce poles into the NTF

- $$NTF(z) = \frac{(1 - z^{-1})^N}{D(z)}$$

- NTF realizability criterion

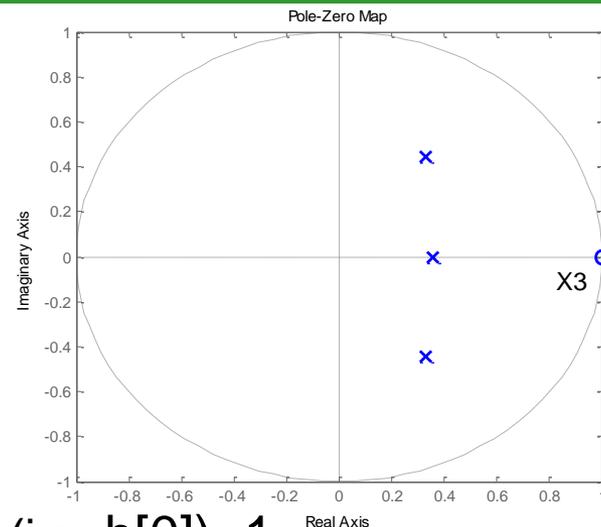
- No delay-free loops in the modulator

- First sample of the NTF impulse response (i.e. $h[0]$)=1

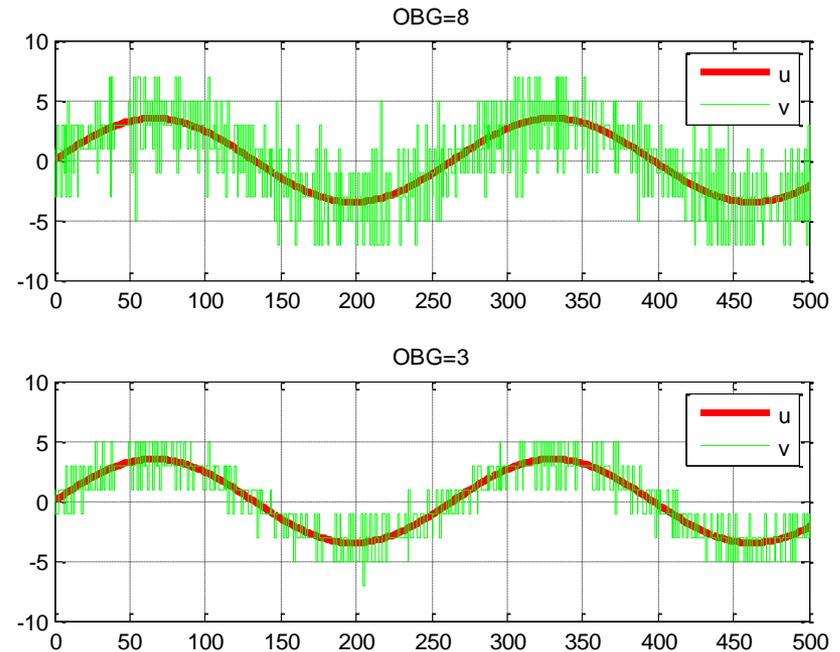
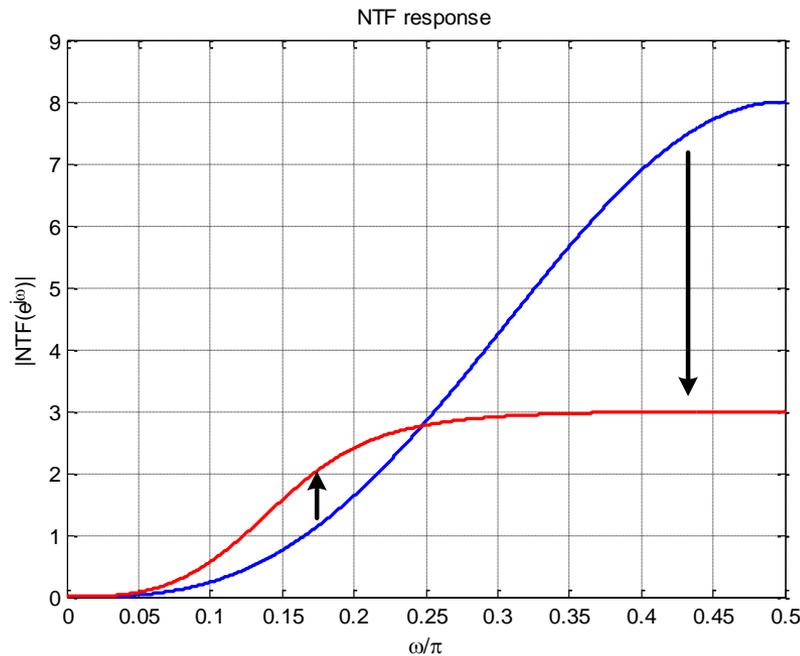
$\Rightarrow NTF(\infty)=1$

$\Rightarrow D(z=\infty)=1$

- Commonly used pole positions: Butterworth, Inverse Chebyshev and maximally flat poles (maxflat)



NTF Response with Poles

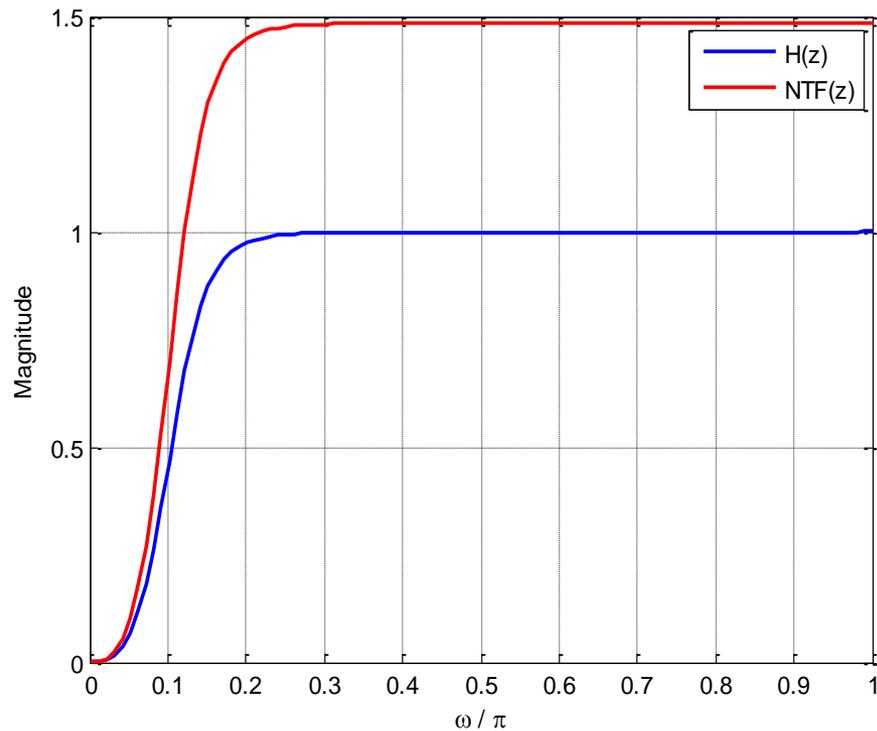


- Select appropriate OBG for the NTF to assure stability
- Trade-off between stability and increased in-band noise
 - MSA vs SQNR for a given order and quantizer resolution

Systematic NTF Design Example

- ❑ Specifications
 - SQNR > 120 dB
 - A signal bandwidth which results in an OSR = 64
 - Study optimal clock rate for the given process and quantizer design.
- ❑ Designer's Choice
 - Order = 3
 - Quantizer levels (nLev) = 16
 - Butterworth high-pass response for the NTF
- ❑ Use MATLAB for finding coefficients of the HPF response.
 - $[b,a] = \text{butter}(\text{order}, \omega_{3\text{dB}}, \text{'high'})$
 - The cutoff frequency $\omega_{3\text{dB}}$ specifies the transfer function.

Systematic NTF Design contd.

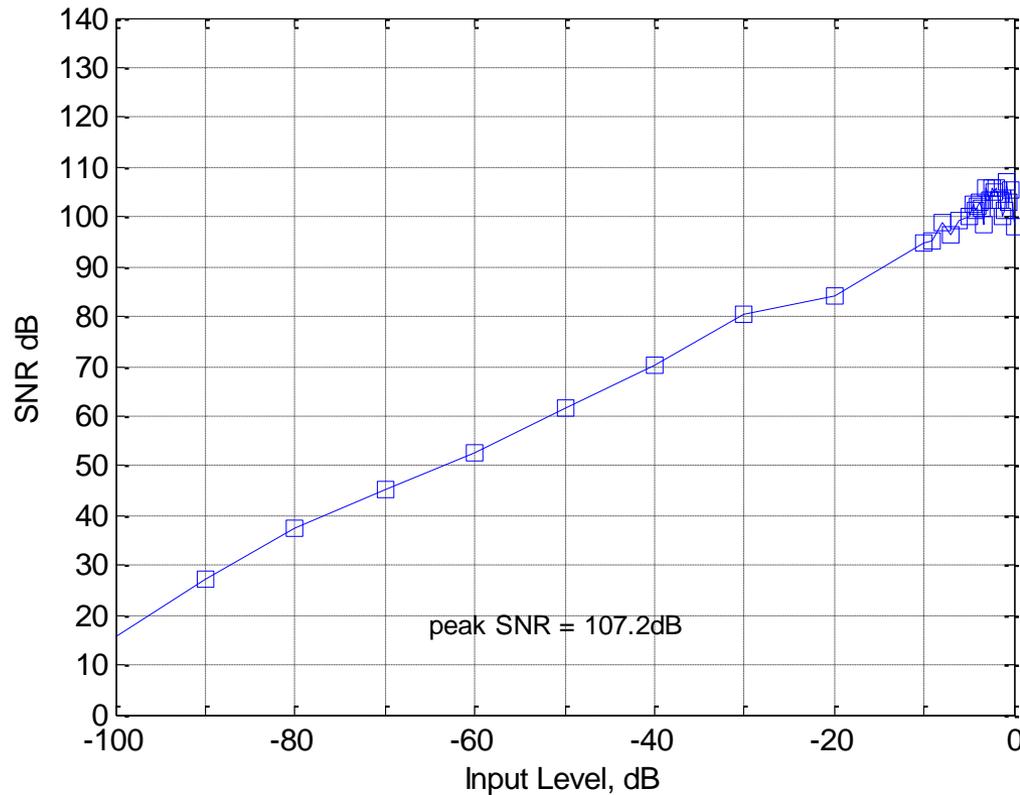


- Start with cutoff frequency $\omega_{3dB} = \pi/8$, for the butterworth HPF $H(z)$.
- Derive a realizable NTF using $NTF(z) = H(z)/b_0$

Systematic NTF Design contd.

- ❑ Map the NTF response to a loop-filter architecture (details later)
- ❑ Simulate the modulator for all possible amplitudes and input tone frequencies.
- ❑ Compute the peak SQNR and MSA.
 - `simulateDSM` function in the toolbox.
 - Can use Risbo's method shown later

Systematic NTF Design contd.

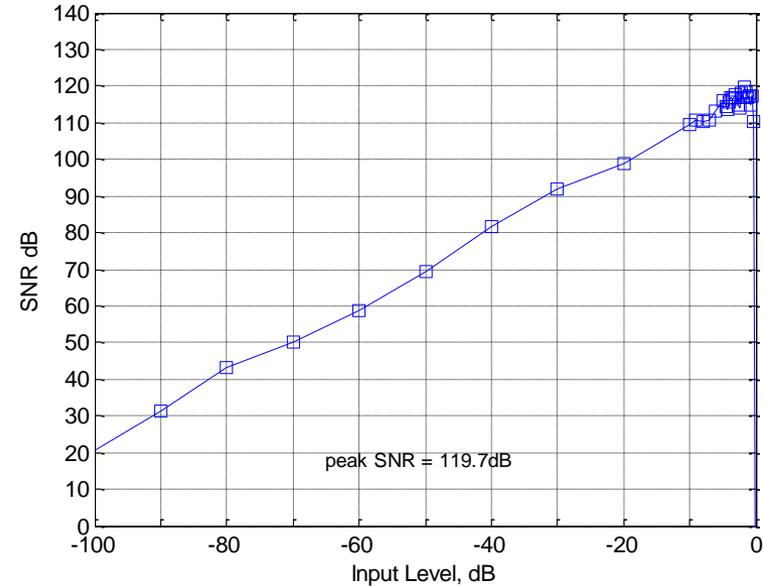
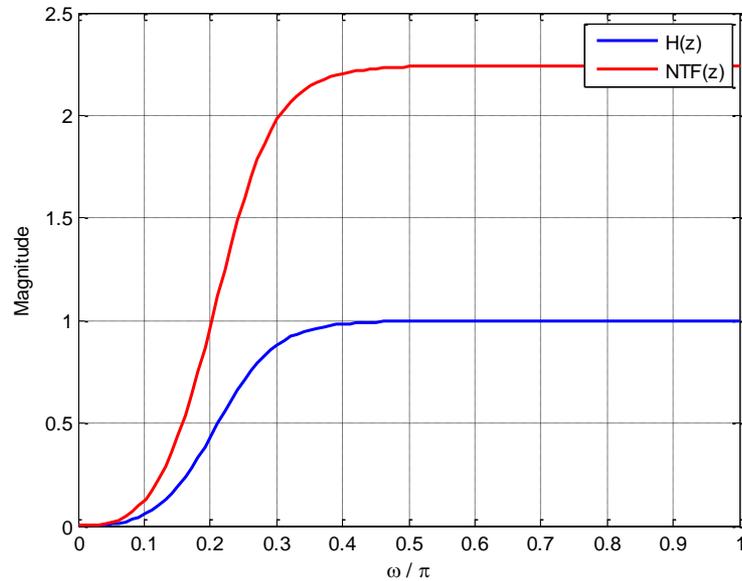


- Peak SNR = 107 dB
- MSA = 0.9

Systematic NTF Design contd.

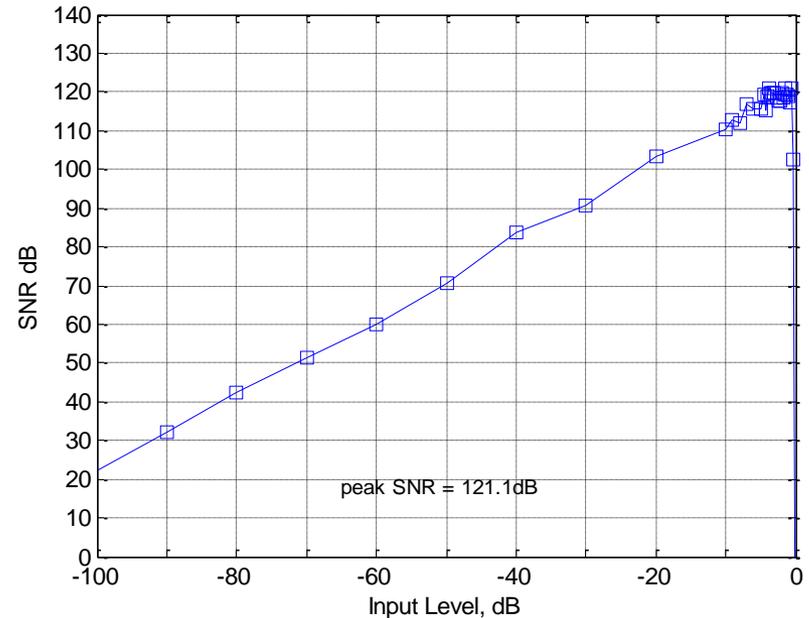
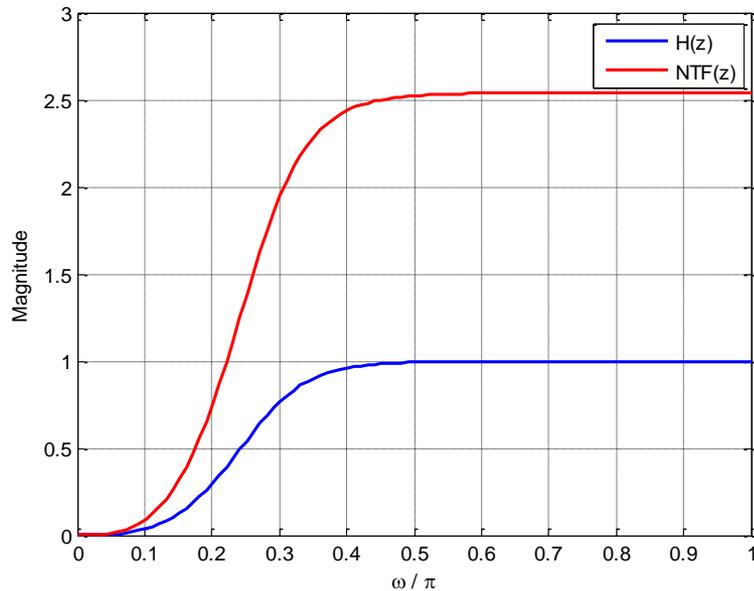
- If SNR is not enough, repeat the entire procedure with a higher cutoff frequency for the Butterworth HPF
 - IBN ↓, SQNR ↑
 - OBG ↑ and MSA ↓
- If SNR is too high, repeat the entire procedure with a lower cutoff frequency for the Butterworth HPF
 - IBN ↑, SQNR ↓
 - OBG ↓ and MSA ↑

Systematic NTF Design contd.



- $\omega_{3dB} = \pi/4$.
- Peak SNR = 119 dB, OBG = 2.25, MSA = 0.8

Systematic NTF Design contd.



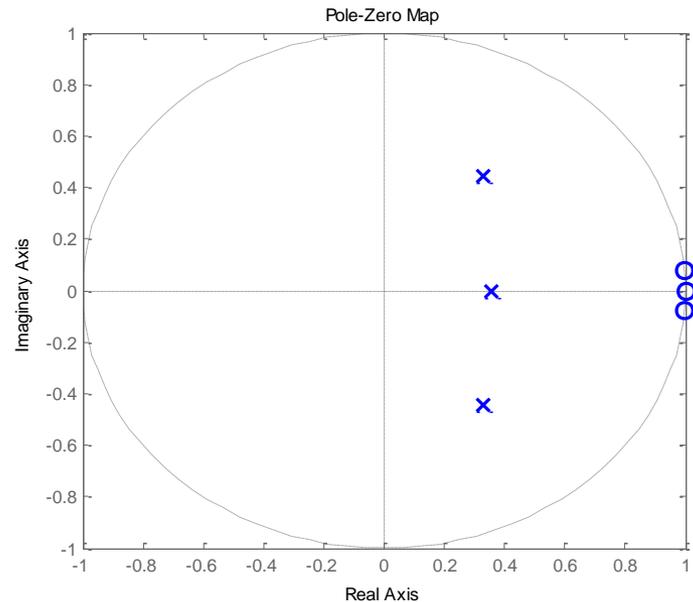
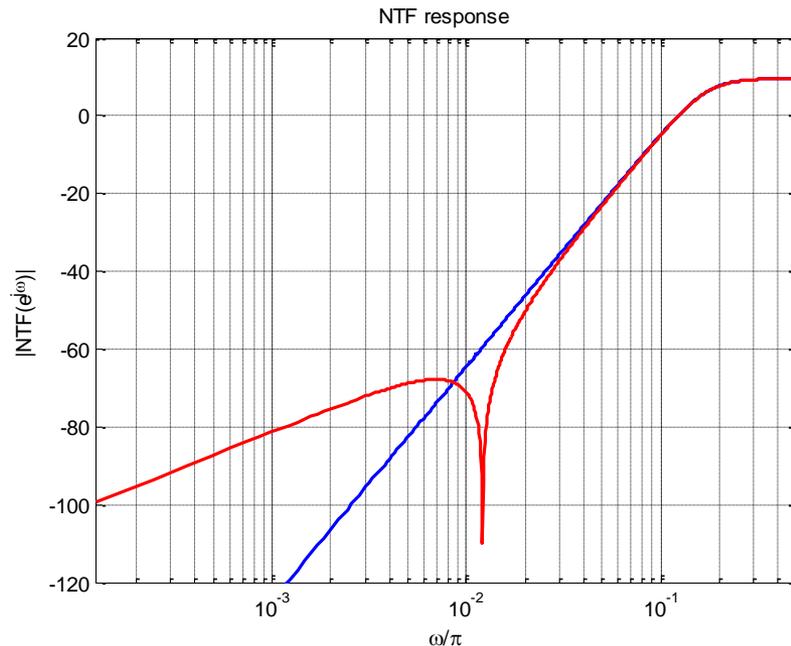
- $\omega_{3dB} = 2\pi/7$.
- Peak SNR = 121 dB, OBG = 2.54, MSA = 0.8.
 - Design closed !

Systematic NTF Design contd.

- An advanced version of this iterative process is implemented as the function `synthesizeNTF` in the delta-sigma Toolbox.
 - Several 'opt' params for NTF zero (and pole) optimization
 - Use `synthesizeChebyshevNTF` for low OSR and low OBG designs.

NTF-Zero Optimization

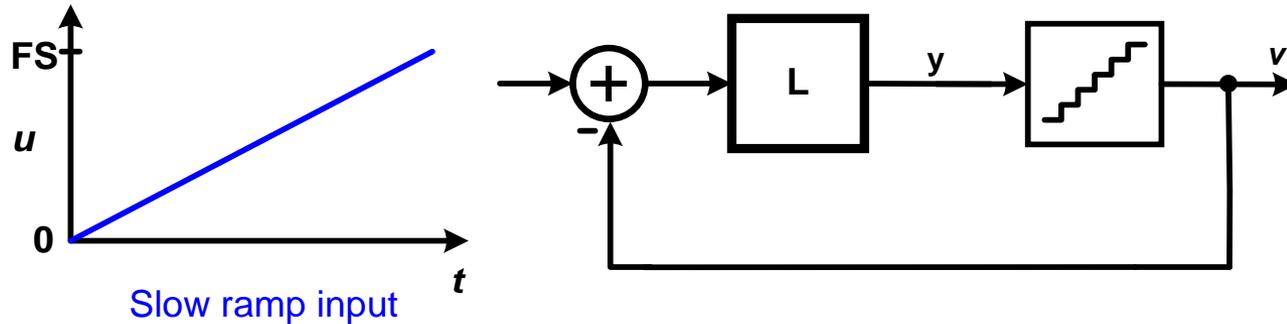
- ❑ Spread zeros in the signal band to minimize in-band noise
 - Complex zeros on the unit circle
 - 8dB increase in SQNR for 3rd order modulator
- ❑ Bandwidth normalized NTF-zero locations obtained by toolbox function `ds_optzeros(order, 1)`
- ❑ Already implemented in `synthesizeNTF` function for `opt=1`



Estimating MSA (Maximum Stable Amplitude)

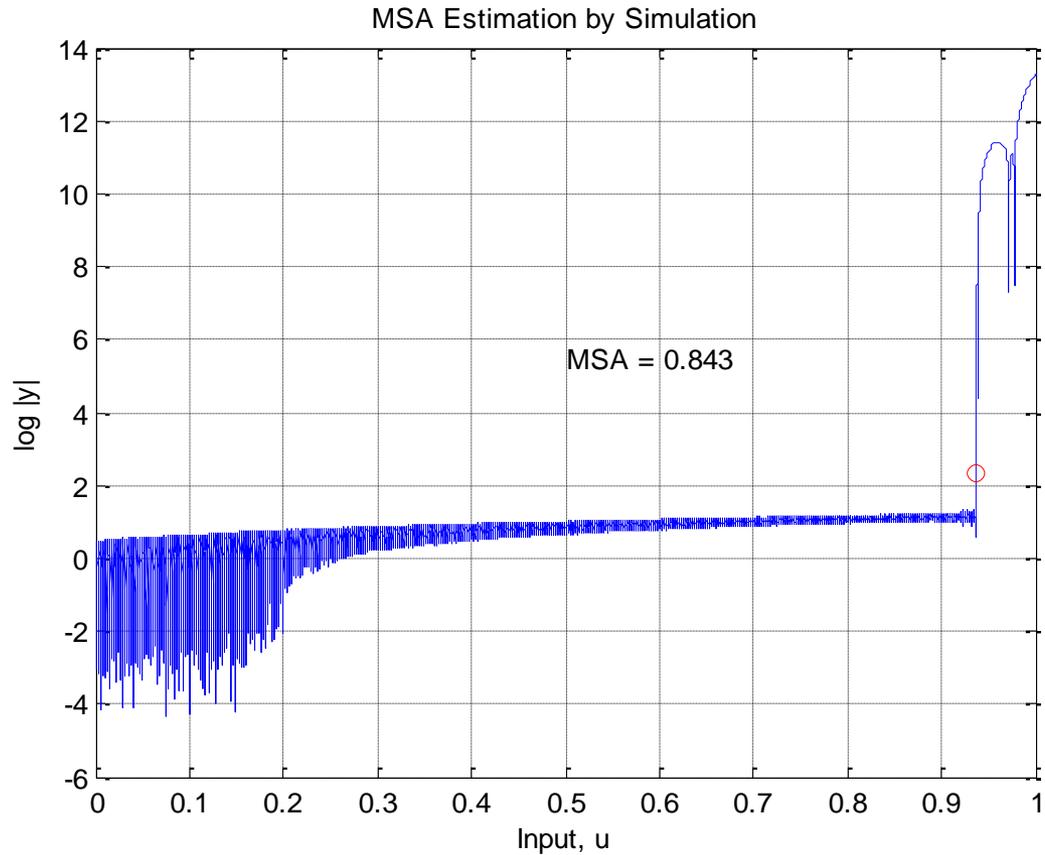
- ❑ MSA is found through extensive simulation
- ❑ Simulate for input sinusoids of varying amplitudes for all possible signal frequencies in the signal band.
 - For every input amplitude compute in-band SNR.
 - Beyond the MSA, the NTF poles move out of the unit circle.
 - Noise shaping is disrupted and the in-band SNR drops.
 - At this point the quantizer input ($y[n]$) blows up.
- ❑ `simulateSNR` function in the toolbox does exactly the same
- ❑ Time consuming and often impractical for iterative design

Estimating MSA using Risbo's Method



- Use a slow ramp input from 0 to FS value.
 - Plot $\log_{10}|y[n]|$. Observe where this plot blows up.
 - Take 90% of the input amplitude where $\log_{10}|y[n]|$ blows up as a conservative estimate for MSA.
 - Estimated MSA is close to that predicted by the sinewave input method.
- Much quicker than the sinewave technique (simulateSNR function)

Estimating MSA using Risbo's Method



Loop-Filter Architectures

- Several loop-filter discrete-time architectures possible
- Toolbox function `realizeNTF` maps the synthesized NTF to loop-filter co-efficients

```
[a,g,b,c] = realizeNTF(H, form);
```

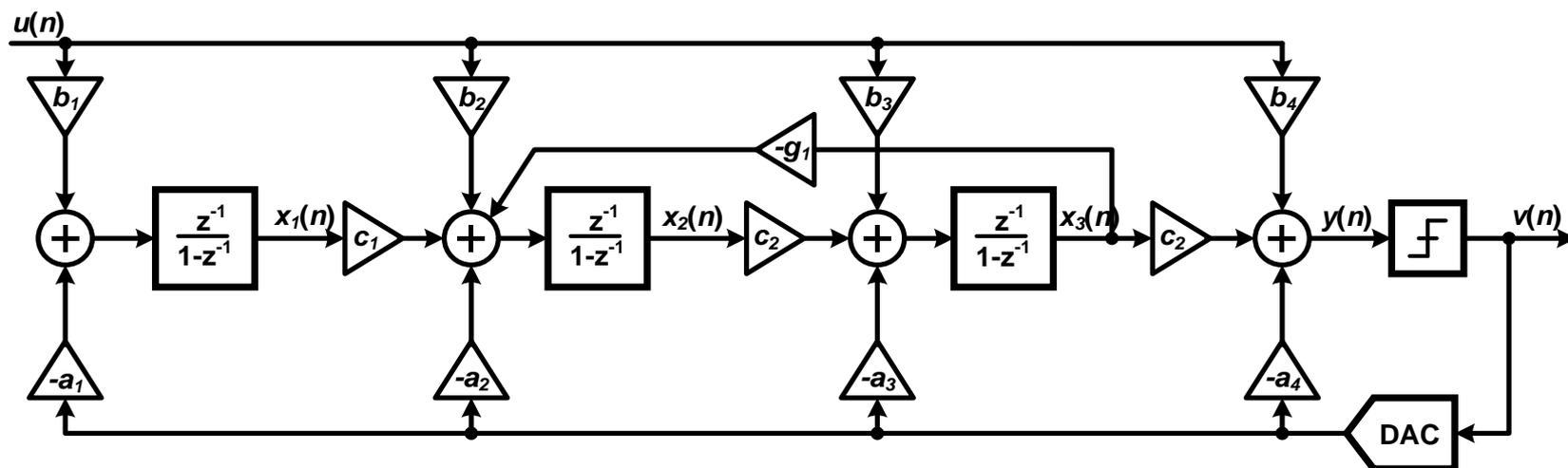
- Dynamic Range Scaling (DRS) performed to scale loop-filter states to a bounded value
 - Scaling performed using ABCD matrix representation of the loop-filter
 - See any introductory text on Linear Systems

```
ABCD = stuffABCD(a,g,b,c,form);
```

```
[ABCDs umax] = scaleABCD(ABCD, nLev, f0, xLim);
```

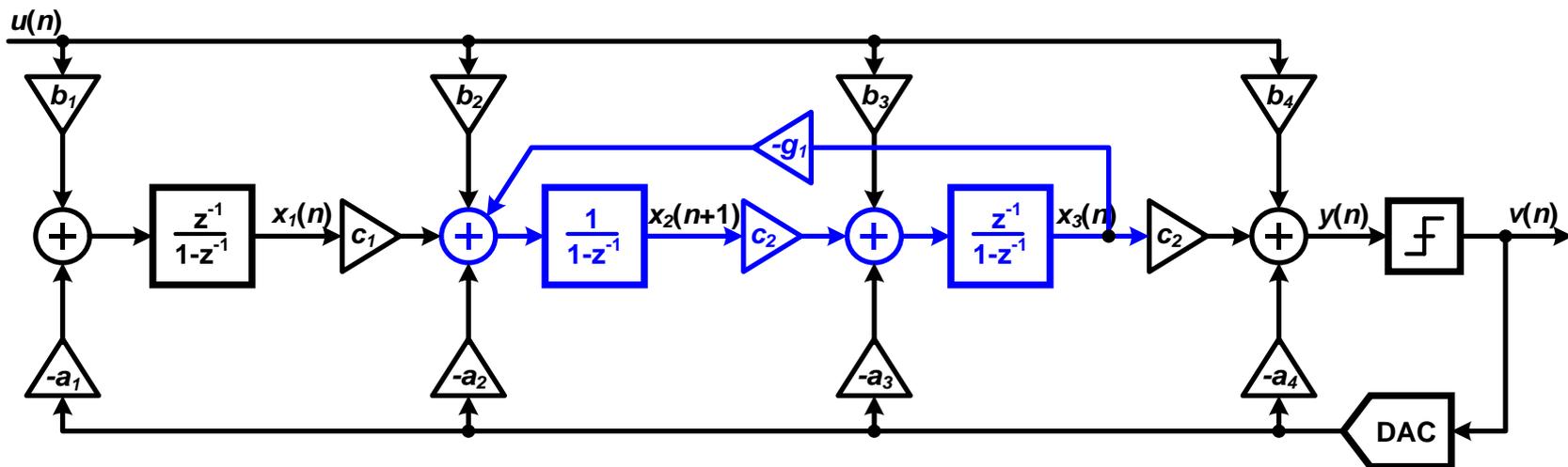
```
[a,g,b,c] = mapABCD(ABCDs, form);
```

CIFB (Cascade of Integrators with Distributed Feedback)



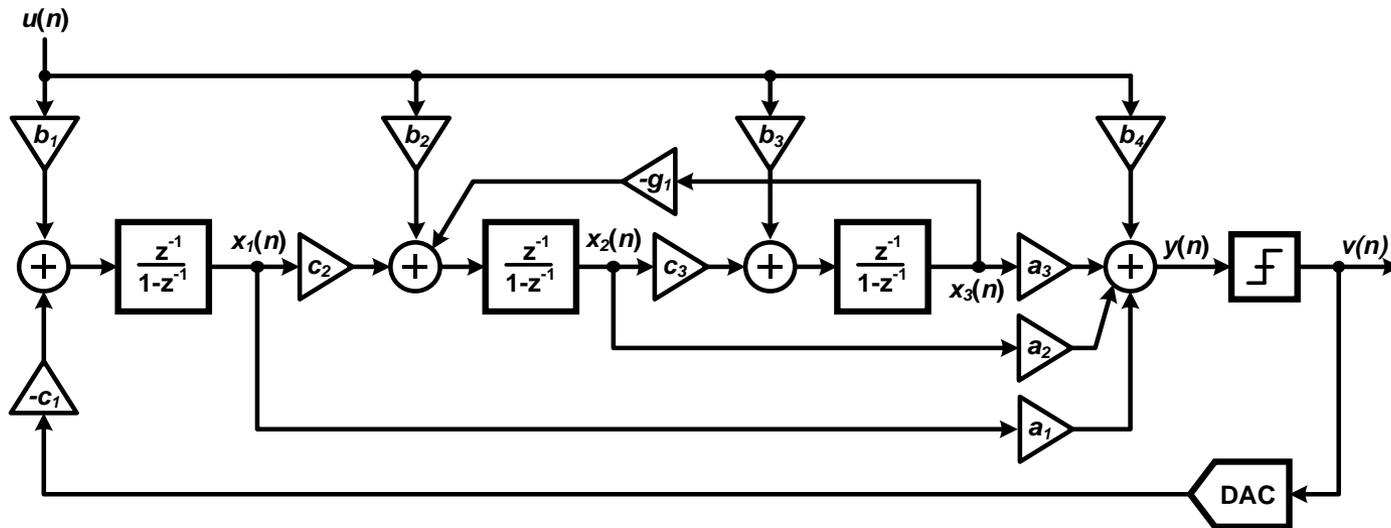
- Cascade of delaying integrators:
 - Feedback coefficients **a**'s realize the zeros of L_1 and thus the NTF and STF poles.
 - Feed-in coefficients **b**'s determine zeros of L_0 and thus the STF zeros.
 - State scaling coefficients **c**'s are used for dynamic range scaling.

CRFB (Cascade of Resonators with Distributed Feedback)



- Combine a non-delaying and a delaying integrator with local feedback around them, to form a stable resonator
 - Local feedback coefficients \mathbf{g} 's realize the complex zeros in the NTF.
 - Implements NTF with complex zeros $z_i = e^{\pm j\sqrt{g_1}}$
- For odd-order, use an integrator in the front to avoid noise coupling due to g

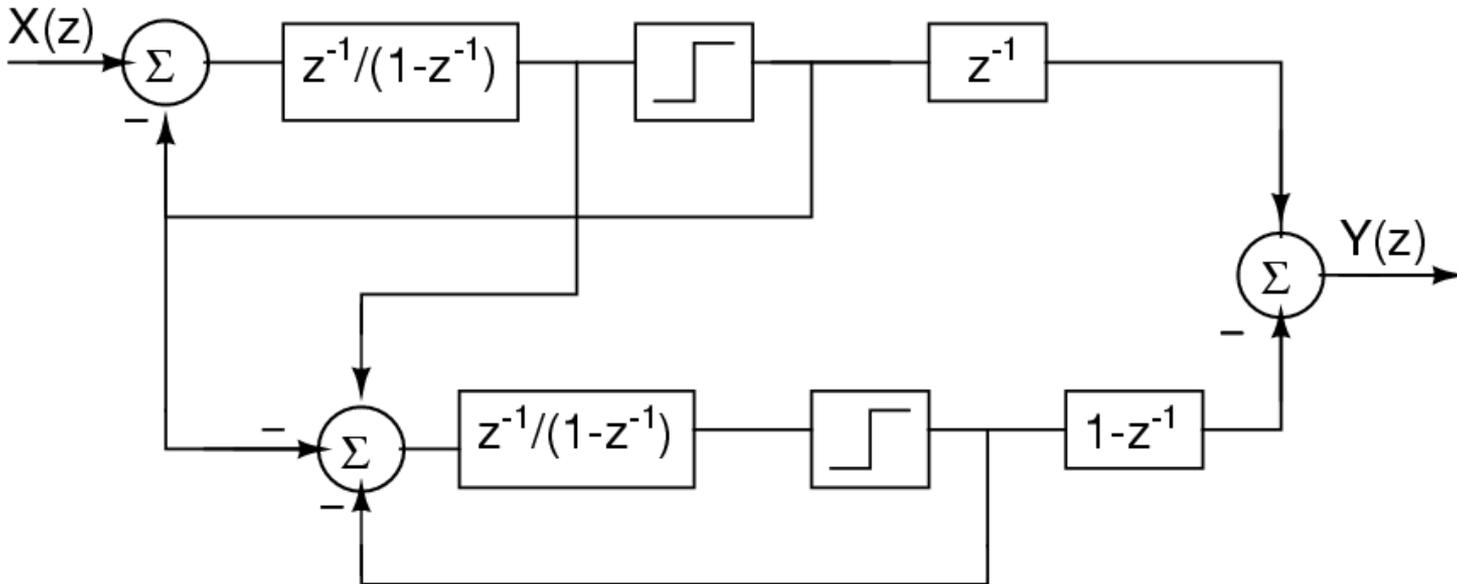
CIFF (Cascade of Integrators with Feed-Forward Summation)



- ❑ Feedforward summation of states
- ❑ For $b_1 = b_{N+1} = 1$ and b_2 to $b_N = 0$, $STF = 1$
 - Loop-filter only processes quantization noise, low power and distortion
- ❑ Feedforward loop-filters typically result in lower-power implementation

$\Delta\Sigma$ Modulator Architectures

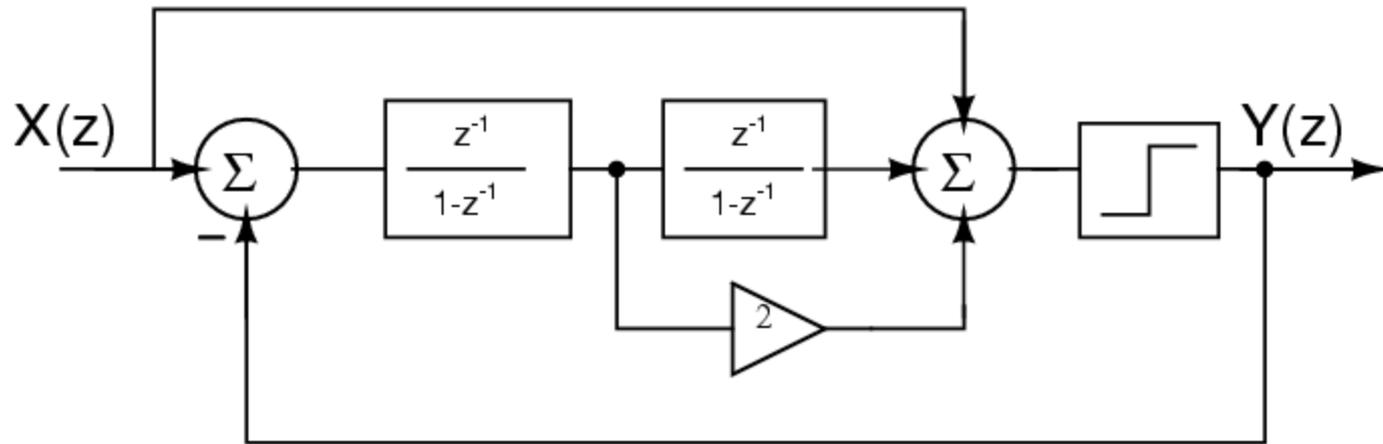
- Cascade/ MASH architecture:



- Eg. Two first order modulators are used to implement second order modulator.
- Stability concerns are relaxed but mismatch in the two forward paths should be properly monitored.

$\Delta\Sigma$ Modulator Architectures

□ Feedforward modulators

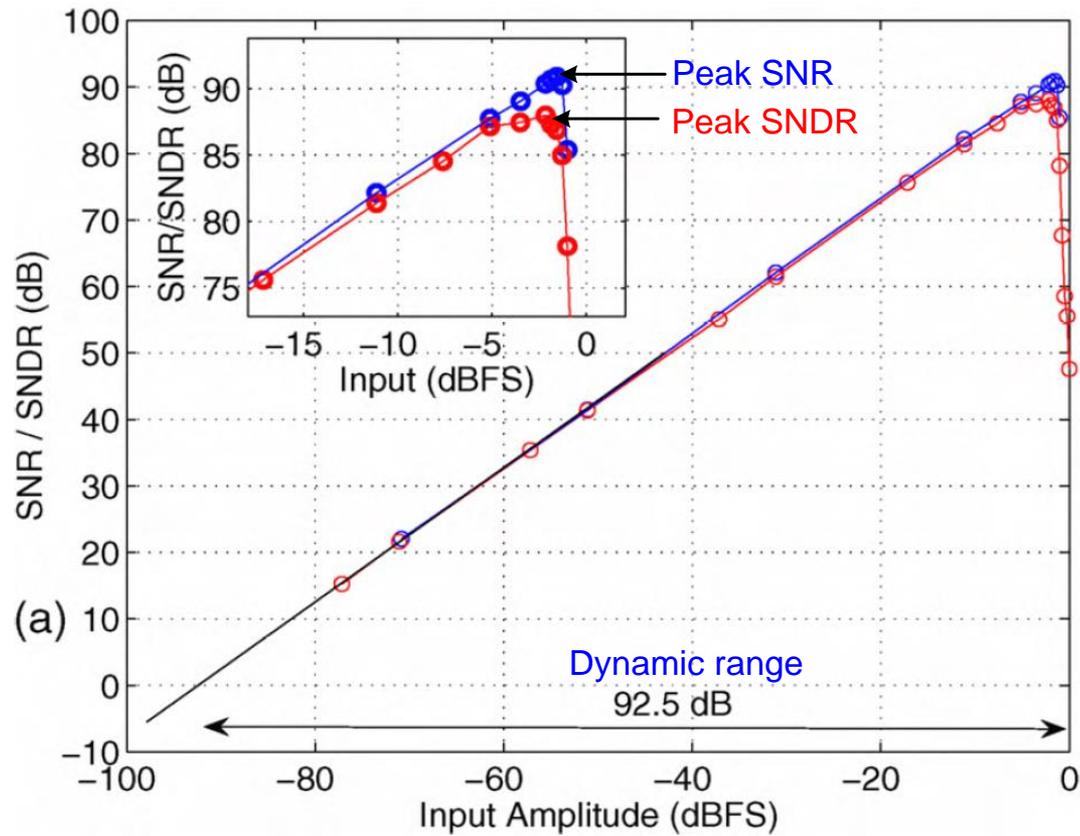


- Most popular architecture.
- Input signal is summed at Nth stage integrator output.
- Summation block may be required at higher order modulators.
- Multibit quantizer is necessary.

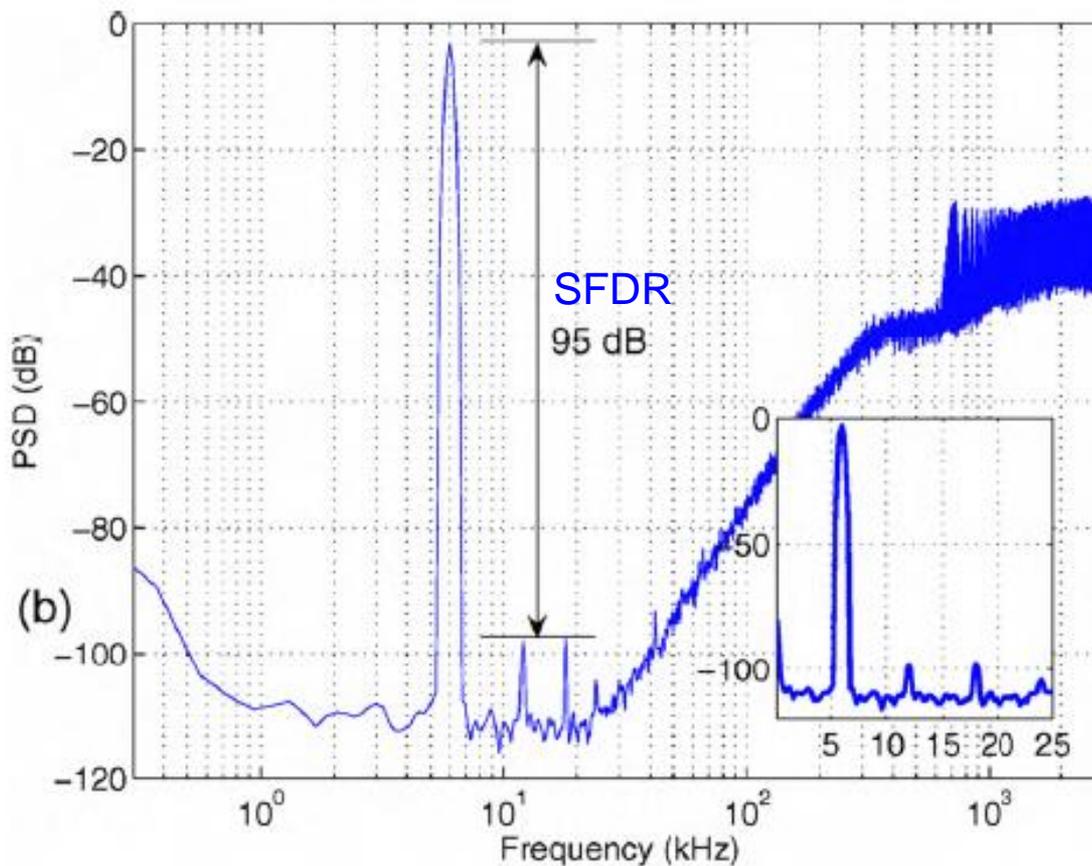
Key Terminologies :

- ❑ SQNR – Signal to quantization noise ratio
 - Thermal/electrical noise are not included.
- ❑ SNR - Signal to noise ratio
 - Distortion is not included.
- ❑ SDNR - Signal to noise and distortion ratio
 - All noise sources are included.
- ❑ ENOB – Effective number of bits (resolution)
 - This is very important than actual number of output bits
- ❑ Dynamic Range (DR)
 - Measured with input of the modulator shorted.
- ❑ Harmonic Distortion
 - THD is usually total harmonic distortion. Or Third??
- ❑ Spur Free Dynamic Range (SFDR)
 - Very key parameter in communication systems

Frequency Domain Measurements



Spurious (tone) Free Dynamic Range (SFDR)



References

Delta-Sigma Data Converters

- B.1** R. Schreier, G. C. Temes, *Understanding Delta-Sigma Data Converters*, Wiley-IEEE Press, 2005 ([the Green Bible of Delta-Sigma Converters](#)).
- B.2** S. R. Norsworthy, R. Schreier, G. C. Temes, *Delta-Sigma Data Converters: Theory, Design, and Simulation*, Wiley-IEEE Press, 1996 ([the Yellow Bible of Delta-Sigma Converters](#)).
- B.3** S. Pavan, N. Krishnapura, “Oversampling Analog to Digital Converters Tutorial,” 21st International Conference on VLSI Design, Hyderabad, Jan, 2008.
- B.4** S. H. Ardalan, J. J. Paulos, “An Analysis of Nonlinear behavior in Delta-Sigma Modulators,” *IEEE TCAS*, vol. 34, no. 6, June 1987.
- B.5** R. Schreier, “An Empirical Study of Higher-Order Single-Bit Delta-Sigma Modulators,” *IEEE TCAS-II*, vol. 40, no. 8, pp. 461-466, Aug. 1993.
- B.6** J. G. Kenney and L. R. Carley, “Design of multibit noise-shaping data converters,” *Analog Integrated Circuits Signal Processing Journal*, vol. 3, pp. 259-272, 1993.
- B.7** L. Risbo, “Delta-Sigma Modulators: Stability Analysis and Optimization,” Doctoral Dissertation, Technical University of Denmark, 1994 [[Online](#)].
- B.8** R. Schreier, J. Silva, J. Steensgaard, G. C. Temes, “Design-Oriented Estimation of Thermal Noise in Switched-Capacitor Circuits,” *IEEE TCAS-I*, vol. 52, no. 11, pp. 2358-2368, Nov. 2005.