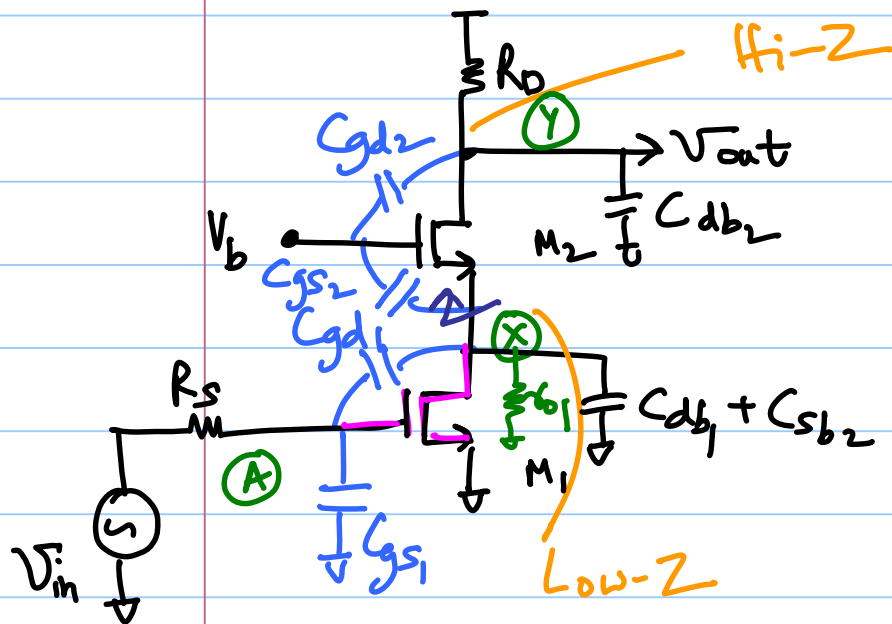


ECE 511 - Lecture 19

Note Title

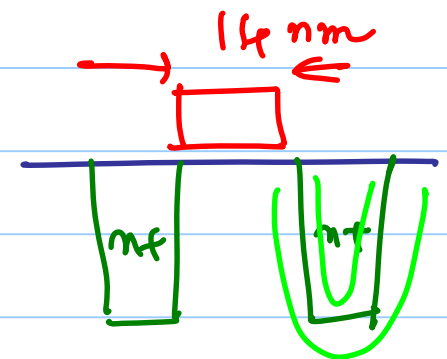
3/31/2015

Cascode Amplifier Frequency Response:



$$A_v = -g_{m1} \cdot R_D \parallel g_{m2} r_{o2} r_{o1}$$

$$\approx -g_{m1} R_D$$



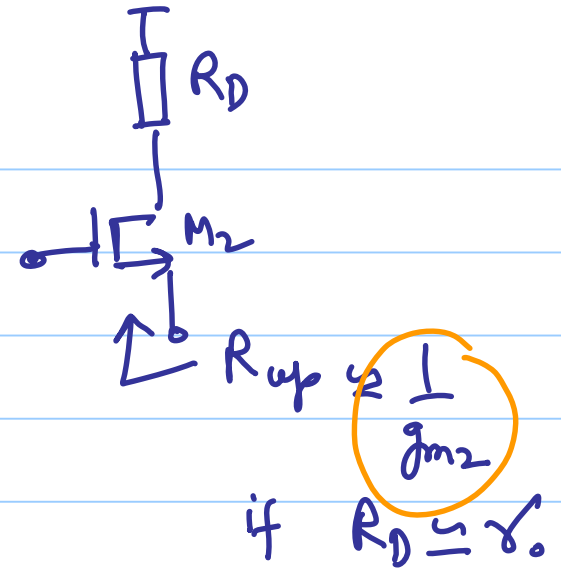
$C_{gd1} \rightarrow$ Miller Cap determined by the gain from $\textcircled{A}$ to $\textcircled{X}$

In nano cmos

$$A_x = -g_{m1} \cdot R_x = -g_{m1} \times \left(\frac{1}{g_{m2} + g_{m2b}} \parallel r_{o1} \right)$$

$$S_1 = \frac{g_{m1}}{g_{m2} + g_{mb}}$$

5 - 1 if M_1 & M_2 are identical



$$\omega_{PA} = \frac{1}{R_s \left[C_{gs1} + \left(1 + \frac{g_{m1}}{g_{m2} + g_{m3}} \right) C_{gd1} \right]} \approx \frac{1}{R_s [C_{gs1} + 2C_{gd1}]}$$

"Miller Killer"

because G_{d1} sees
a gain of -1
not A_v

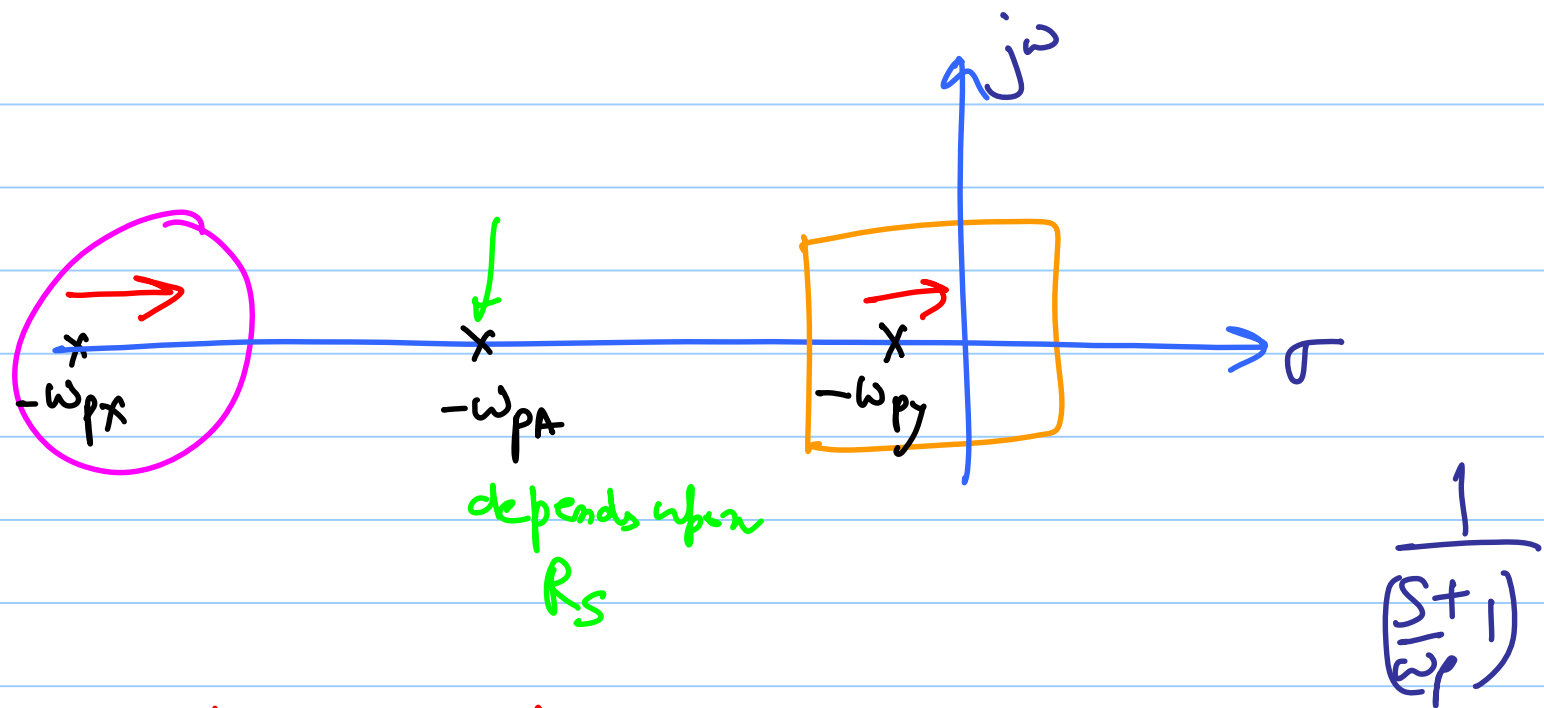
@ node-x

$$C_x = 2C_{gd1} + C_{db1} + C_{sb2} + C_{gs2}$$

$$\omega_{px} = \frac{1}{\frac{1}{(g_{m2} + g_{mb2})} \times C_x} = \frac{g_{m2} + g_{mb2}}{C_x}$$

output pole

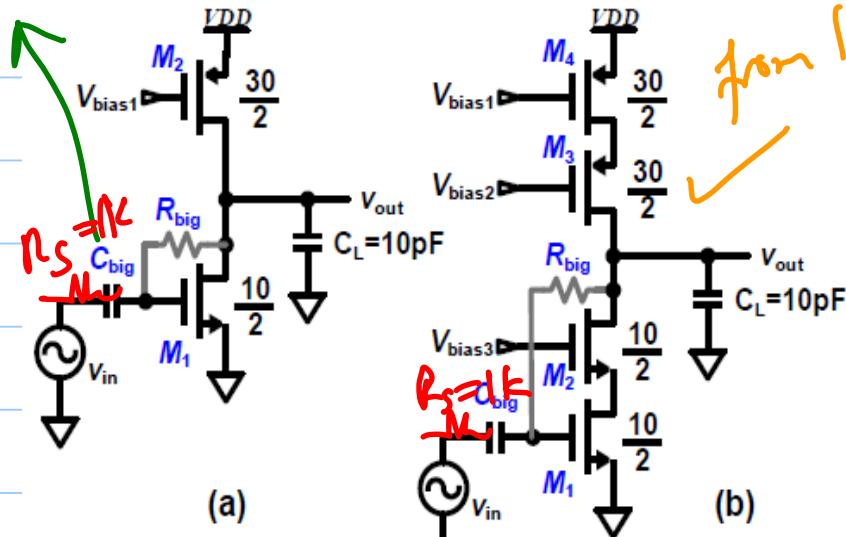
$$\omega_{py} = \frac{1}{\boxed{R_D} (C_{db2} + C_L + C_{gd2})}$$



What if R_D was a cascode
 PMOS current mirror

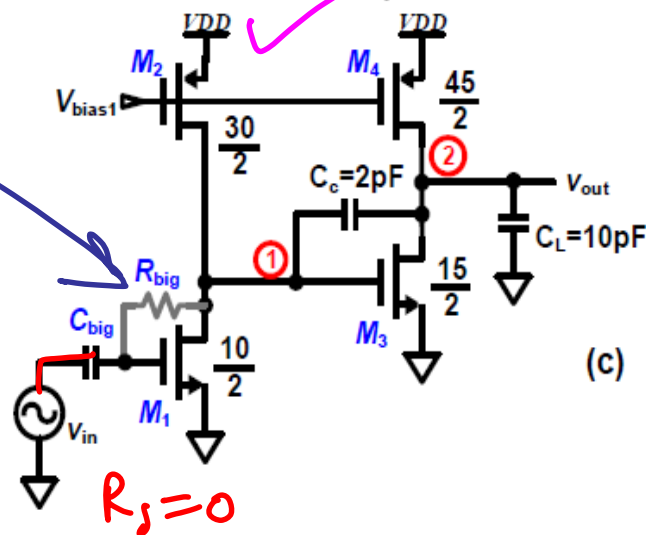
(a) & (c) use 2-stage model with Miller Cap

$$\omega_{p1} = \frac{1}{g_{m2} R_2 R_1 C_c}$$



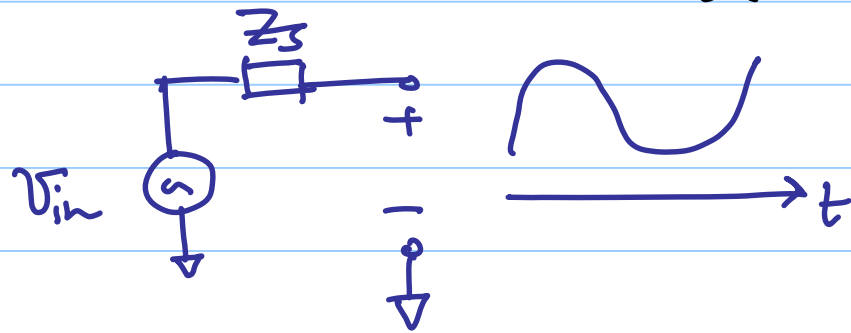
$$\omega_z = \frac{g_{m2}}{C_c}$$

$$\omega_{um} = \frac{g_{m1}}{C_c}$$



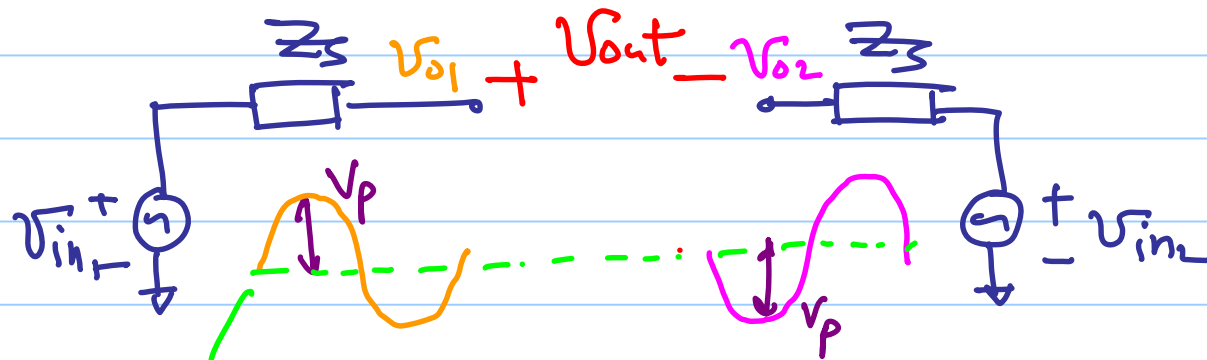
Differential Amplifiers

- * Single-ended signal is defined as one that is measured wrt a fixed potential i.e. ground



1-wire

- * A differential signal is defined as one measured between two nodes that have equal & opposite signal excursions, around a fixed potential



(Common mode)

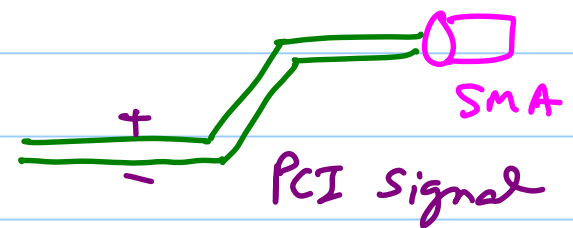
CM-level

2-wire signalling



differential signal

$$V_d = v_{o1} - v_{o2}$$



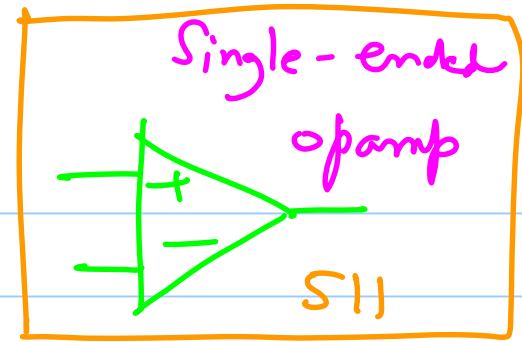
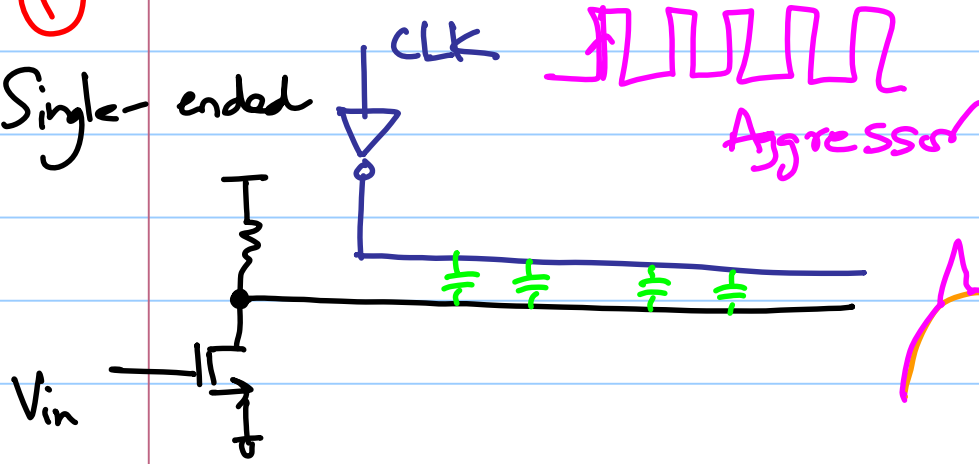
$$V_{CM} = \frac{(v_{o1} + v_{o2})}{2} \leftarrow \text{average of } v_{o1} \text{ \& } v_{o2}$$

↑ ideally a fixed DC value

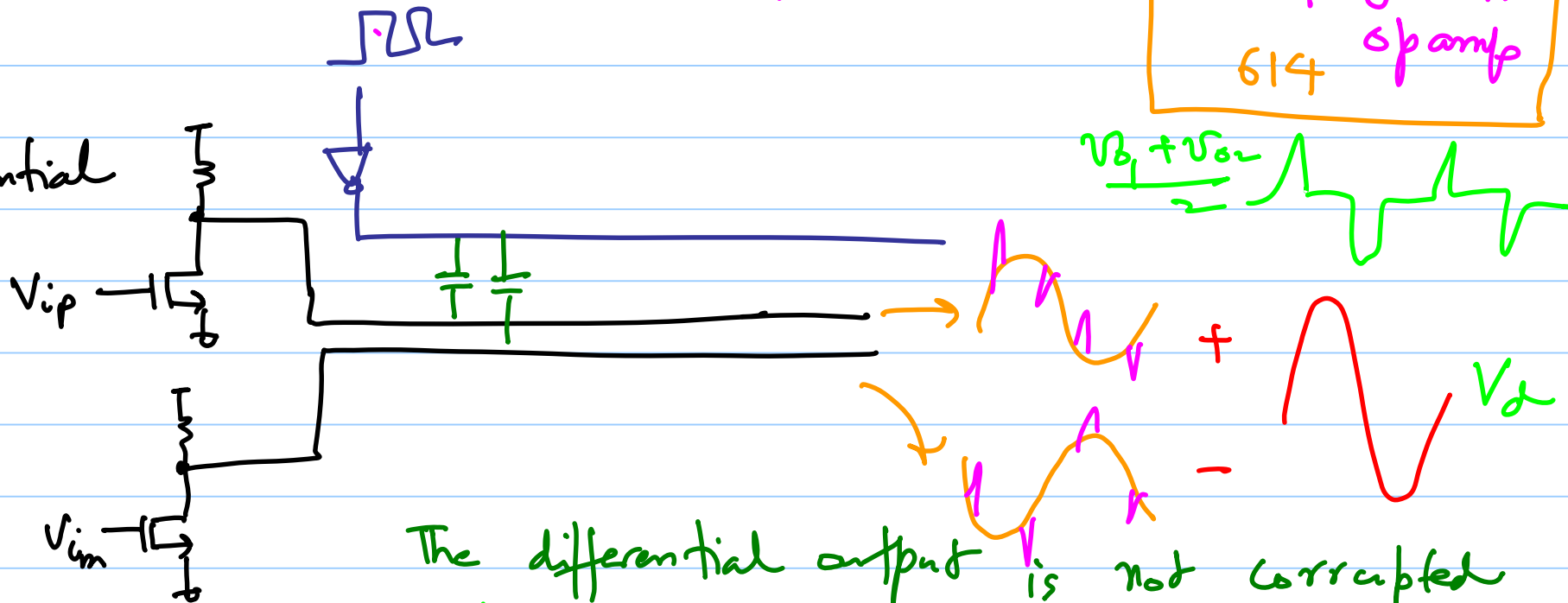
Advantages :

①

Single-ended



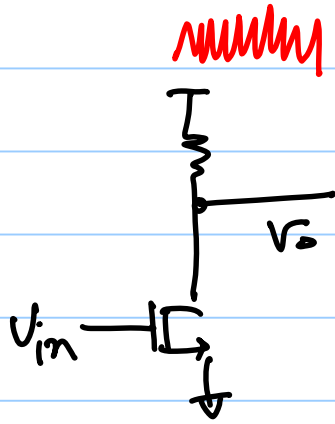
Differential



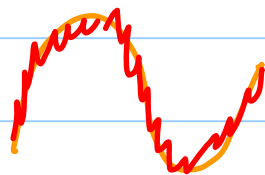
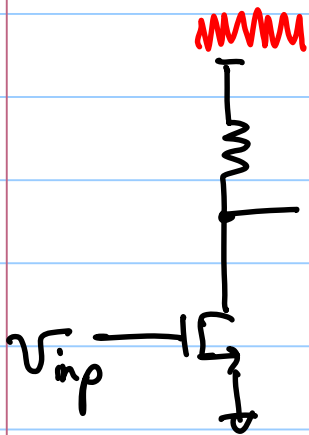
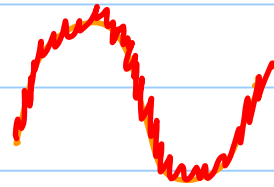
The differential output is not corrupted
 ↳ CM-level is disturbed

This arrangement rejects any CM disturbance

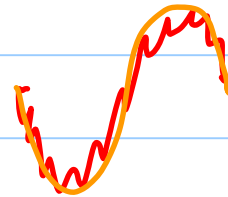
②



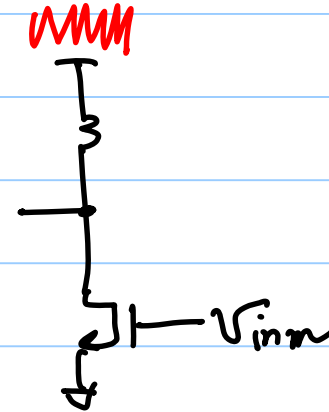
SE circuit



+



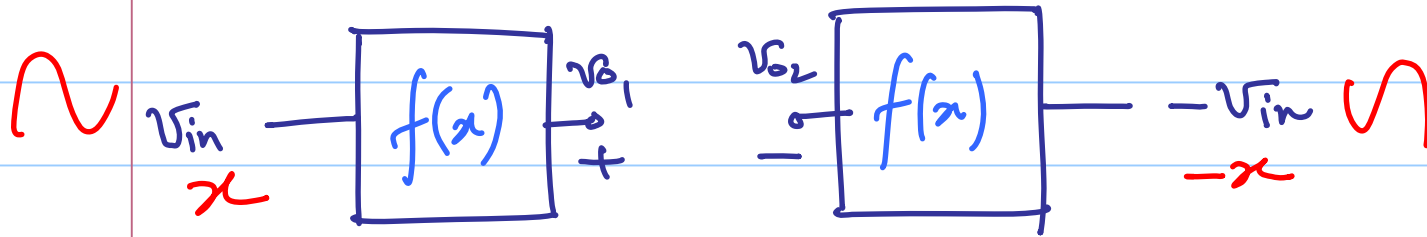
-



rejects correlated
supply noise



③ Simple-biasing $\Rightarrow$ Differential amplifiers don't need
big RDC for biasing

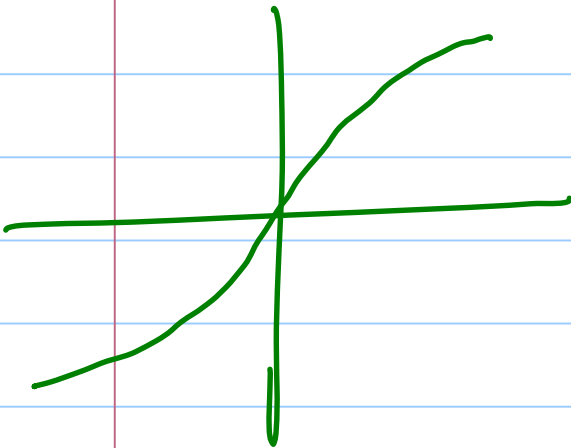


$$V_d = v_{01} - v_{02} = f(x) - f(-x)$$

Odd function

$$V_{cm} = \frac{v_{01} + v_{02}}{2} = f(x) + f(-x)$$

even function



$$V_d = f(x) - f(-x) \quad \text{odd function}$$

$$= [\alpha_0 + \alpha_1 x + \alpha_2 x^2 + \alpha_3 x^3] + \dots$$

$$- [\alpha_0 - \alpha_1 x + \alpha_2 x^2 - \alpha_3 x^3] + \dots$$

$$= 2\alpha_1 x + \underline{\underline{2\alpha_3 x^3}} + \underline{\underline{x^5}} + \dots$$

gets rid of even-order non-linearity

"More linear"

CS

