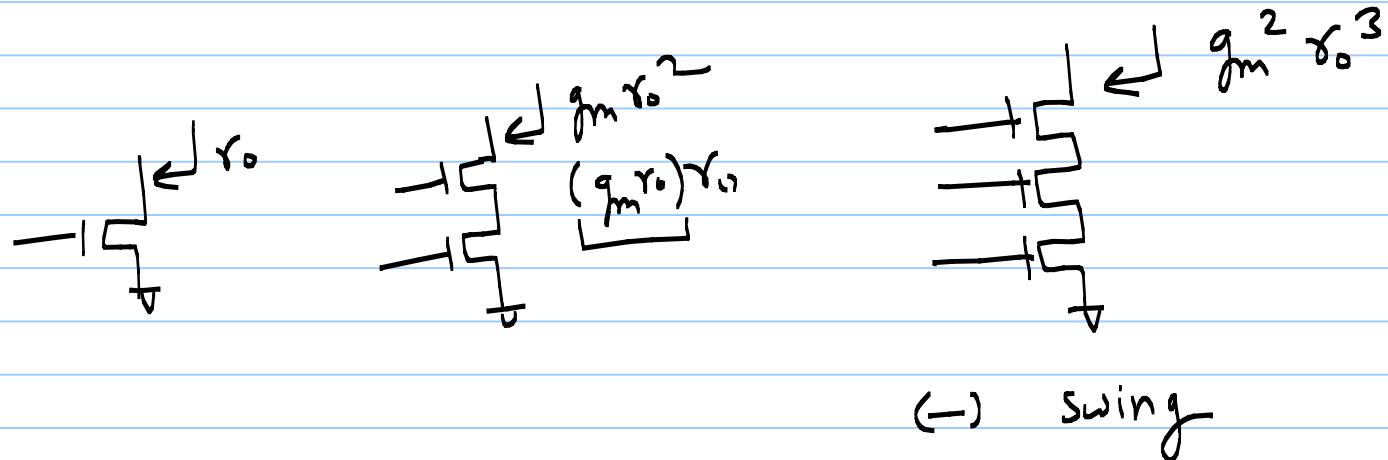
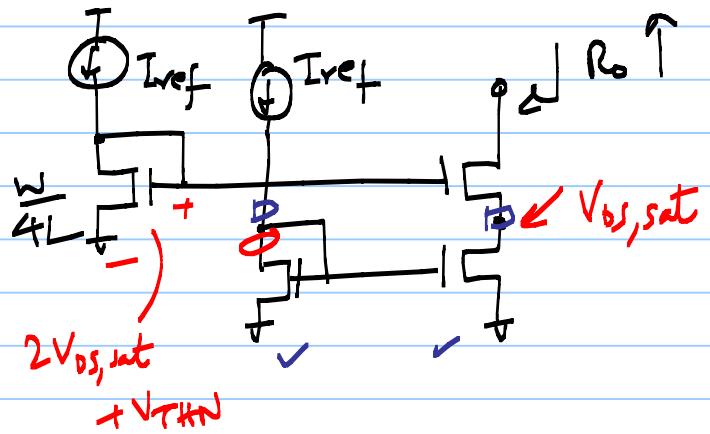
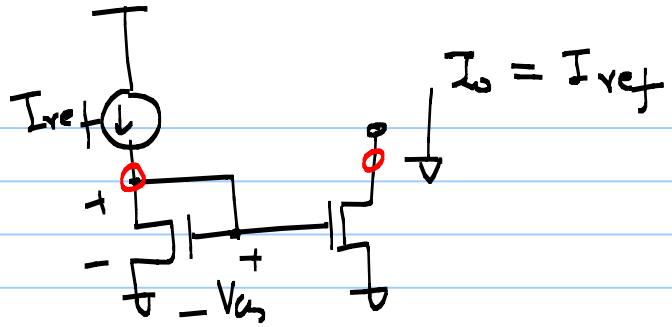


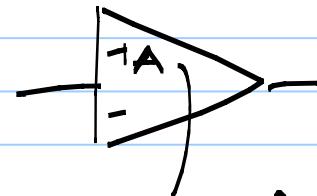
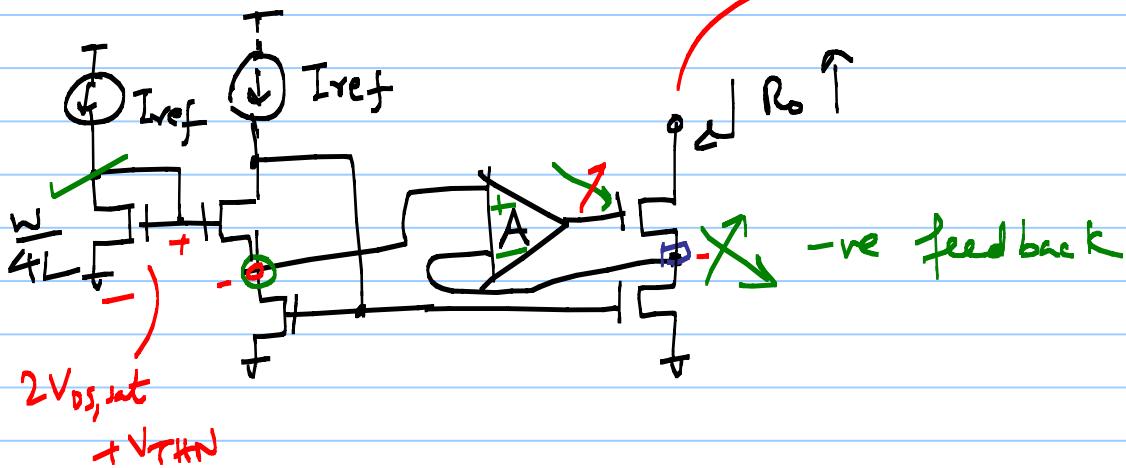
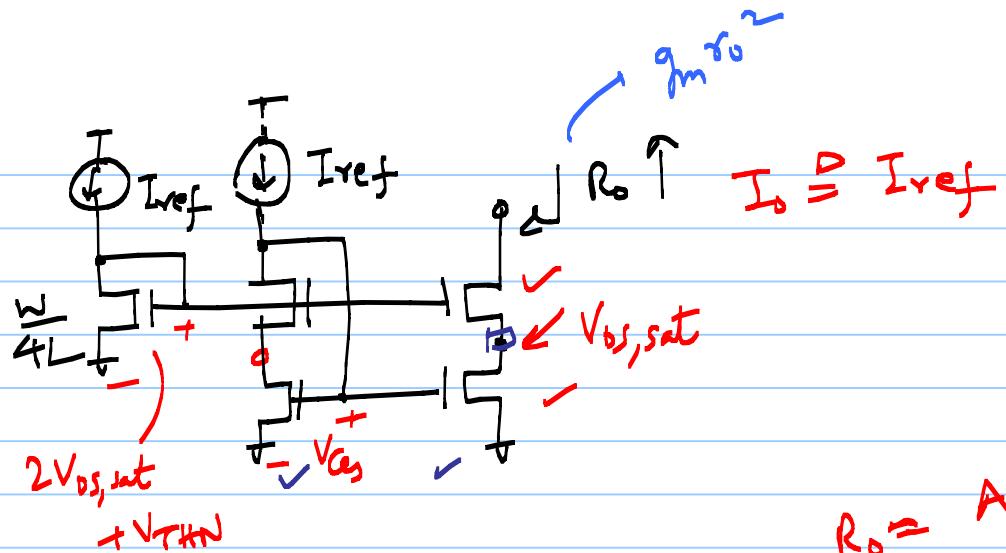
ECE 5411 - Lecture 11.

Note Title

2/28/2011







$$A(s) = \frac{A_D}{(1 + s/\omega_p)}$$

"Later"

Single-Stage Amplifiers

amplify an analog or digital signal

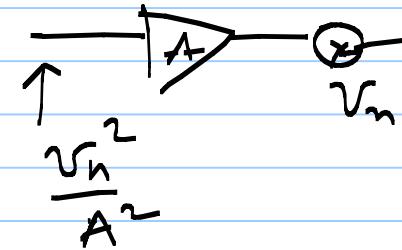
↳ drive a load

↳ overcome noise
of a subsequent stage

↳ provide logic levels
to a digital gate

↳ in feedback systems

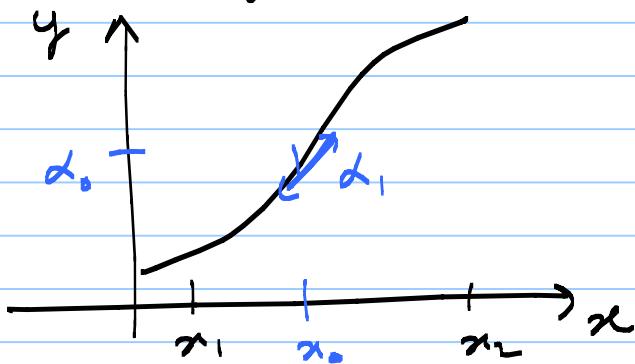
† Single-Stage amplifiers → low-frequency behavior



- * input-output characteristics of an amplifier

$$y(t) = \alpha_0 + \alpha_1 x(t) + \alpha_2 x^2(t) + \dots + \alpha_n x^n(t)$$

$$x_1 \leq x \leq x_2$$



Small-signal amplitude

$$y(t) \approx \underline{\alpha_0} + \underline{\alpha_1} x(t)$$

**operating
point**

**Small-signal
gain**

for $\underline{\alpha_1 x(t)} \ll \alpha_0$, the bias point is disturbed negligibly.

$$\Delta y = \alpha, \Delta x$$

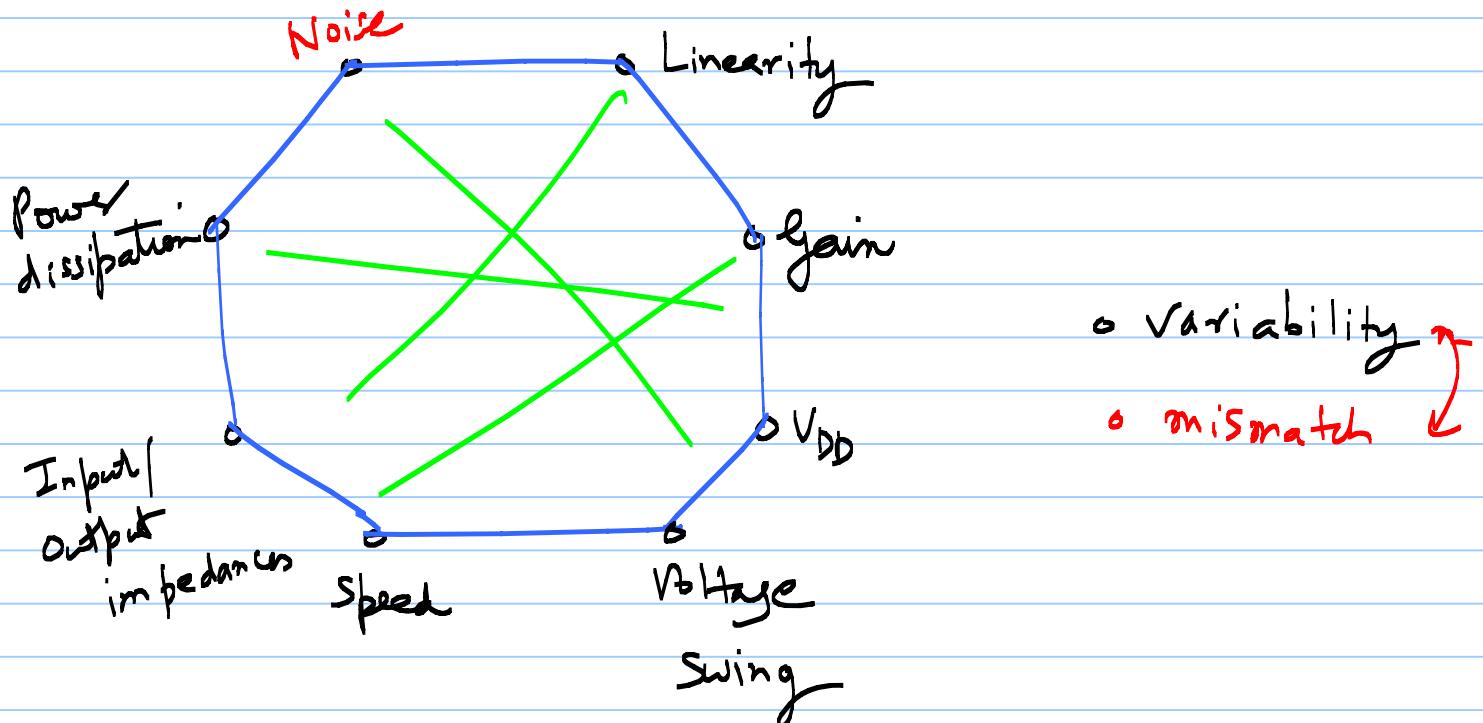
As $x(t)$ increases in magnitude

\Rightarrow large-signal behavior

\Rightarrow higher-order terms start manifesting

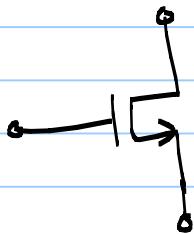
↳ distortion

"Analogy design Octagon"



- Variability ↗
- mismatch ↘

multidimensional design "tradeoffs" problem.

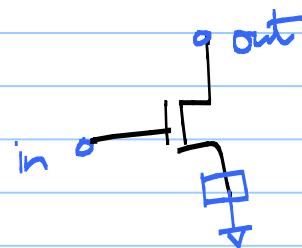


3-terminals

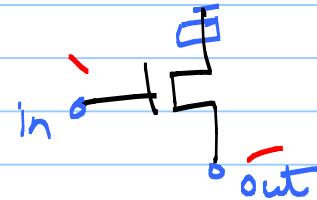
- 1 - input
- 2 - output

$$3C_2 = 3$$

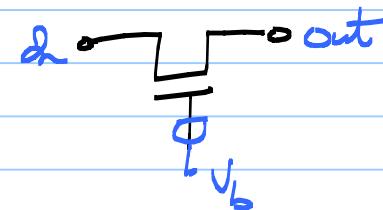
~~3C₂~~



C_S
=

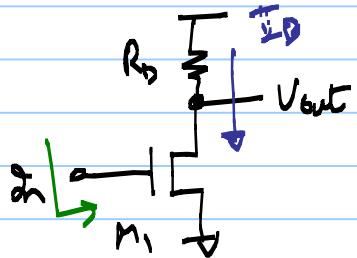


C_D
(Source-follower)



C_G

Common Source Stage

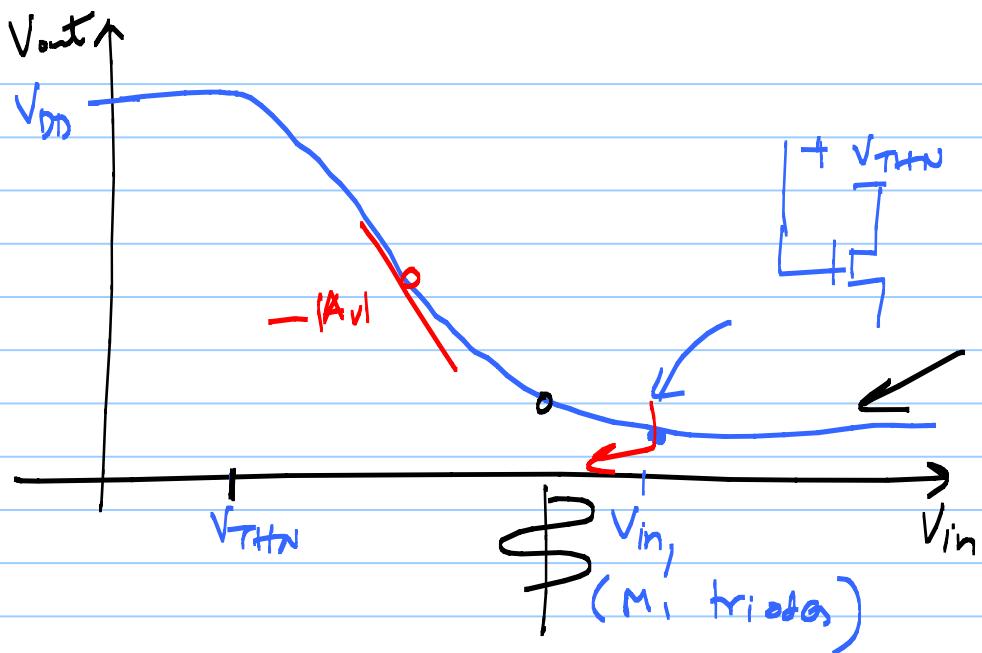


@ low-freq

Z_{in} is very high

Initially $V_{in} = 0$, $V_{out} = V_{DD}$

$V_{in} \geq V_{THN}$, V_{DD} is sufficiently large

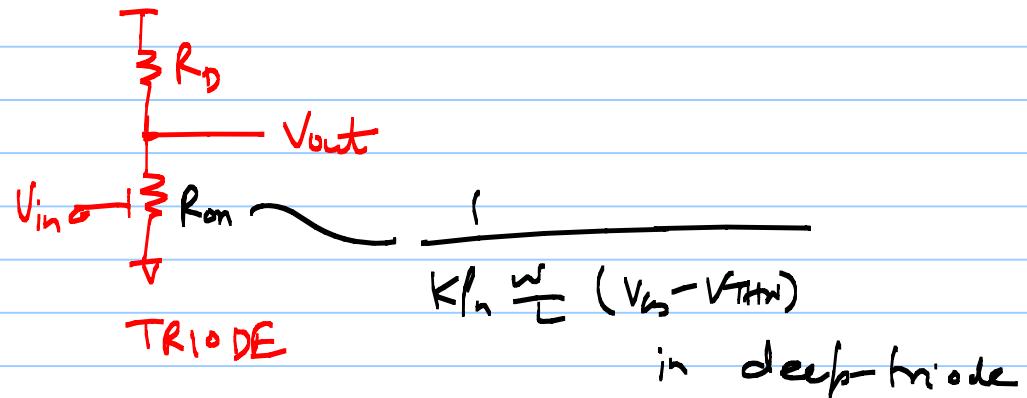
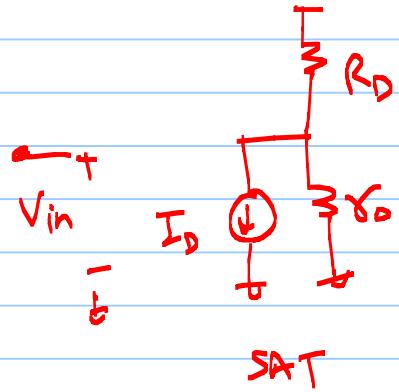


$$V_{out} = V_{DD} - I_D \cdot R_D$$

$$V_{out} = V_{DD} - R_D \cdot \frac{kP_n}{2} \frac{w}{L} (V_{in} - V_{THN})^2 \rightarrow ①$$

we increase V_{in} further, M₁ triodes

$$V_{out} = V_{DD} - R_D \cdot \frac{kP_n w}{L} \left((V_{in} - V_{THN}) V_{out} - \frac{V_{out}^2}{2} \right) \rightarrow ②$$

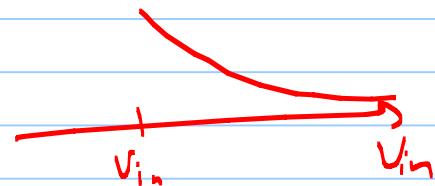


In triode:

$$V_{out} = V_{DD} \cdot \frac{R_D}{R_D + R_m} = \frac{V_{DD}}{1 + kP_n \frac{w}{l} (V_{in} - V_{THN})}$$

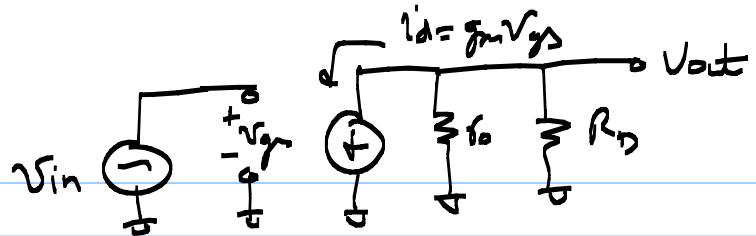
In saturation:

$$V_{out} = V_{DD} - R_D \cdot \frac{kP_n}{2} \frac{w}{l} (V_{in} - V_{THN})^2, \quad \lambda = 0$$



Small-signal
Voltage gain

$$A_V = \frac{\partial V_{out}}{\partial V_{in}} = -R_D \cdot \boxed{kP_n \frac{w}{l} (V_{in} - V_{THN})}$$
$$= -\frac{g_m R_D}{}$$



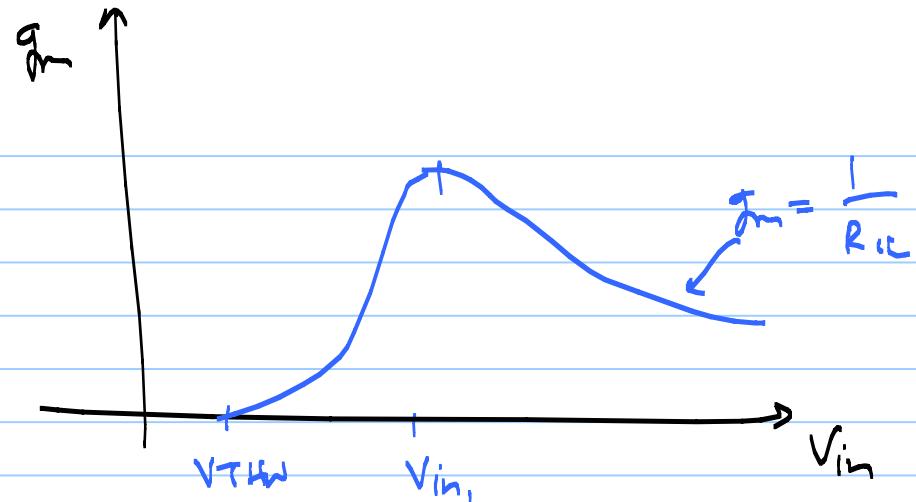
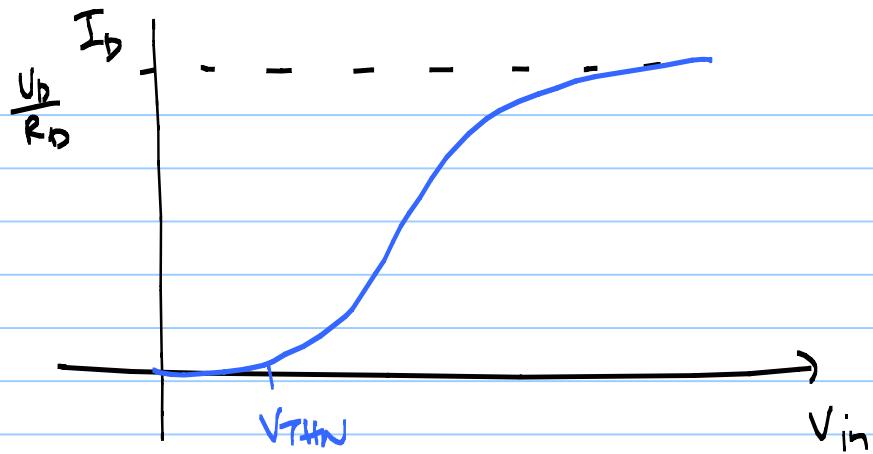
$$\begin{aligned}
 V_{out} &= -i_d (r_o \parallel R_D) \\
 &= -g_m v_{gs} (r_o \parallel R_D) \\
 &\xrightarrow{V_{in}}
 \end{aligned}$$

$$A_v = \frac{V_{out}}{V_{in}} = -g_m (r_o \parallel R_D) \leq -g_m R_D$$

$\times \quad A_v = -R_D \cdot \overbrace{\frac{kT}{L} \left(V_{in} - V_{THN} \right)}^{g_m}$

* gain varies with large signal swing

* $g_m = f(V_{in})$



$$\Delta v = -\sqrt{2kp_n \frac{w}{L} I_D} \cdot R_D$$

$$= -\sqrt{2kp_n \frac{w}{L} I_D} \cdot \frac{V_{RD}}{I_D}$$

$$= -\sqrt{2kp_n \frac{w}{L}} \cdot \frac{V_{RD}}{\sqrt{I_D}}$$

V_{RD} is the drop across R_D

|Av| $(\frac{w}{L}) \uparrow \Rightarrow$ larger device capacs

$V_{DD} \uparrow \Rightarrow$ lower swing

$I_D \downarrow \Rightarrow$ lower speed.

$$f_T \propto \frac{V_{DD}}{L}, A_v \propto \frac{L}{V_{DD}}$$