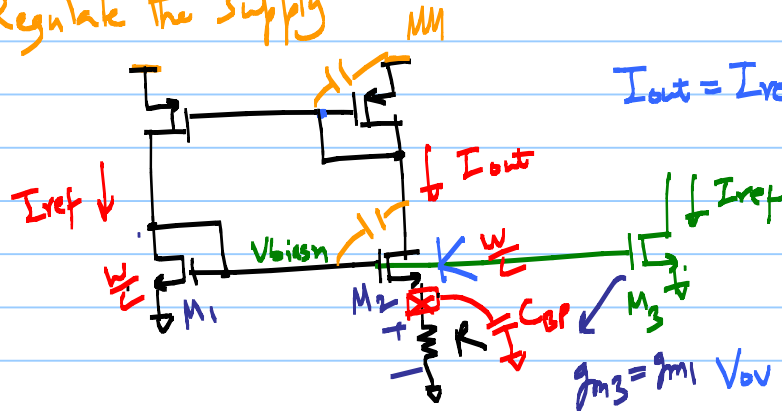


ECE 511 - Lecture 6

Note Title

2/7/2013

Regulate the supply

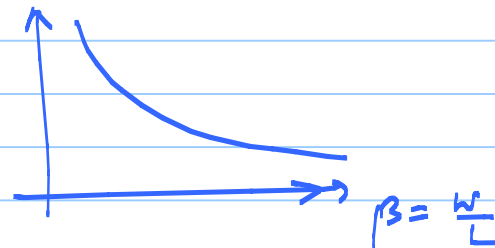


$$I_{out} = I_{ref} = \frac{2}{\beta R^2} \left(1 - \frac{1}{\sqrt{K}}\right)^2, \quad \boxed{0}$$

$I_D = \text{const}$

$$g_{m3} = g_{m1} V_{ov}$$

$$V_{as1} = V_{as2} + I_{out} \cdot R$$



$\lambda > 0 \Rightarrow V_{DD}$ dependence

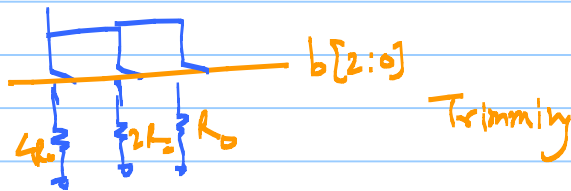
$$I_D = \frac{\beta}{2} V_{ov}^2$$

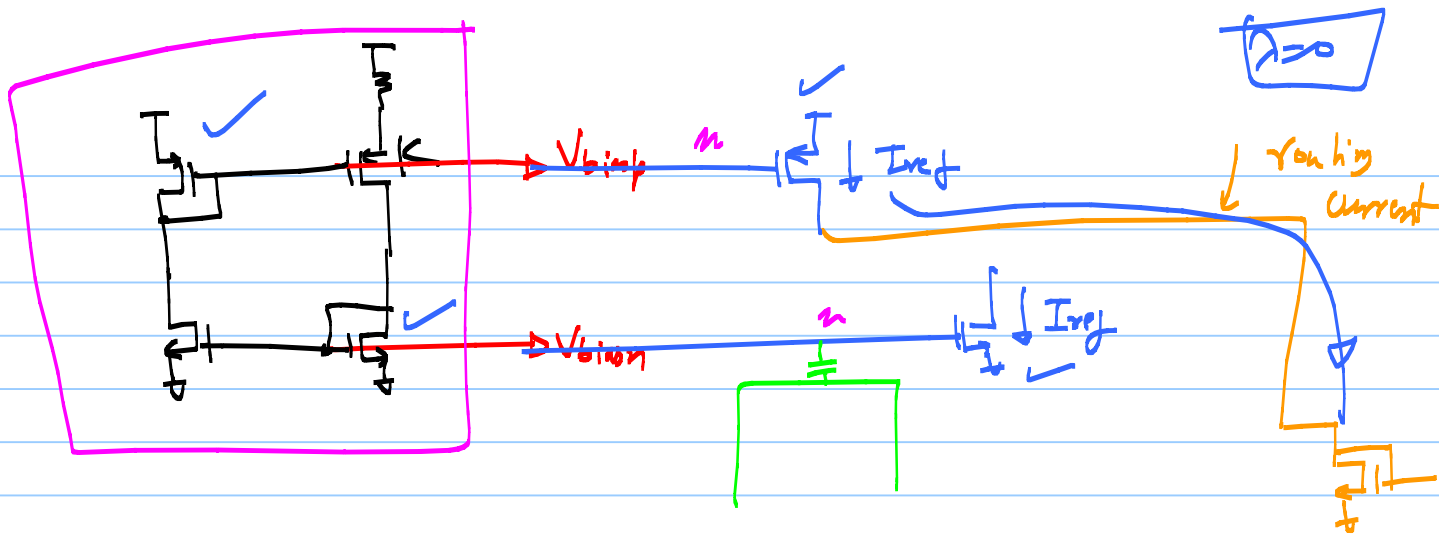
$$g_m \text{ of } M_1 \Rightarrow g_{m1} = \sqrt{2\beta \cdot I_{REF}}$$

$$\Rightarrow g_{m1} = \frac{2}{\boxed{R}} \left(1 - \frac{1}{\sqrt{K}}\right) \leftarrow \text{indep of } V_{DD}$$

Constant g_m -biasing circuit

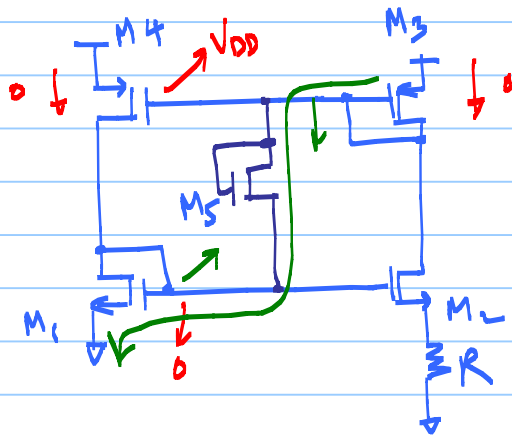
$$\text{for } K=4 \Rightarrow g_{m1} = \frac{1}{R}$$





* routing current is better than routing
voltages over large distance

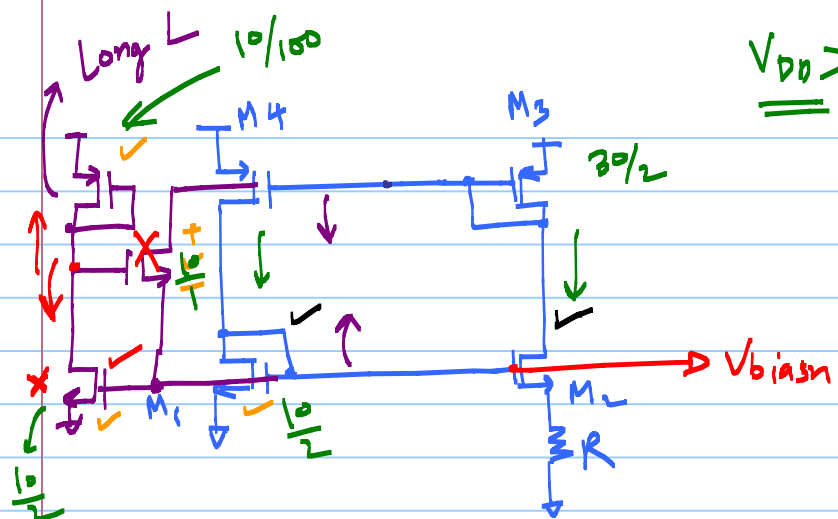
Start-up problem



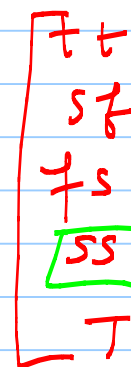
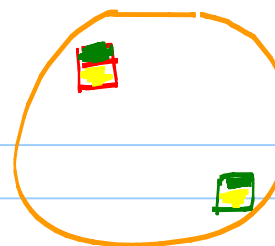
M_5 provides current path

↳ turns M_1 & M_3 ON

↳ M_2 & M_4 ON



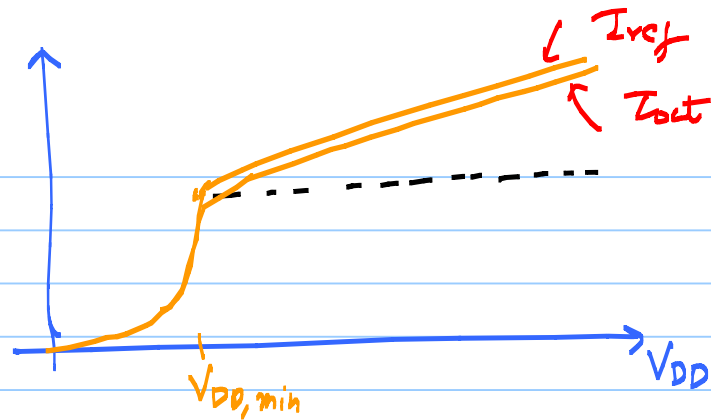
$$\underline{\underline{V_{DD} > V_{GS1} + V_{SD4}}}$$



* in all process corners

fig 20.18

50nm CMOS

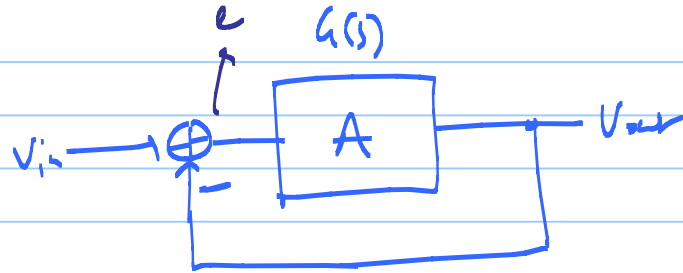


bad supply sensitivity

⇒ reduce V_{DD} sensitivity

⇒ reduce V_{DS} variation

$$H(s) = 1$$

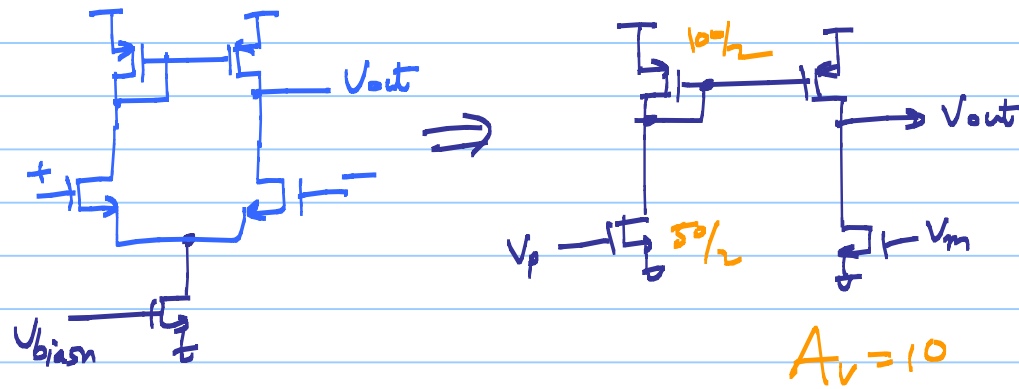


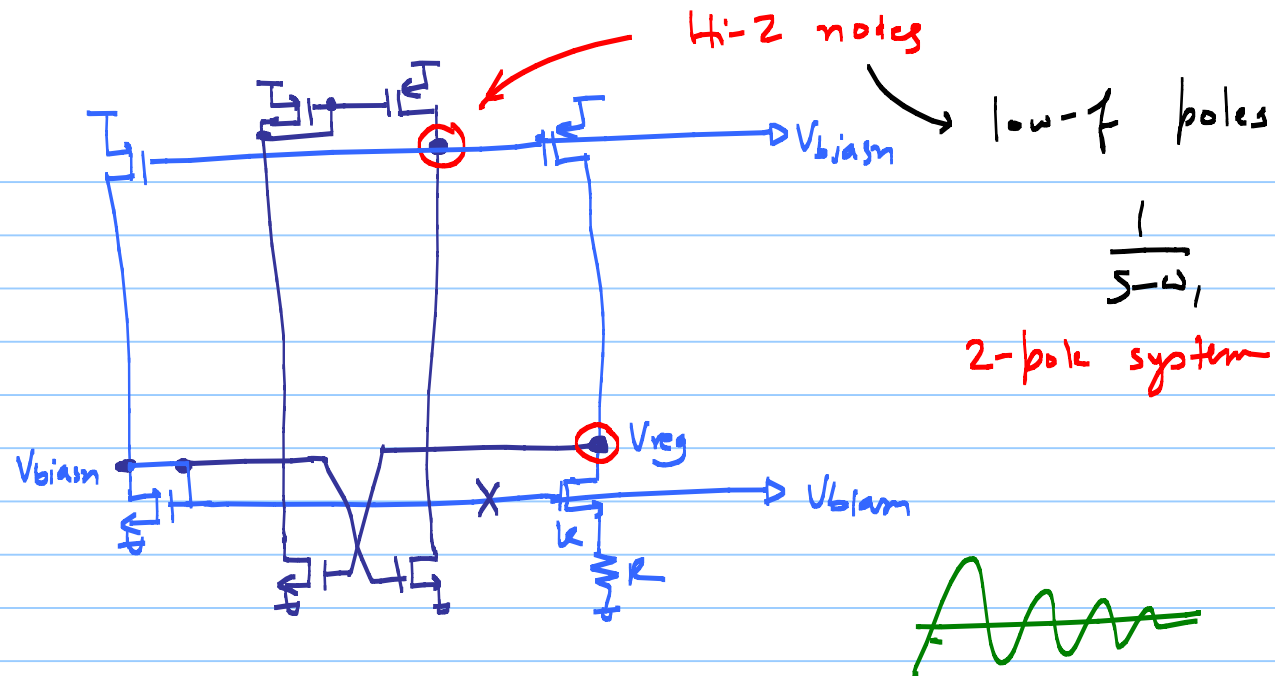
$$e \rightarrow 0$$

if $A \rightarrow \infty$

$$e \sim \frac{1}{A}$$

CRUDE





* two dominant poles

$\hookrightarrow$ needs compensation

Modify $\rightarrow$ remove one of the hi-Z nodes

