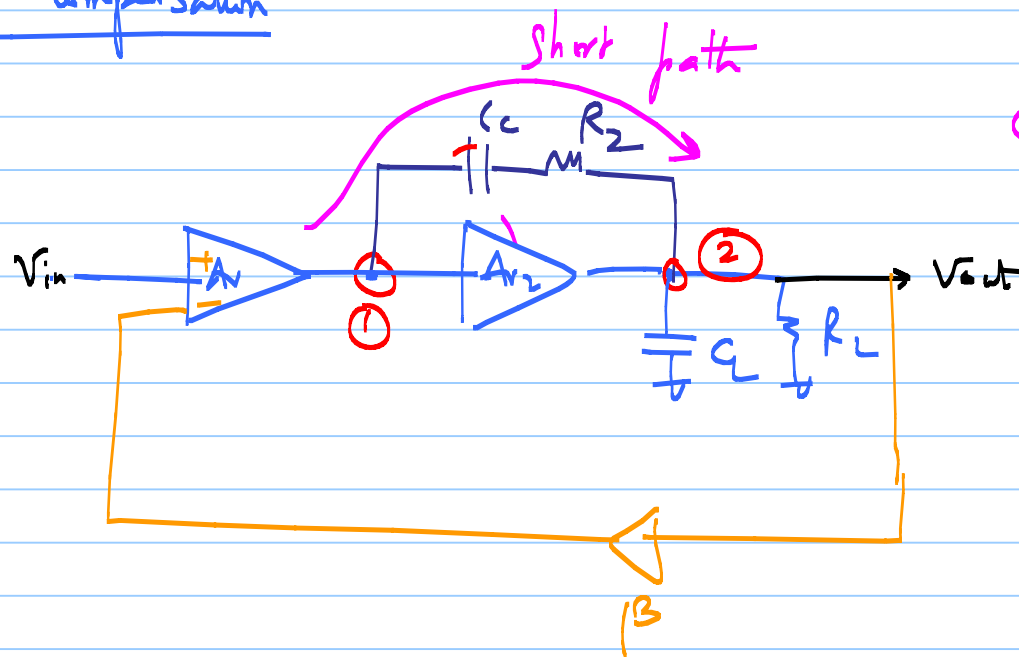


ECE 511 - Lecture 26

Note Title

5/2/2013

Miller Compensation



@ high frequencies
the system looks
like a 1st-order
system

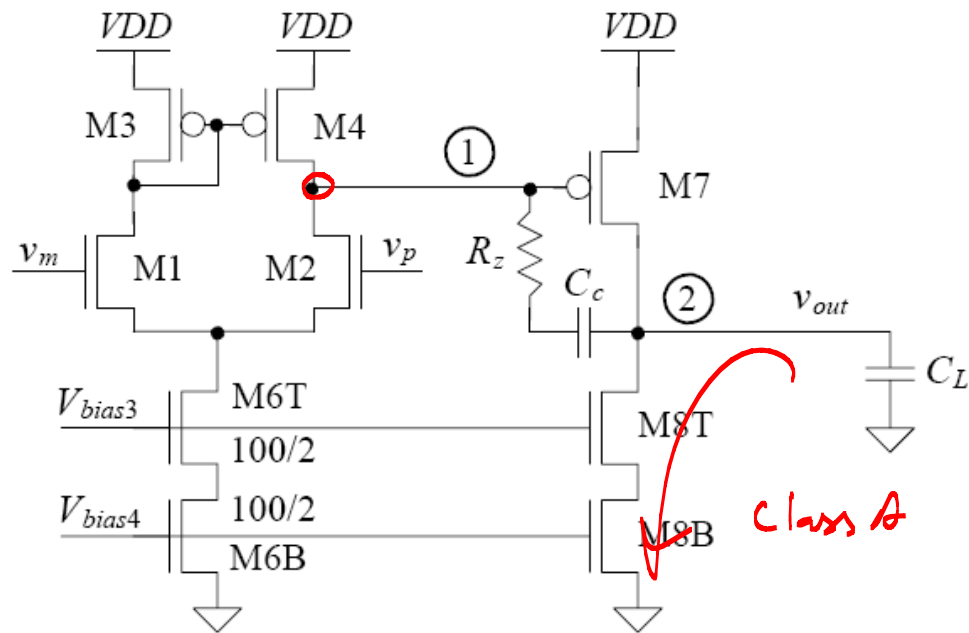
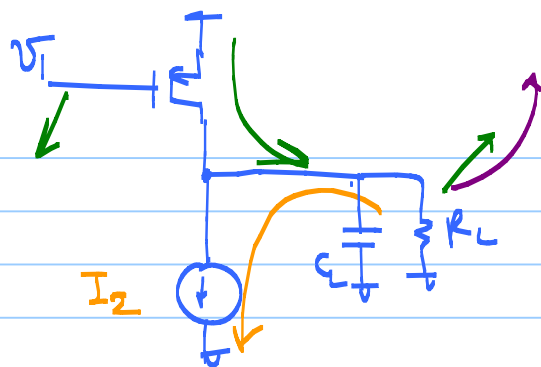
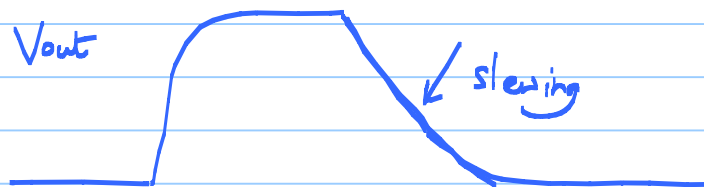
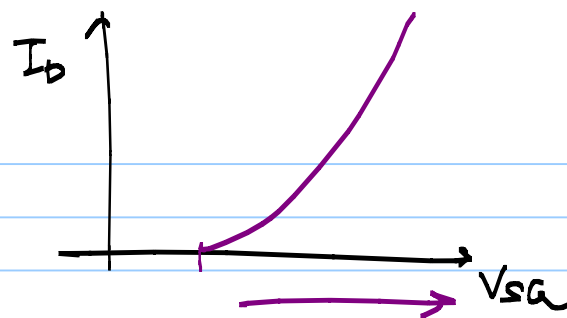


Figure 24.8 Calculating the frequency response of the op-amp.



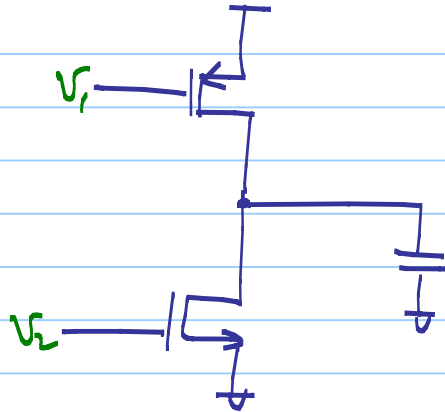
$$\frac{dV_{out}}{dt} = \frac{I_2}{C_L}$$

Class-A stage

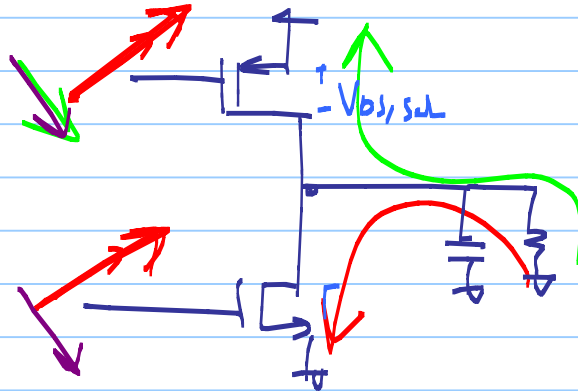


Class-B

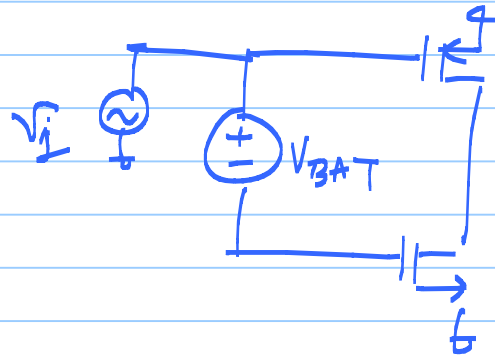
either pull-up or pull-down is
on at a time



Class- AB



near rail-to-rail
output swing



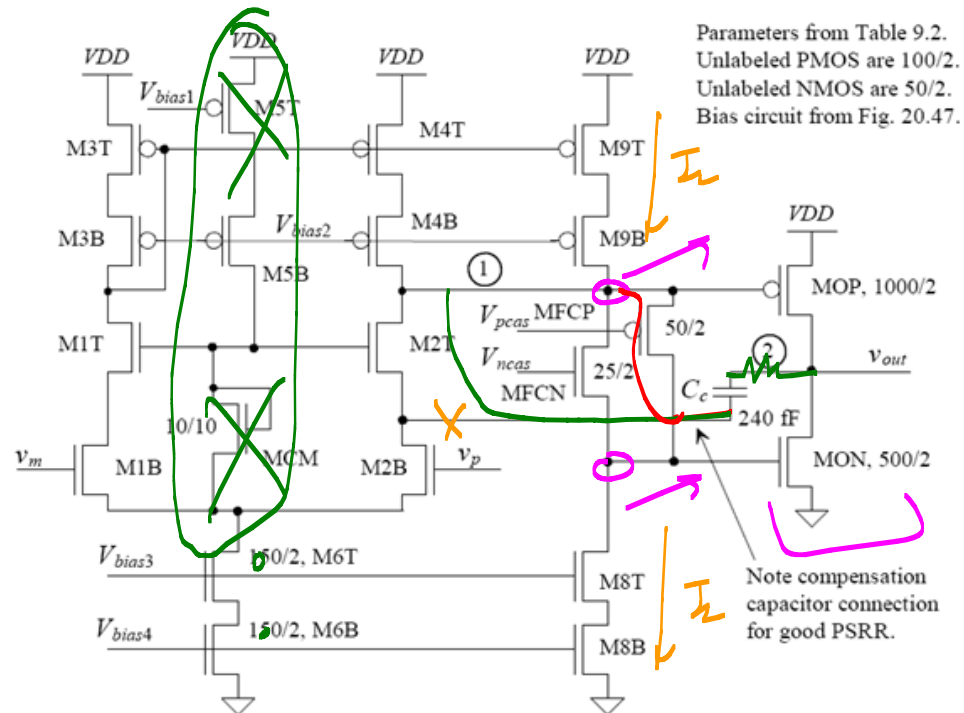


Figure 24.29 A CMOS op-amp with output buffer.

class-AB
 o/p stage

