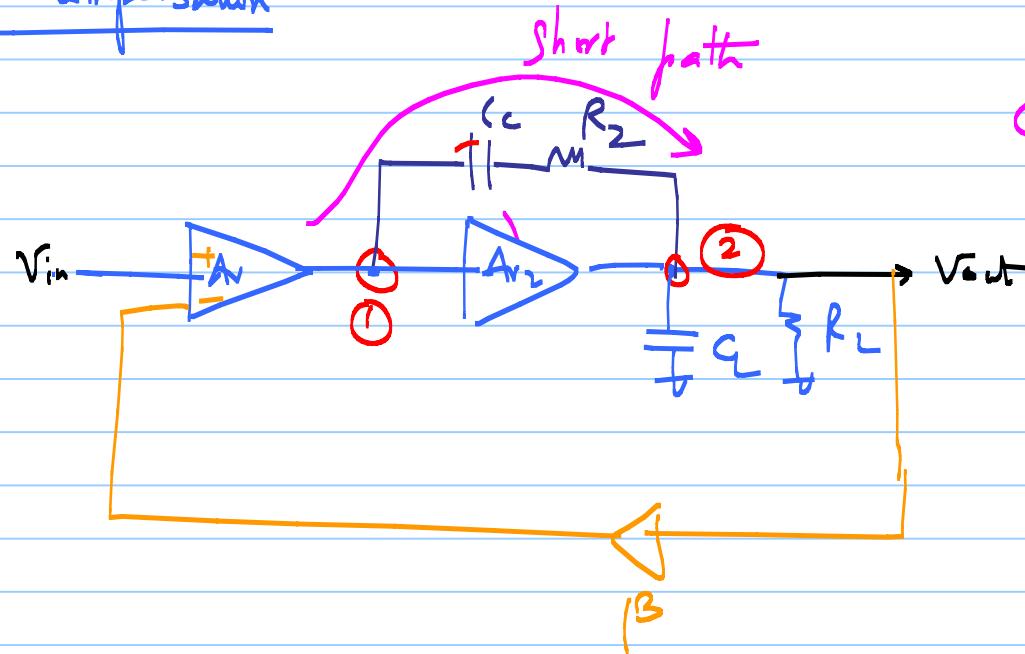


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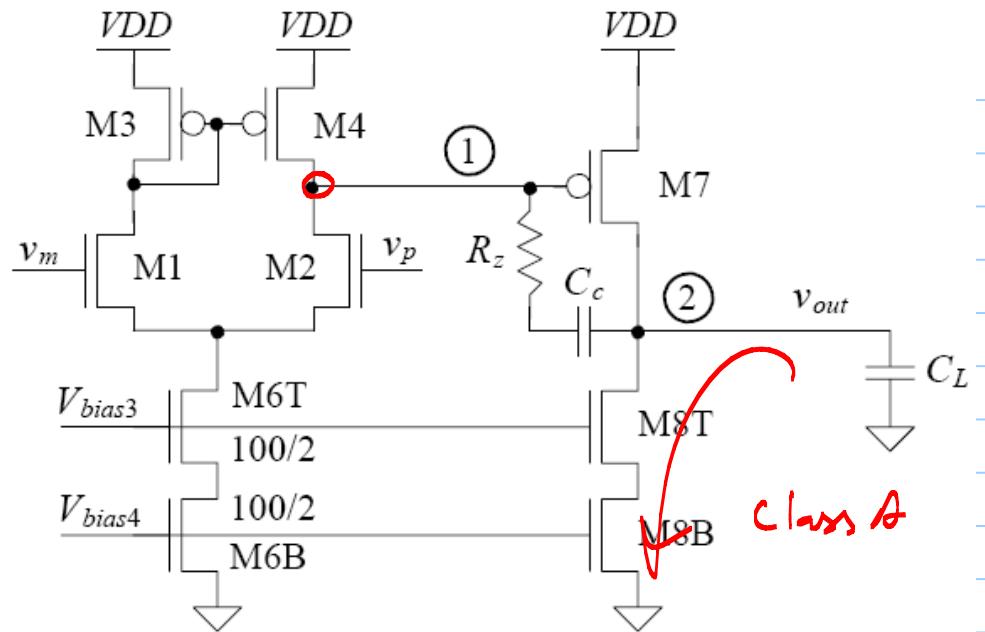
Note Title

5/2/2013

Miller Compensation

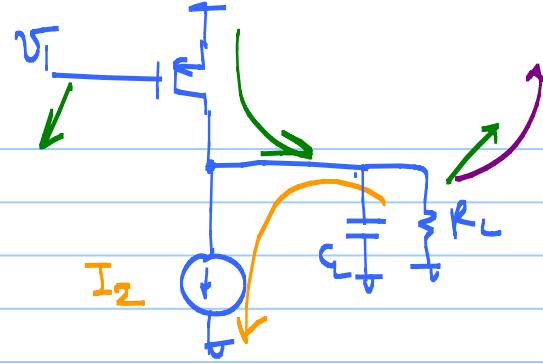


@ high frequencies
the system looks
like a 1st-order
system



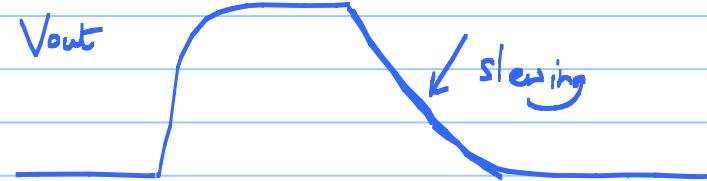
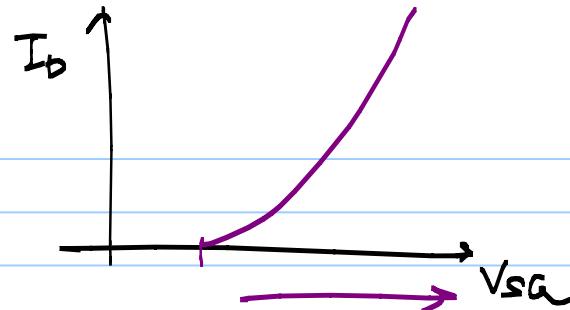
class-A op-amp
output stage

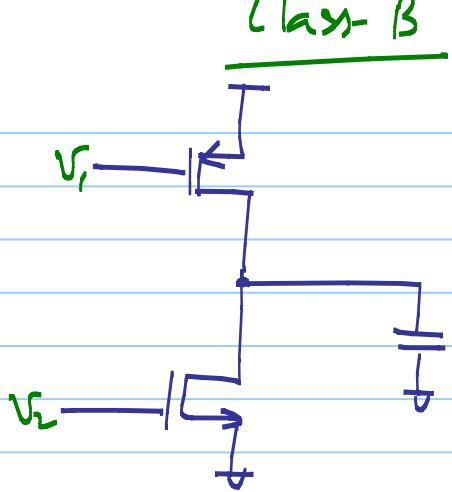
Figure 24.8 Calculating the frequency response of the op-amp.



$$\frac{dV_{out}}{dt} = \frac{I_2}{C_L}$$

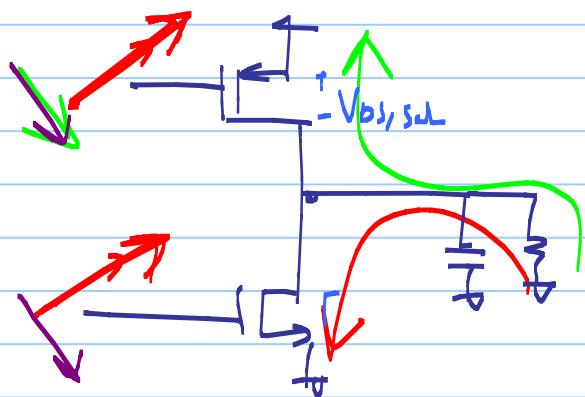
Class-A stage



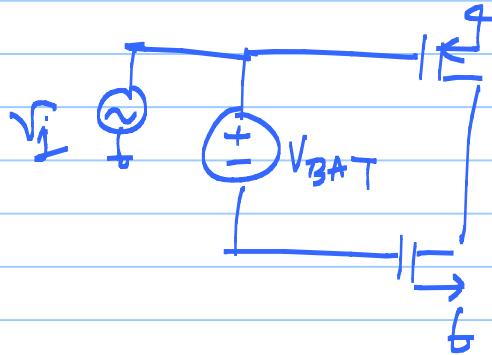


either pull-up or pull-down is
on at a time

Class- AB



near rail-to-rail
output swing



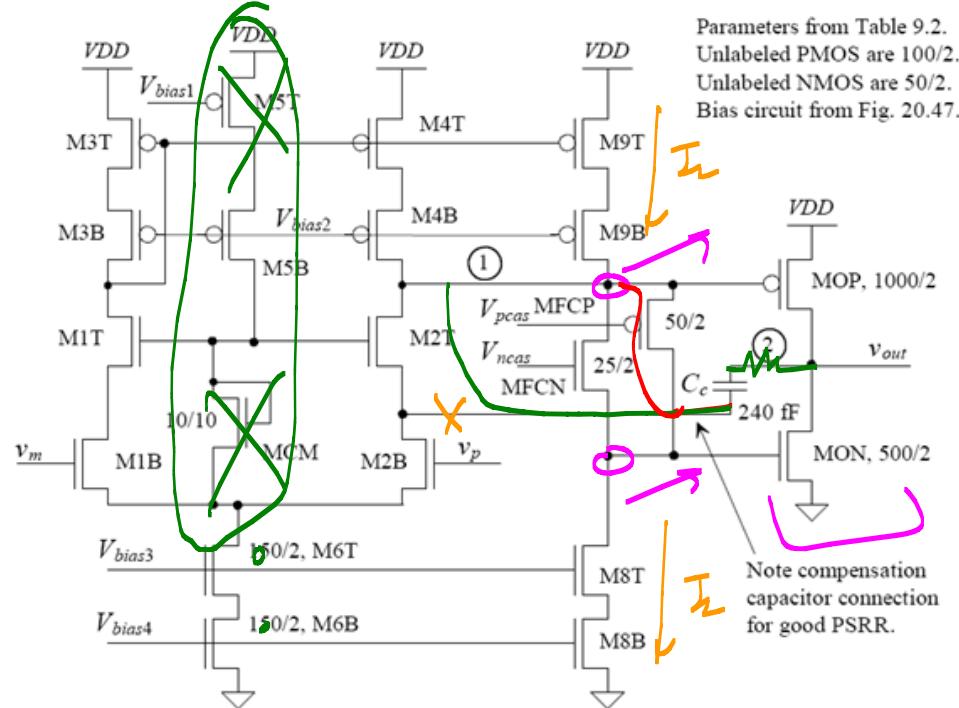


Figure 24.29 A CMOS op-amp with output buffer.

Biasing from Fig. 20.47.
Sizes given in Table 9.2.

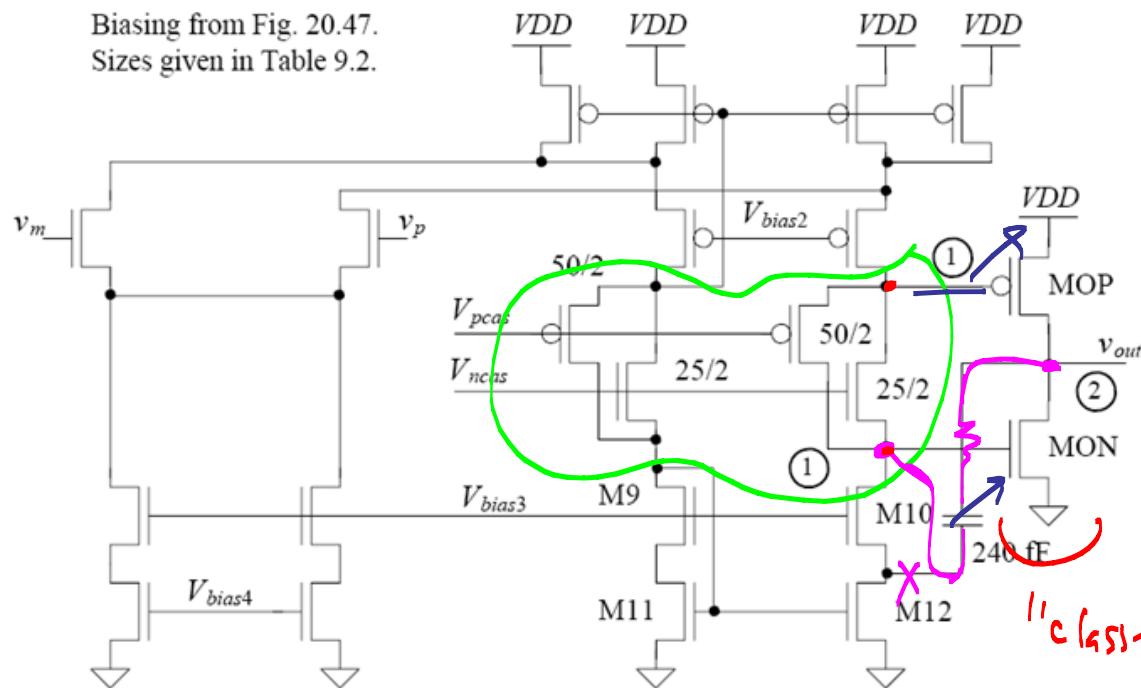


Figure 24.44 Folded-cascode op-amp with class AB output buffer.

Floating CM
is embedded
in the
fe structure

"class-AB o/p stage"

