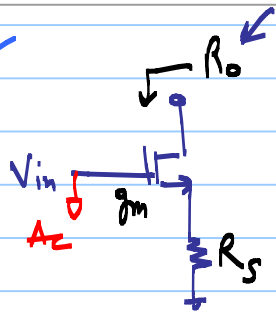
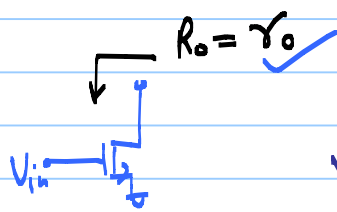


ECE 511 - Lecture 11

Note Title

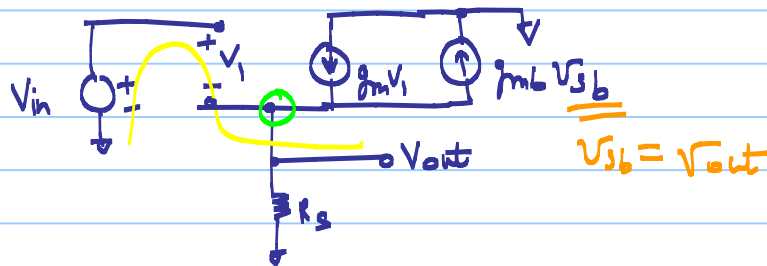
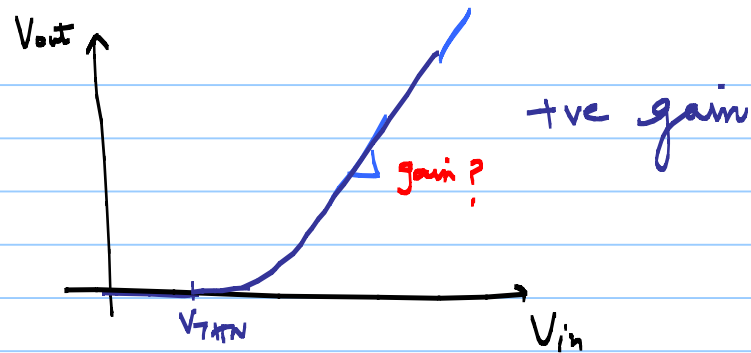
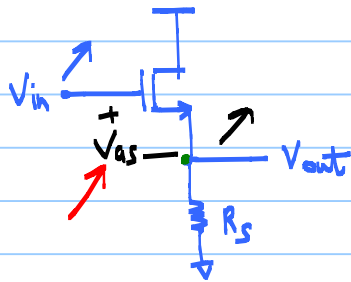
2/26/2013



$$R_o = (1 + (g_m + g_{mb}) R_s) r_o$$

$$\approx (g_m r_o) R_s \leftarrow \text{moderate} \leftrightarrow \text{high resistance}$$

Source follower (common Drain Amplifier)



$$\lambda = 0$$

$$V_{in} - V_1 = V_{out} \rightarrow \textcircled{1}$$

$$g_m V_1 - g_m V_{out} = \frac{V_{out}}{R_S} \rightarrow \textcircled{2}$$

$$\textcircled{1} \Delta \textcircled{2} \quad g_m (V_{in} - V_{out}) - g_m V_{out} = \frac{V_{out}}{R_S}$$

$$\Rightarrow v_{out} (g_m + g_{mb} + \frac{1}{R_S}) = g_m v_{in}$$

$$\Rightarrow A_v = \frac{v_{out}}{v_{in}} = \frac{g_m R_S}{1 + (g_m + g_{mb}) R_S}$$

+ve non-inverting gain

$$= \frac{g_m R_S}{1 + (\eta + 1) g_m R_S}$$

$$= \frac{1}{\eta + 1 + \frac{1}{g_m R_S}}$$

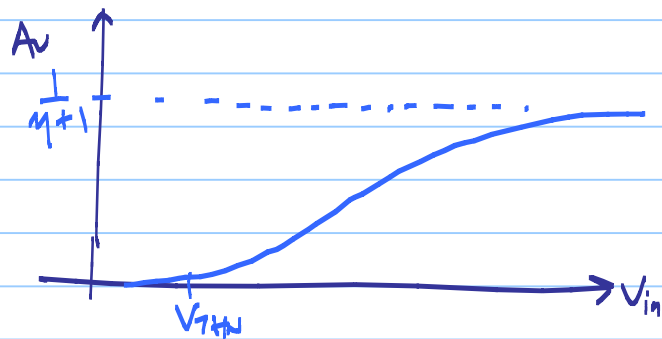
$$\approx \left(\frac{1}{\eta + 1} \right) \text{ for } g_m R_S \gg 1$$

$$\downarrow < 1$$

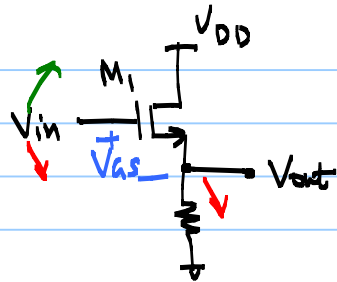
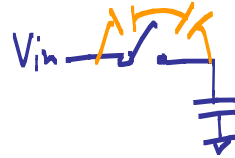
$$0.7 - 0.8$$

$$\therefore g_{mb} = \eta g_m$$

$$\eta < 1$$



* Will M_1 ever triode?



$$V_{DS} = V_{DD} - V_{out}$$

$$= V_{DD} - (V_{in} - V_{gs})$$

$$= V_{DD} - V_{in} + V_{gs}$$

$$= V_{gs} - (V_{in} - V_{DD})$$

$$V_{DS} \geq V_{DS, sat}$$

$$= V_{gs} - V_{thn}$$

M_1 Triodes:
for

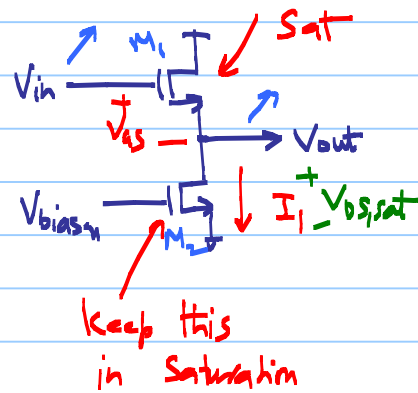
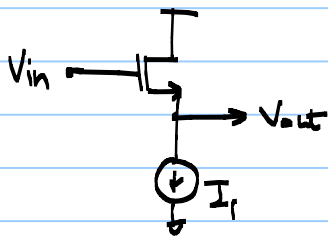
$$V_{DS} < V_{gs} - V_{thn}$$

$$\cancel{V_{gs}} - V_{in} + V_{DD} < \cancel{V_{gs}} - V_{thn}$$

$$\Rightarrow V_{in} > V_{DD} + V_{thn}$$

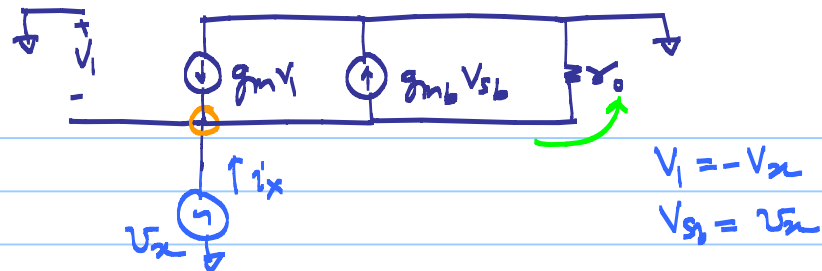
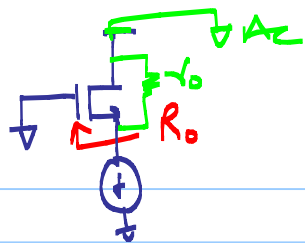
Never happens!

* I_D depends upon $V_{in} \rightarrow$ DC level
 $\Rightarrow I_D = f(V_{in})$
 $g_m = f(V_{in}) \rightarrow$ non-linear



I_1 biases M_1

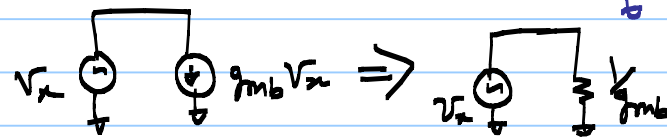
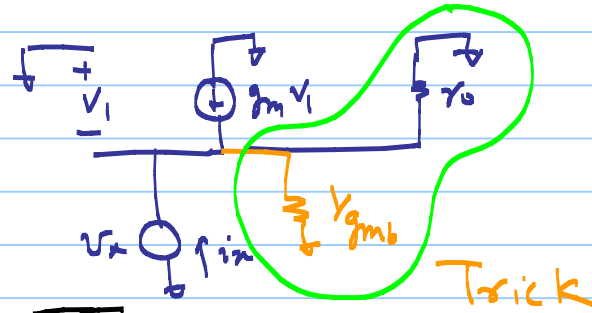
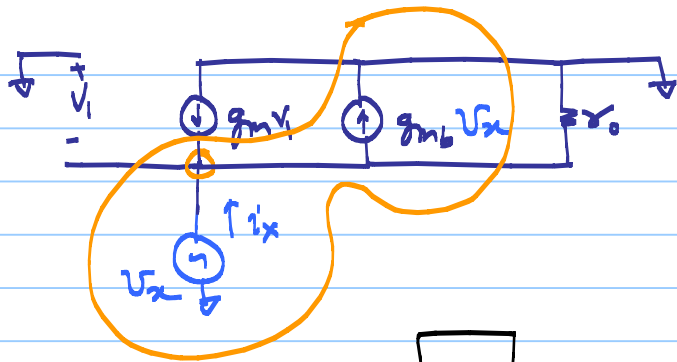
$V_{in} \geq V_{gs} + V_{DS,sat_2}$
 $\uparrow$ input headroom issue

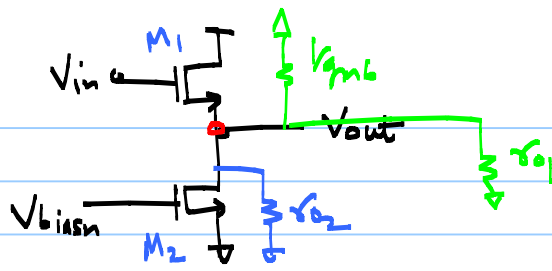


$$i_x - g_m v_x - g_{mb} v_x - \frac{v_x}{r_o} = 0$$

$$\Rightarrow R_o = \frac{v_x}{i_x} = \frac{1}{g_m + g_{mb} + \frac{1}{r_o}} = \frac{1}{g_m} \parallel \frac{1}{g_{mb}} \parallel r_o$$

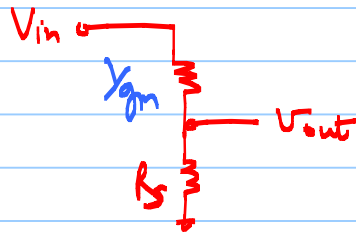
$$\approx \frac{1}{g_m} \leftarrow R_{out} \text{ is small}$$





$$R_S = \frac{1}{g_{m1}} \parallel r_{o1} \parallel r_{o2}$$

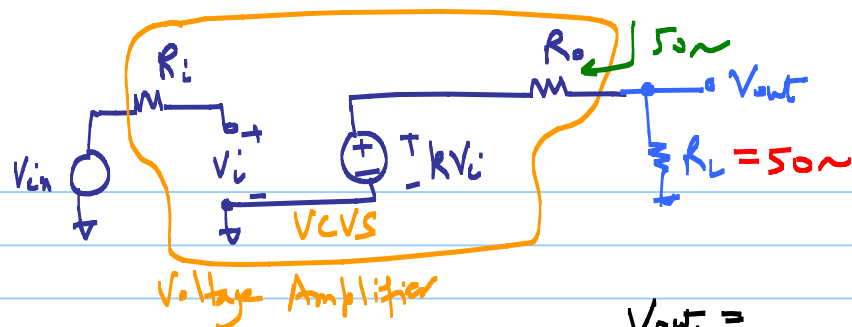
Resistance connected to source



"Voltage Divider Analogy"

$$A_v = \frac{\frac{1}{g_{m1}} \parallel r_{o1} \parallel r_{o2}}{\frac{1}{g_{m1}} + \frac{1}{g_{m1}} \parallel r_{o1} \parallel r_{o2}}$$

for a very large g_{m1} , $A_v \rightarrow 1$



$$V_{out} = kV_{in} \cdot \left(\frac{R_L}{R_o + R_L} \right)$$

$$= \frac{kV_{in}}{\left(\frac{R_o}{R_L} \right) + 1}$$

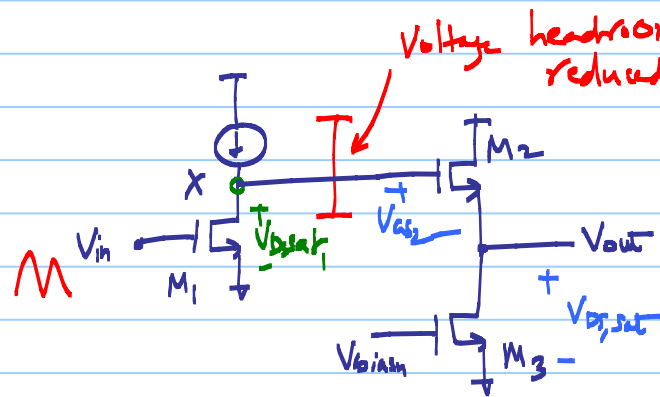
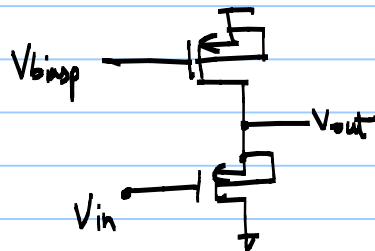
$$\approx kV_{in} \quad \text{for } R_o \ll R_L$$

Issues as a Voltage Buffer

- ① Body effect, $V_{thw} = f(V_{in}) \rightarrow$ non-linearity
- ② DC shift $\rightarrow$ headroom issues
- ③ A_v drops significantly for low R_L .

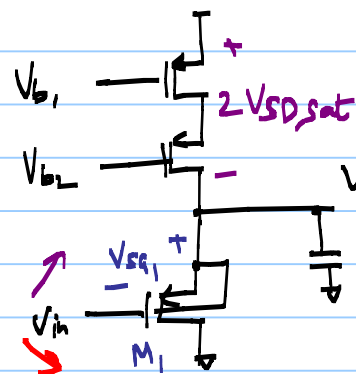
Body - effect

PMOS SF



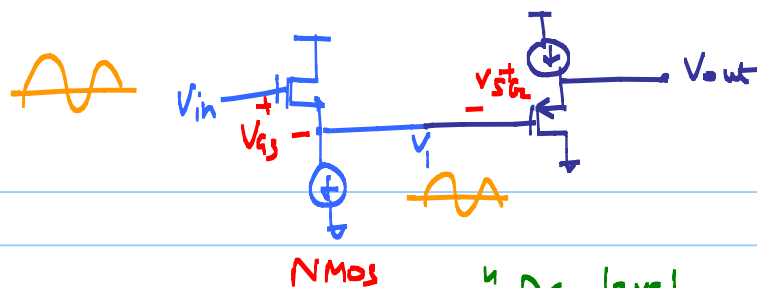
$$V_x > V_{ds2} + V_{gs2}$$

"DC level shift"



$$V_{out} = V_{in} + V_{sg1}$$

* find allowed range of V_{in}



NMOS

"DC level manipulations"

$$-V_{thp} \leq V_{in} \leq V_{DD} - \underbrace{2V_{SD,sat} - V_{sg1}}_{\text{headroom loss}}$$

↑ Triode condition