

ECE 5411 - Review Session 1

Note Title

3/6/2011

- * IV curves
- * Small signal parameters (g_m , r_o , g_{mb})
Sketches
- * R_{CL} Triode
Saturation
- * Body-effect $\Rightarrow g_{mb}$

* Small-signal $\rightarrow$ Taylor Series

* $g_m \rightarrow$ vs V_{ov} , $\frac{W}{L}$ constant

$L \rightarrow$ vs I_0 , w/L constant

→ vs V_{ov} , I_D constant

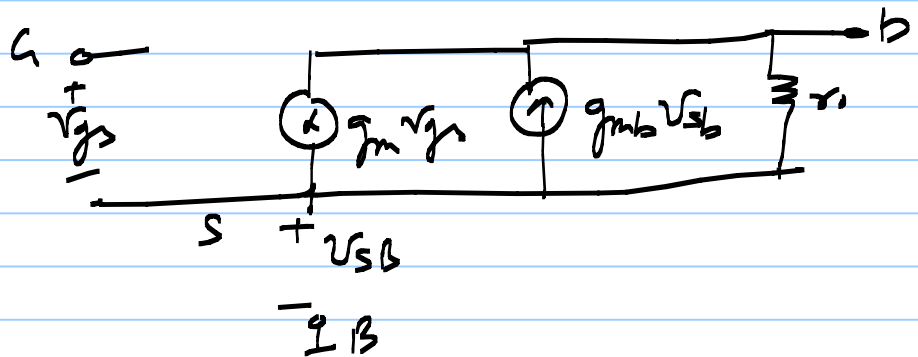
$$\beta_n (V_{GS} - V_{THn})$$

$$\sqrt{2 \beta_n I_D}$$

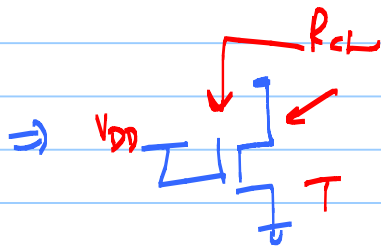
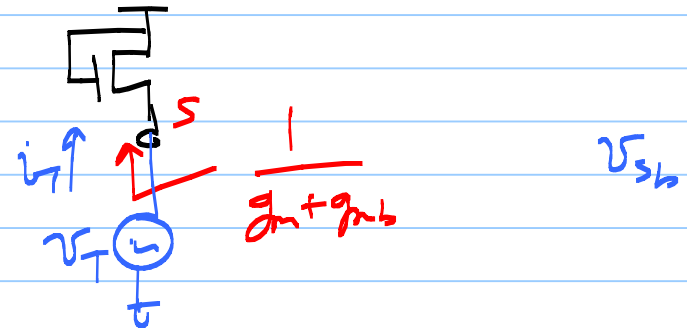
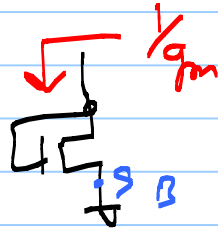
$$\frac{2I_0}{V_{OV}}$$

$$x \quad r_0 = \frac{1}{\lambda I_{D, \text{sat}}}$$

* Small-signal model (low-frequency)



⇒ Diode connected MOSFET
 L as a load



deep triode

$V_{DS} \ll V_{DS, \text{sat}}$

$$R_{ch} = \frac{1}{K \mu_n \frac{W}{L} (V_{GS} - V_{TH})} = \frac{1}{g_m}$$

* Example 9.5

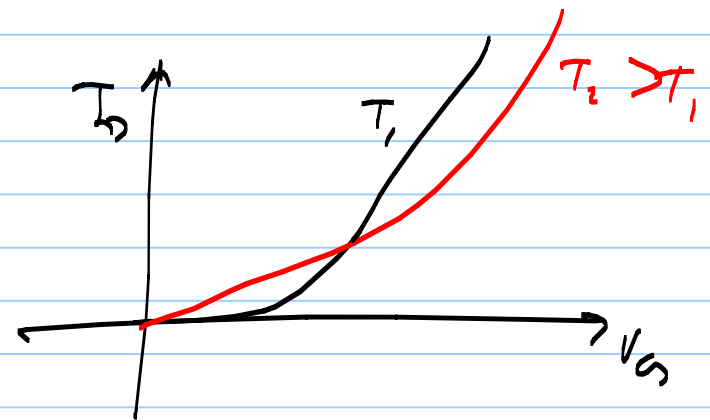
* f_T Derivation & interpretation

$$f_T \propto \frac{V_{ov}}{L^2} \quad \text{Long channel}$$

$$\propto \frac{V_{ov}}{L} \quad \text{Short channel}$$

* g_m behavior in subthreshold.

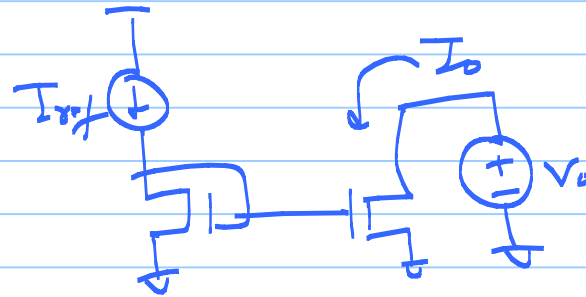
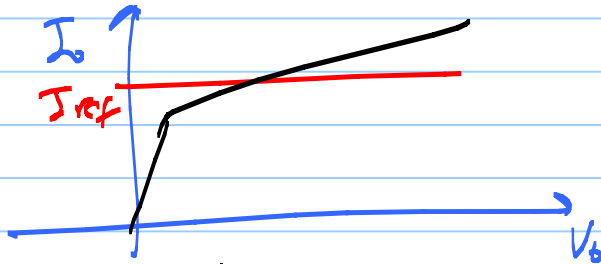
* Temp behavior I_D V_{ov}



Chap 20

* Additional HW problems

$$\rightarrow \frac{I_{out}}{I_{ref}} = \frac{(W/L)_2}{(W/L)_1} \quad \Rightarrow \text{Sketches}$$



$\rightarrow$ Cascoded current mirror

* Mismatch analysis of the current mirror

$$\frac{\Delta I_D}{I_D} = \frac{\Delta(W/L)}{W/L} - \frac{2 \Delta V_{TH}}{(V_{DS} - V_{TH})}$$

× BMR → derive I_{ref} , V_{DS} , g_m
for any variant

× Design Start-up circuits

× $V_{DD_{min}}$ for the BMR

× Short-channel BMR design

↳ error amp to regulate the drains

× Temp behavior/analysis of the BMR.

* Cascoding

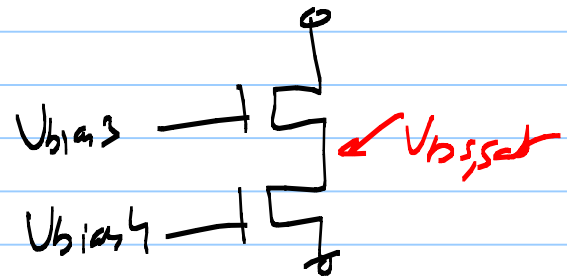
↳ $R_o = g_m r_o^2 \rightarrow$ small-signal analysis

* ↳ wide-swing cascode biasing

↳ MWS $\Rightarrow 2V_{D, \text{sat}} + V_{THN}$

$\Rightarrow$ Derive $\frac{W}{L}$ for MOS devices

$\Rightarrow$ Triple cascode HW problem



* Regulated Drain current mirror

* $R_o \approx \frac{g_m r_o^2 A}{1}$

* find bias voltages in such circuits.

* Triple cascoding + Regulated Drain

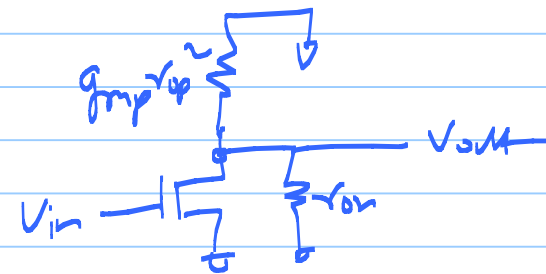
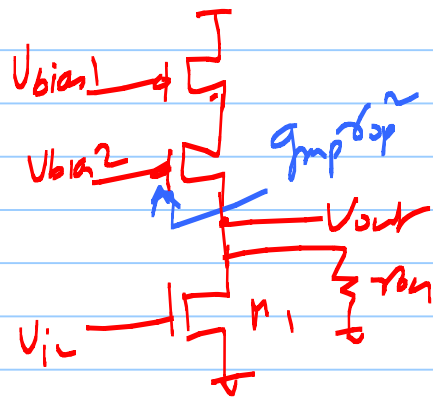
~~★ ★~~ * Cascoded Biasing 20.43 / 20.47

How to generate
bias voltages. $\begin{pmatrix} V_{bias1} & V_{bias2} \\ V_{bias2} & V_{bias4} \end{pmatrix}$

Chapter 21

⇒ Hw 6

* Common Source AC gains



$A_v =$

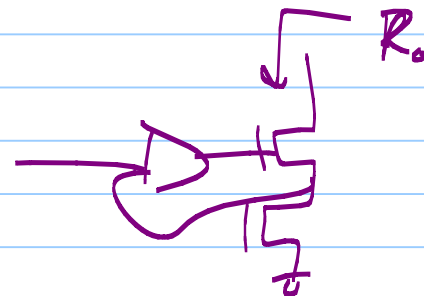
$$V_{out} = -g_{mn}(Z_D)$$

$$= -g_{mn}(g_{mp}r_{op} \parallel r_{on})$$

$$A_v = - \frac{\text{Impedance "attached" to the drain}}{\text{impedance looking into the source}}$$

x Source degenerated CS Amplifier.

↳ "linearity"



$$V_{gs} = -(A+1) i_T r_o \longrightarrow \textcircled{1}$$

$$i_T - g_m v_{gs} - \frac{(v_T - i_T r_o)}{r_o} = 0 \rightarrow (2)$$

$$\underline{i_T} + g_m(A+1)i_T r_o - \frac{v_T}{r_o} + \underline{i_T} = 0$$

$$i_T [2 + g_m(A+1)r_o] = \frac{v_T}{r_o}$$

$$R_o = \frac{v_T}{i_T} = \frac{[g_m r_o (A+1) + 2] r_o}{\leq \underline{g_m r_o^2 A}}$$

$$A \gg 1$$

$$g_m r_o \gg 1$$