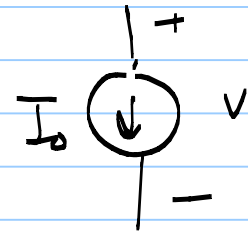
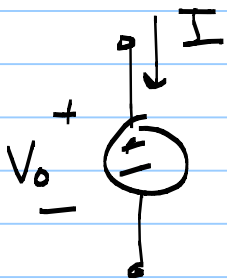
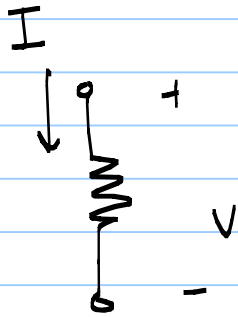
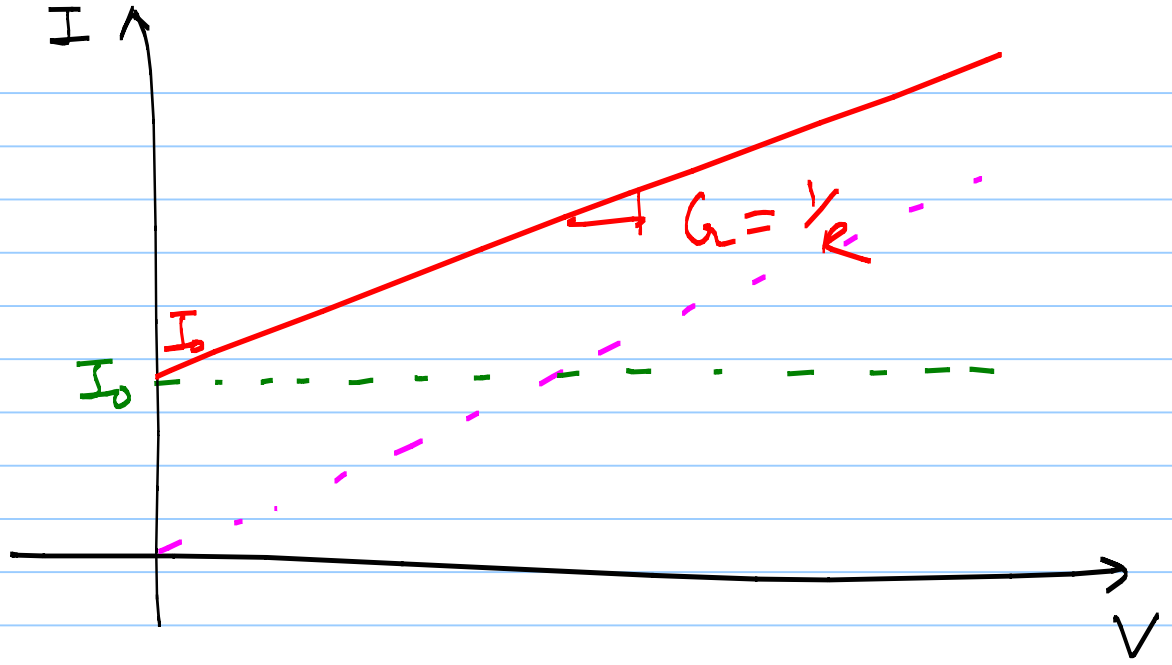
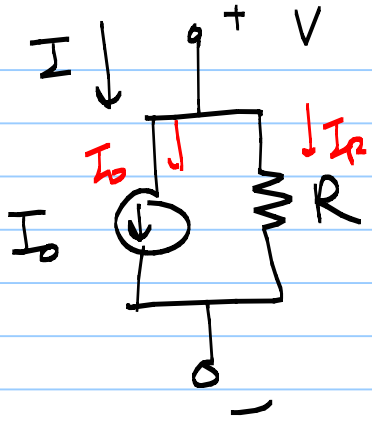


ECE511: Lecture 1.

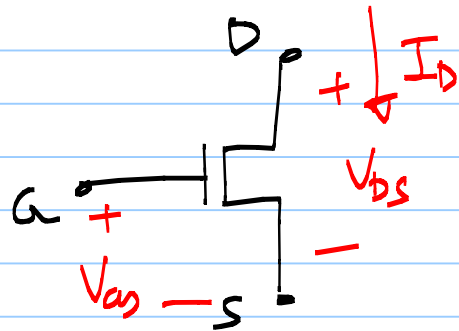
Note Title

1/19/2011





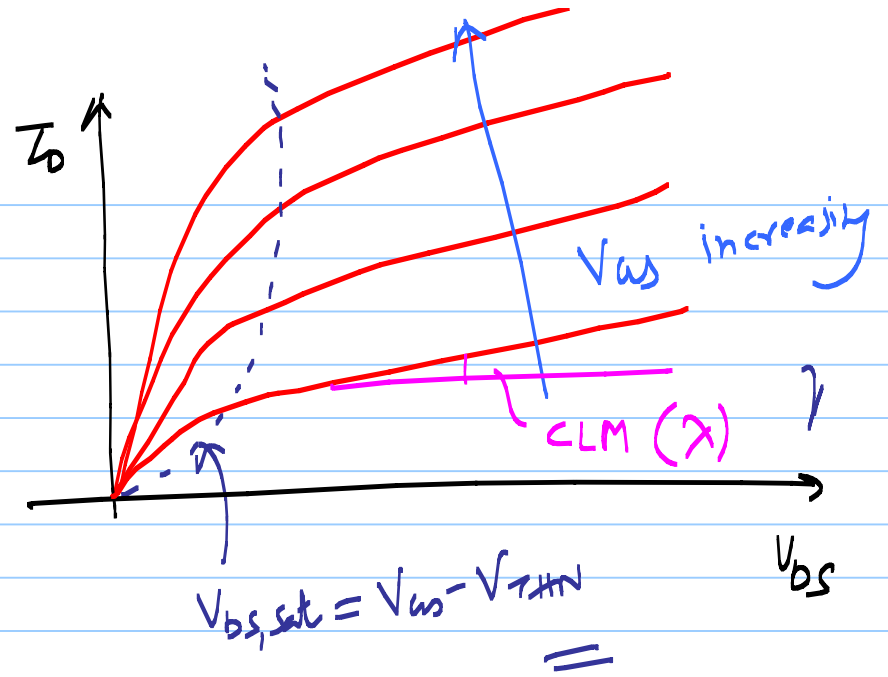
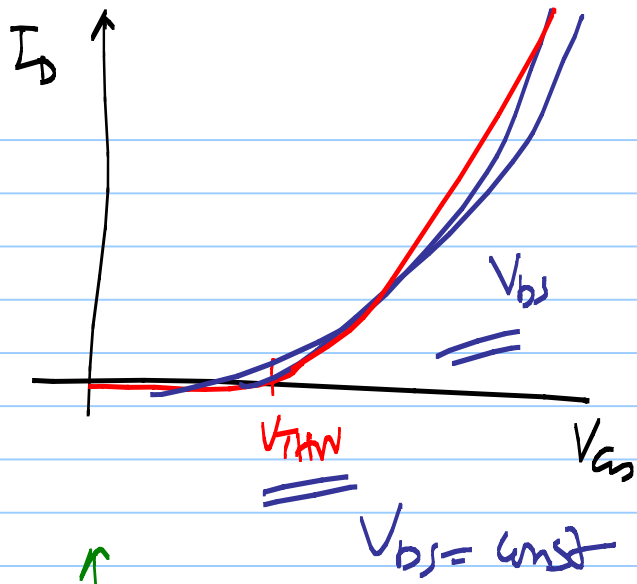
MOSFET I-V characteristics



$V_{GS}, V_{DS} \rightarrow$ independent variable

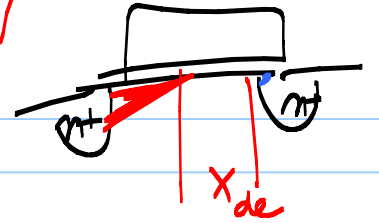
$I_D \leftarrow$ dependent

$$I_D = f(V_{GS}, V_{DS})$$



① Saturation:

$$V_{DS} \geq V_{DS, sat} \equiv V_{GS} - V_{THN}$$



① Cutoff: $V_{GS} < V_{THN}$

② Triode: $V_{DS} < V_{DS, sat}$

$$I_D = K'_n \frac{W}{L} \left[(V_{GS} - V_{THN}) V_{DS} - \frac{V_{DS}^2}{2} \right]$$

③ Saturation: $V_{DS} > V_{DS, sat}$

$$I_D = K'_n \frac{W}{2L} (V_{GS} - V_{THN})^2 [1 + \lambda (V_{DS} - V_{DS, sat})]$$

(Long channel)

* $V_{DS, sat} = V_G - V_{THN}$

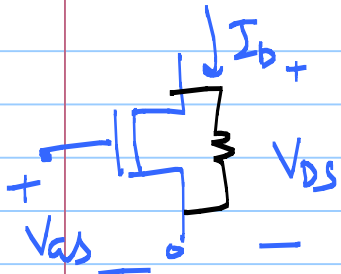
* gate 'overdrive' voltage

* excess gate voltage

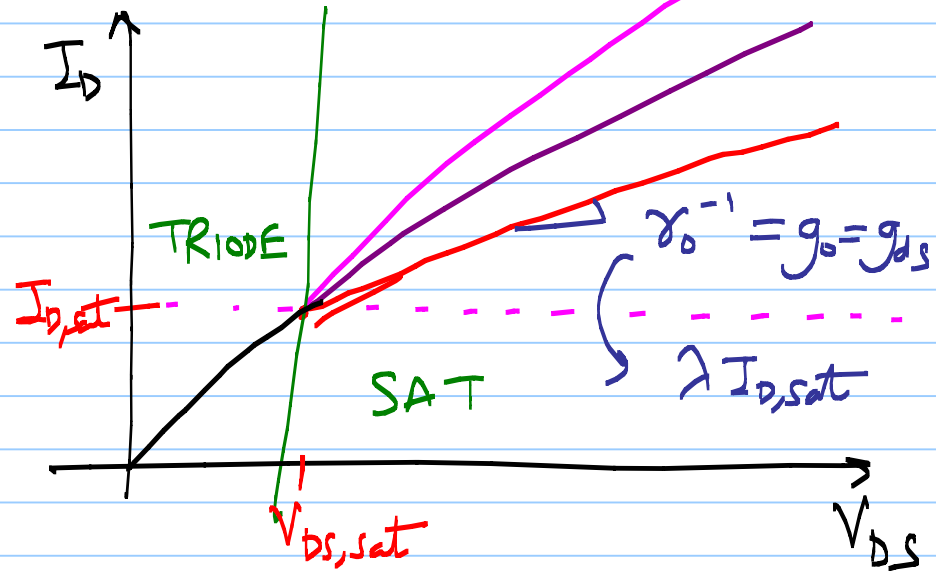
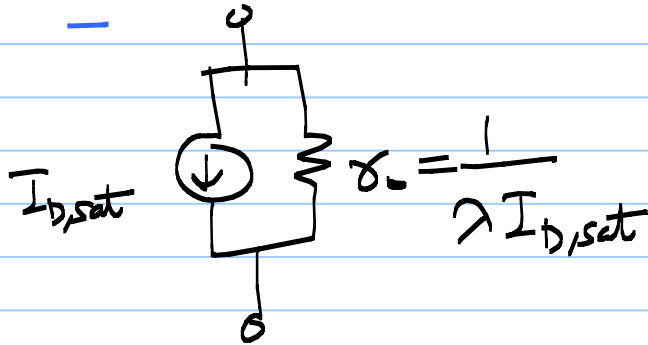
Saturation

$$I_D = I_{D,sat} + I_{D,sat} \cdot \lambda (V_{DS} - V_{D,sat})$$

$$I_{D,sat} = \frac{K_p}{2} \frac{W}{L} (V_{GS} - V_{TH})^2$$



$$\lambda_o = \left(\frac{\partial I_D}{\partial V_{DS}} \right)^{-1} = \frac{1}{\lambda I_{D,sat}}$$



$\lambda \uparrow \Rightarrow r_o \downarrow$ output resistance of

the MOSFET decreases as

the CLM effect increases

Triode :

$$V_{DS} \ll V_{DS, sat}$$

$$I_D = K_p \frac{W}{L} \left[\underbrace{(V_{GS} - V_{THN})}_{V_{DS, sat}} V_{DS} - \cancel{\frac{V_{DS}^2}{2}} \right]$$

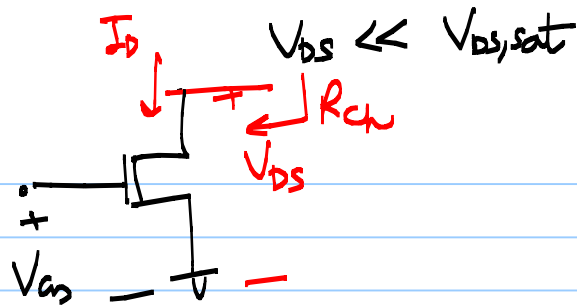
deep Triode

$$I_D \approx K_p \frac{W}{L} \underbrace{(V_{GS} - V_{THN})}_{V_{DS, sat}} V_{DS}$$

* Linear dependence on V_{DS}

$$R_{ch} = \left(\frac{\partial I_D}{\partial V_{DS}} \right)^{-1} = \frac{1}{K_p \frac{W}{L} (V_{GS} - V_{THN})}$$

* I_D depends on V_{GS}

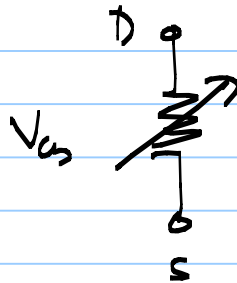


$$R_{ch} = \frac{1}{k_n \frac{W}{L} (V_{gs} - V_{THN})}$$

x for fixed $V_{gs} = \text{const}$

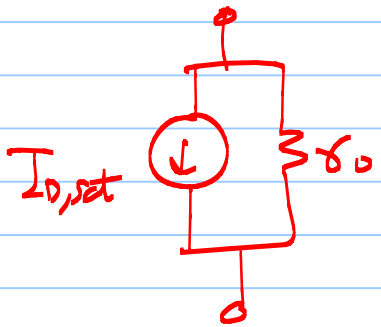
R_{ch} is linear wrt V_{ds}

$$R_{ch} \propto \frac{1}{(V_{gs} - V_{THN})}$$

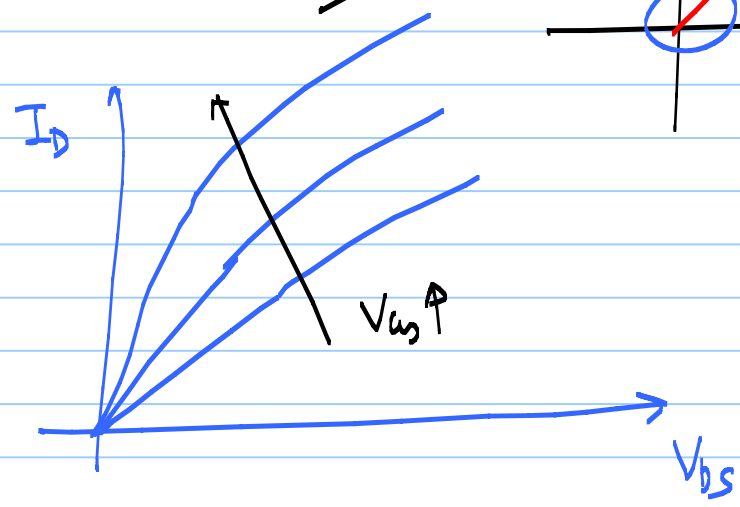
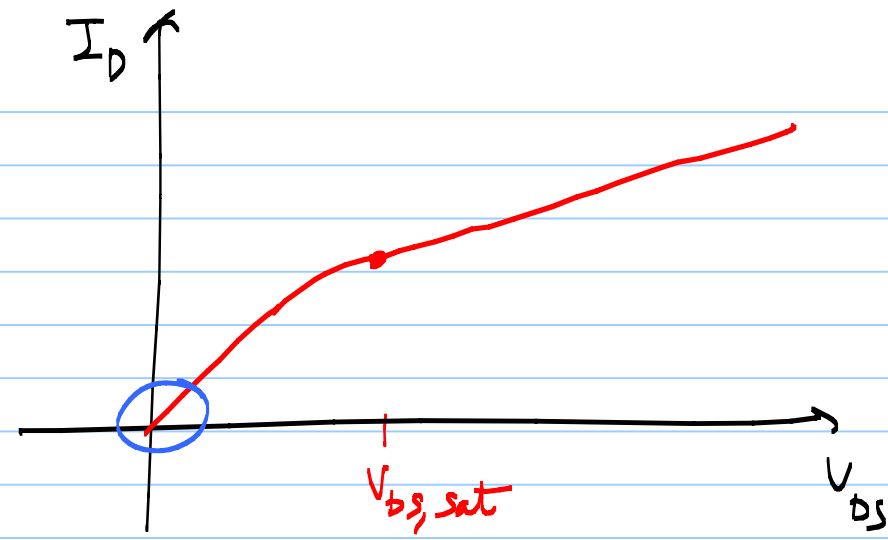
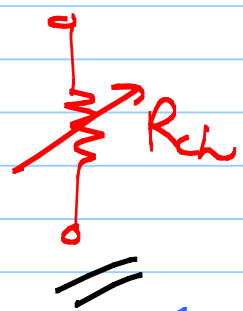


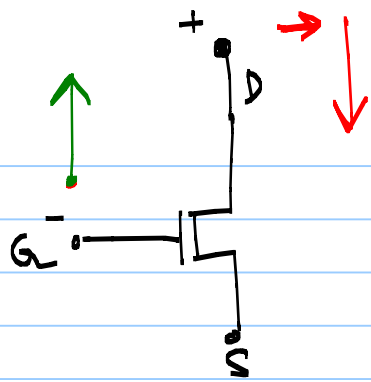
In triode, a MOSFET acts like a resistor, whose resistance is controlled by the gate overdrive voltage

SAT



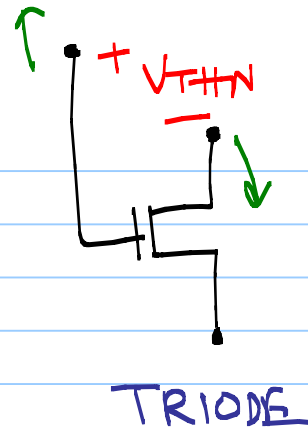
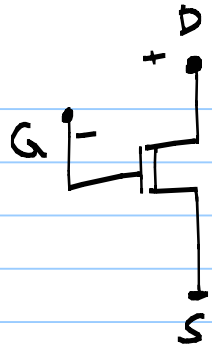
TRIODE





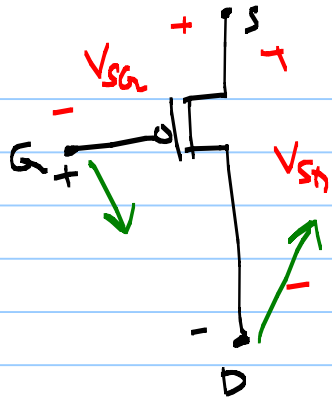
SATURATION

$$V_{DS} > V_{DS,sat}$$



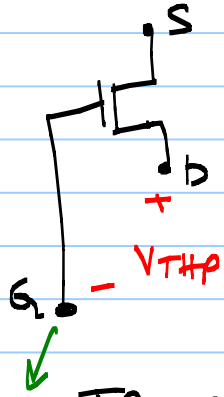
x The NMOS can have the gate at a max of V_{THN} higher than the drain before it triodes.

PMOS



SAT

$$V_{SD} > V_{SD,sat} = V_{SG} - V_{THP}$$



TRIODE

for saturation, gate can't be lower than the drain by more than V_{THP} .