

Loop Stability Analysis

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- The STB analysis linearizes the circuit about the DC operating point and computes the loop-gain, gain and phase margins (if the sweep variable is frequency), for a feedback loop or a gain device [1].
- Refer to the Spectre Simulation Refrence [1] and [2] for details.



Example Single-ended Opamp Schematic





STB Analysis Test Bench



- Pay attention to the **iprobe** component (from analogLib)
 - Acts as a short for DC, but breaks the loop in stb analysis
- Place the probe at a point where it completely breaks (all) the loop(s).



- Annotating the node voltages and DC operating points of the devices helps debug the design
 - Check device gds to see if its in triode or saturation regions



Simulation Setup



Always have dc analysis on for debugging purpose



Bode Plot Setup

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OK Cancel Help	10 Main Form		

• Results->Direct Plot-> Main Form

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Open Loop Response Bode Plots



- Here, *f*_{un}=152.5 MHz, PM=41.8°
- Try to use the stb analysis while the circuit is in the desired feedback configuration
 - Break the loop with realistic DC operation points



Transient Step Response Test Bench



- Transient step-response verifies the closed-loop stability
- Use small as wells as large steps for characterization
- iprobe acts as a short (can remove it)



Small Step Response



- Observe the ringing (PM was 41°)
 - Compensate more





• Note the slewing in the output





[1] Spectre User Simulation Guide, pages 160-165

http://www.designers-guide.org/Forum/YaBB.pl?num=1170321868

[2] M. Tian, V. Viswanathan, J. Hangtan, K. Kundert, "Striving for Small-Signal Stability: Loopbased and Device-based Algorithms for Stability Analysis of Linear Analog Circuits in the Frequency Domain," Circuits and Devices, Jan 2001.

http://www.kenkundert.com/docs/cd2001-01.pdf

[3] https://secure.engr.oregonstate.edu/wiki/ams/index.php/Spectre/STB