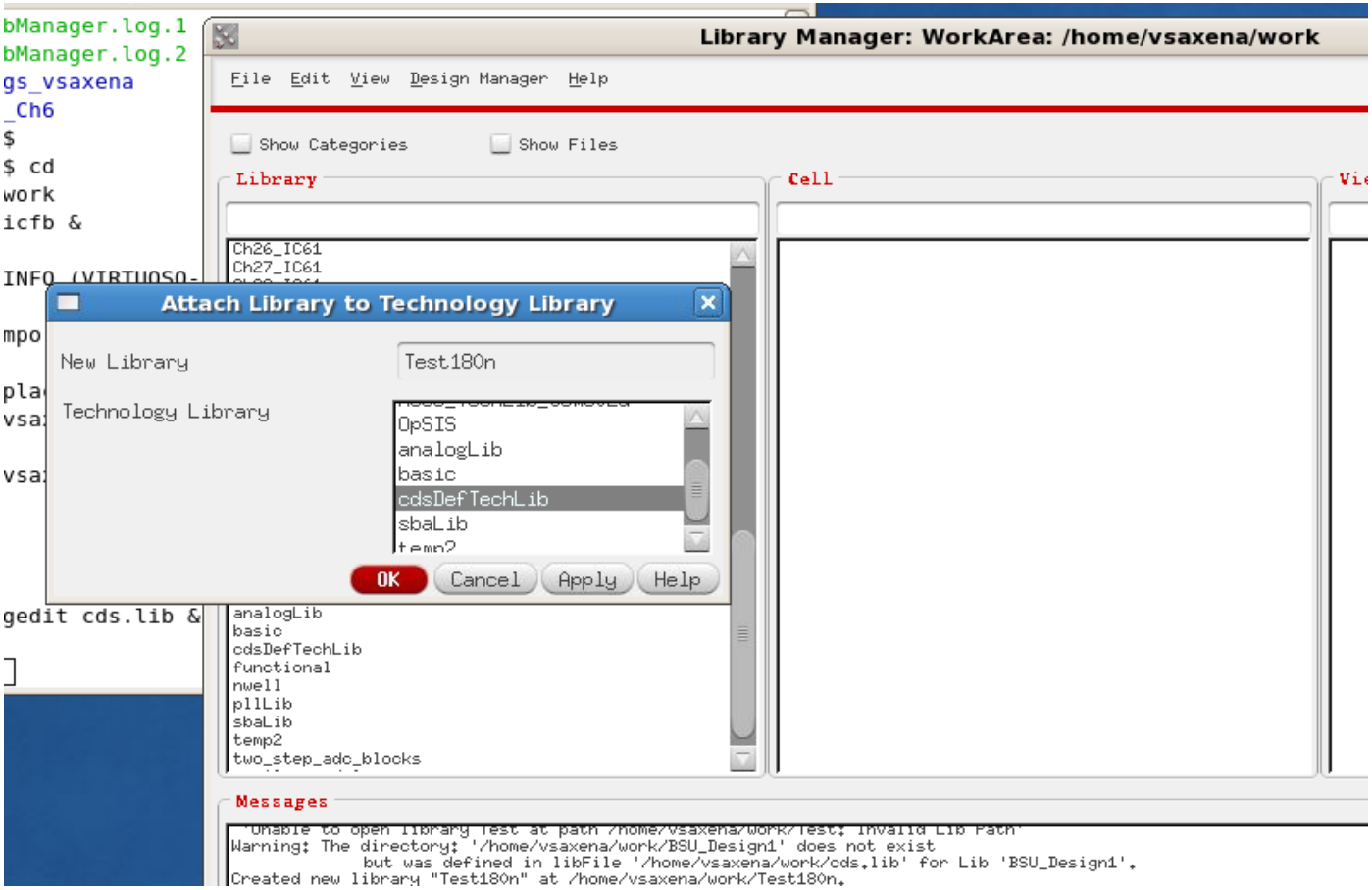
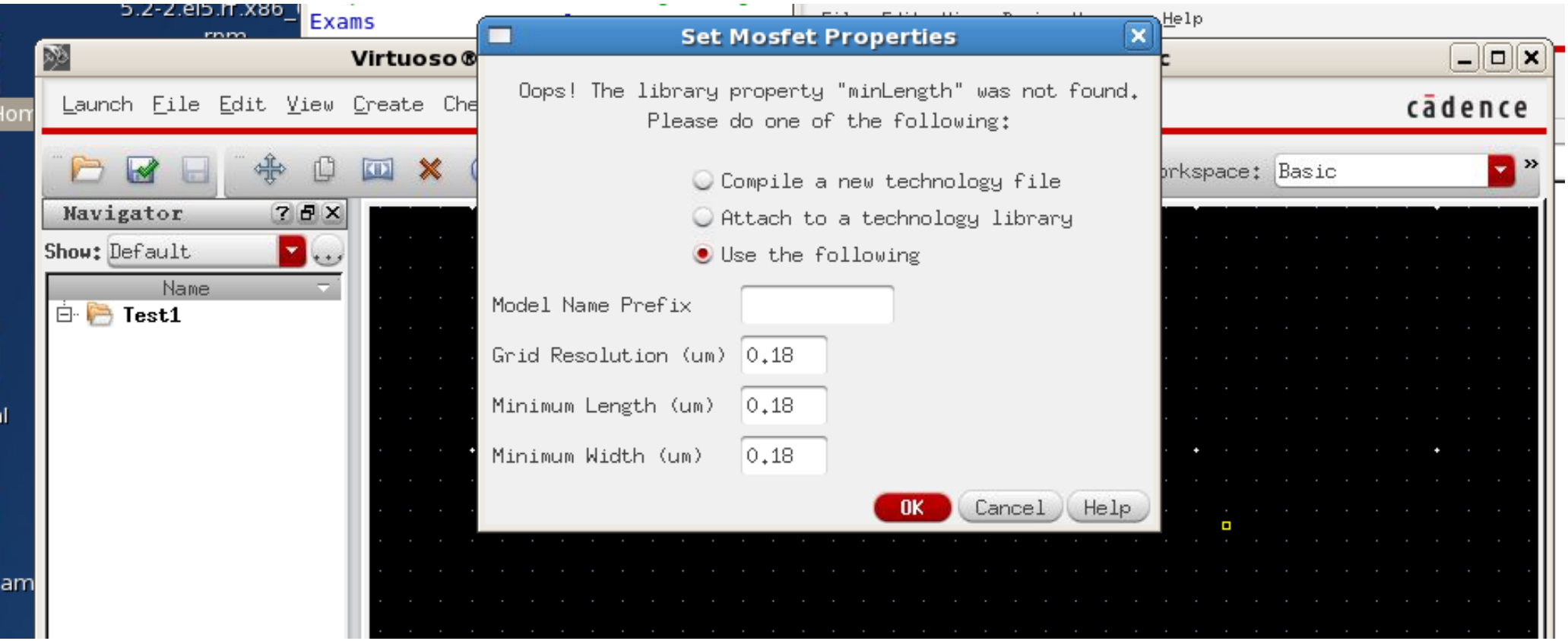
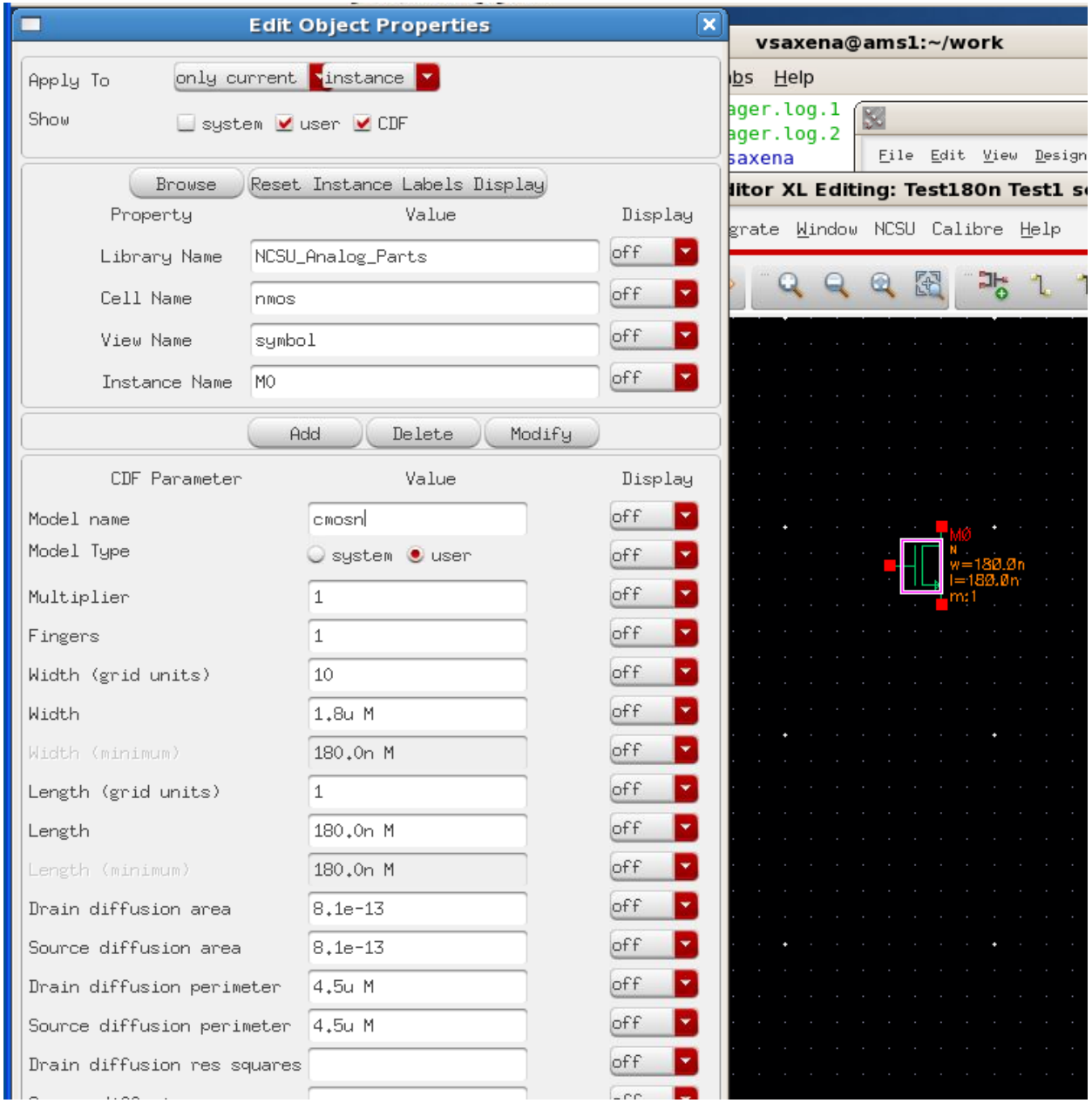
180n CMOS Simulation Setup



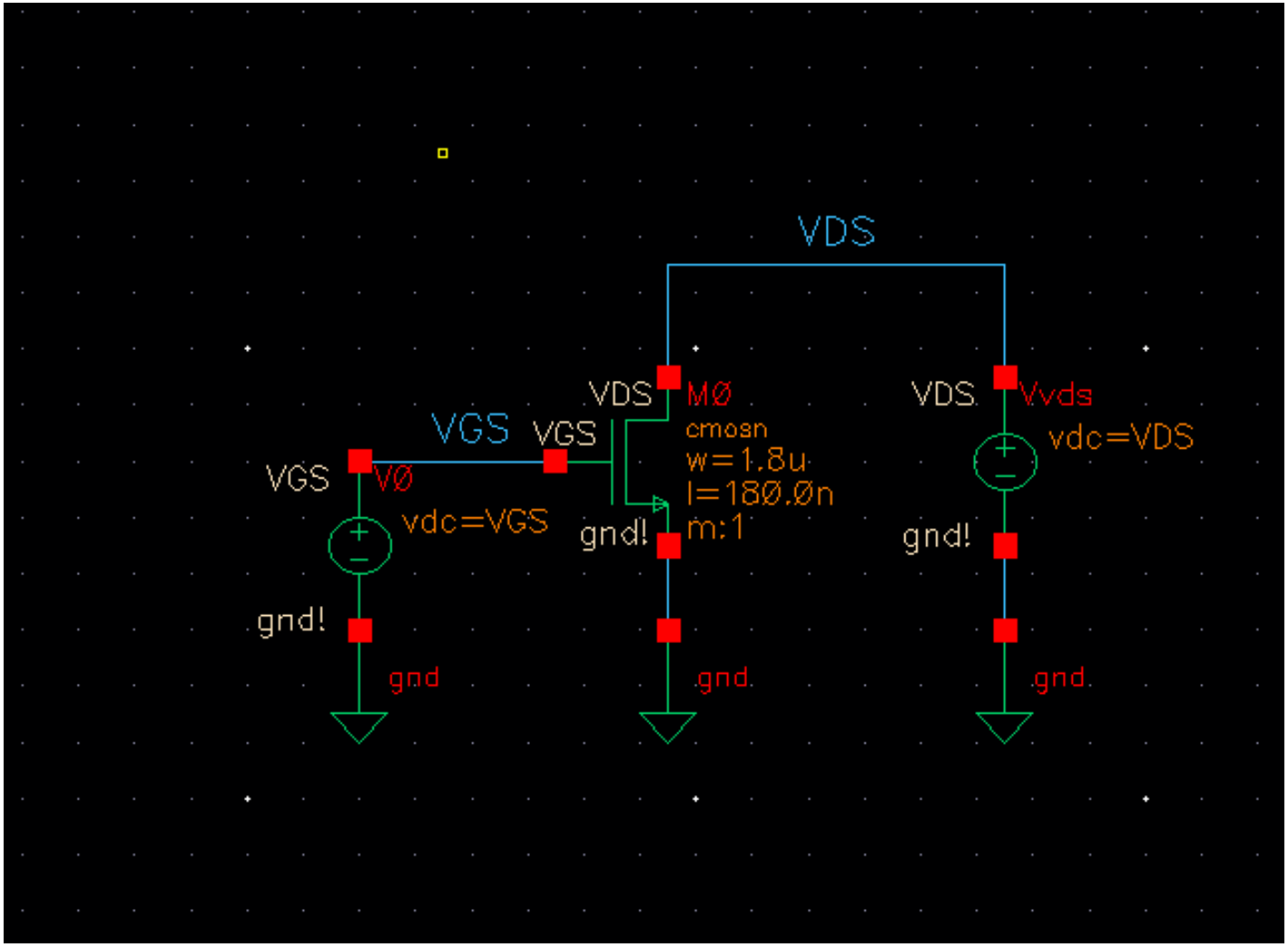
- Create a new library (Test180n here)
- Since we do not have a PDK for the 180n CMOS process, attach “cdsDeftechLib” as the techlib.



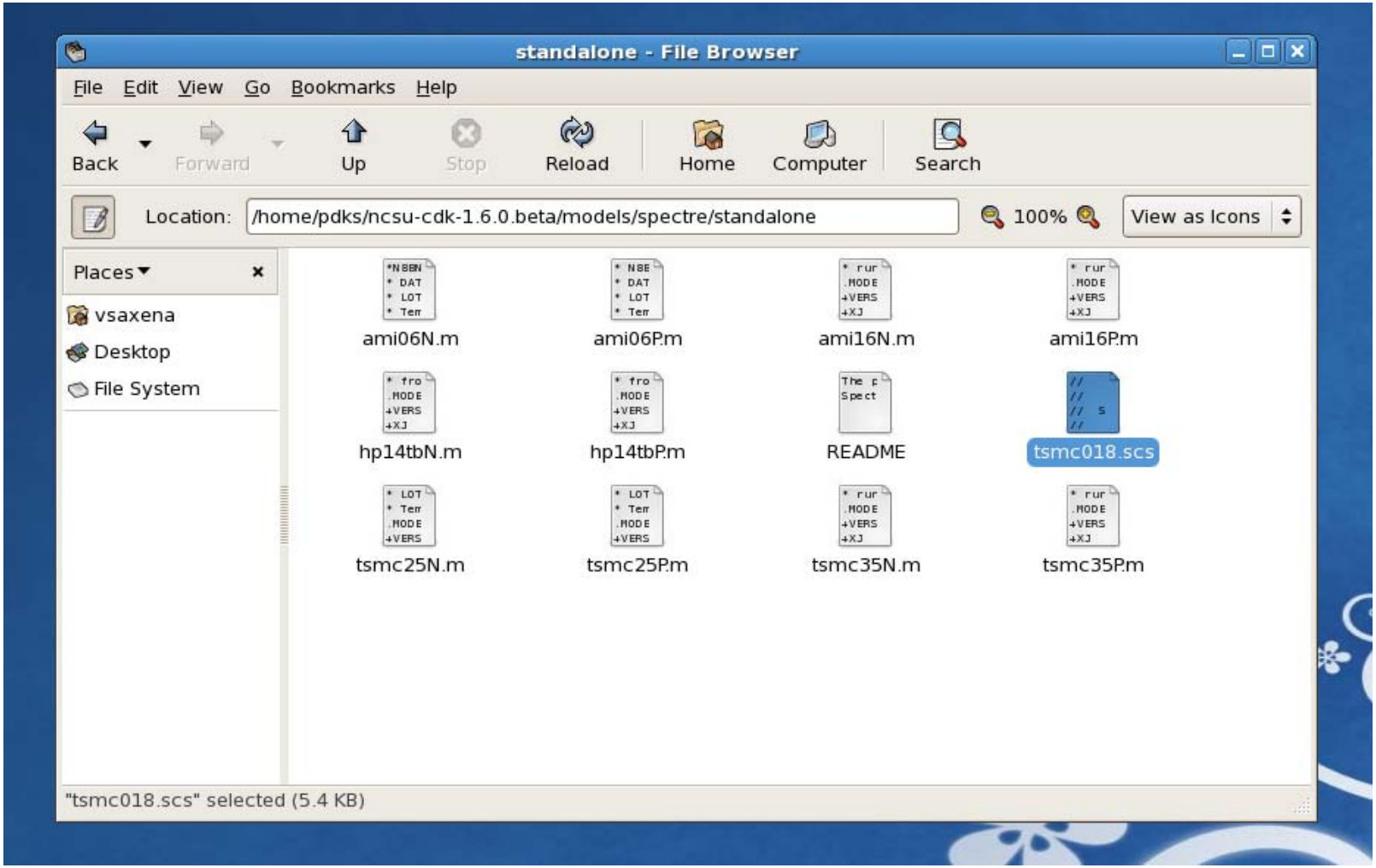
- Create a new schematic view
- You will be prompted to set the Mosfet minimum dimensions
- Enter 0.18 for all values (180n process)



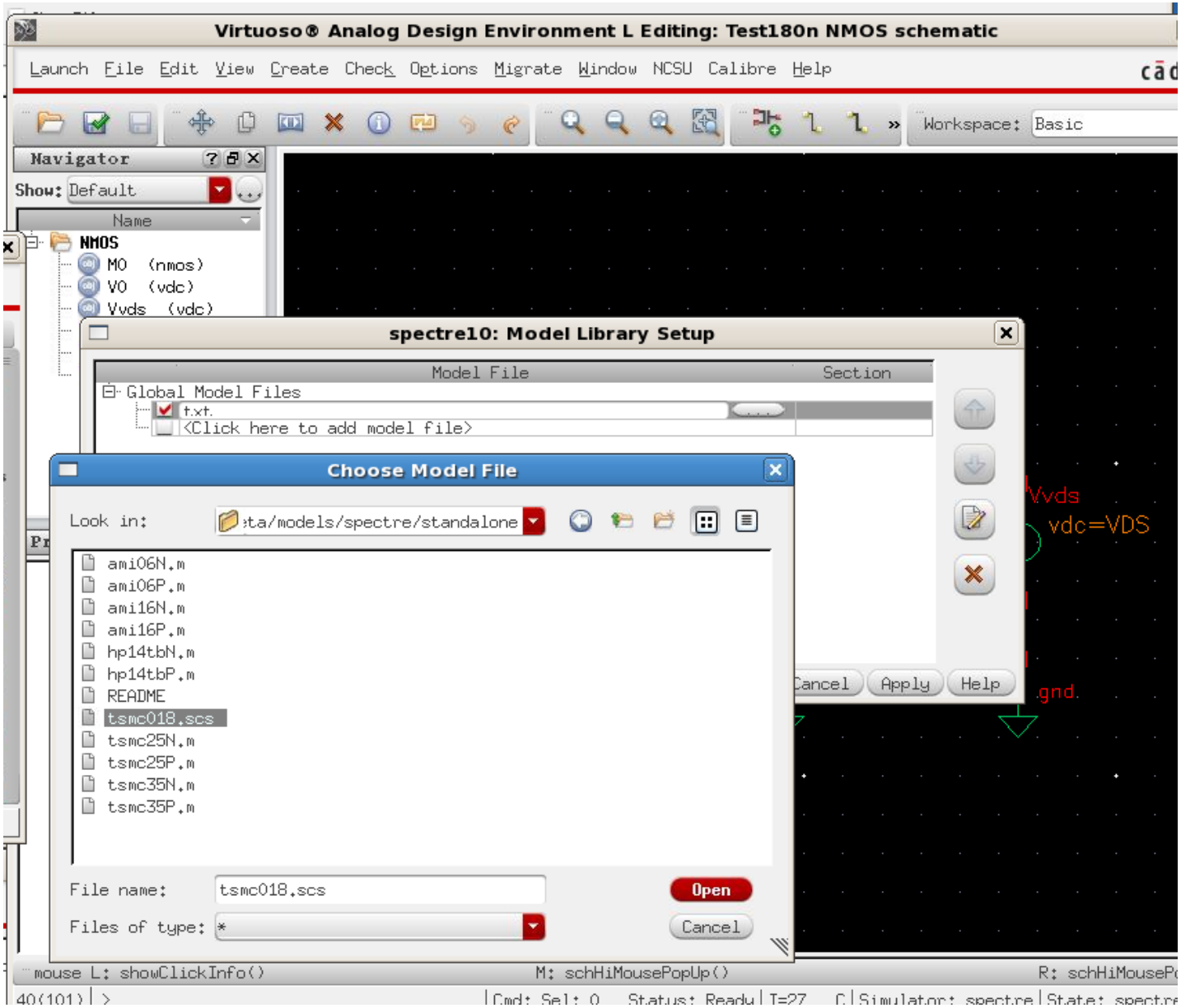
- Create NMOS instance with desired W and L.
- Make sure you set the model name to **cmosn** or **cmosp**
- Use setting of model type=user for changing this parameter



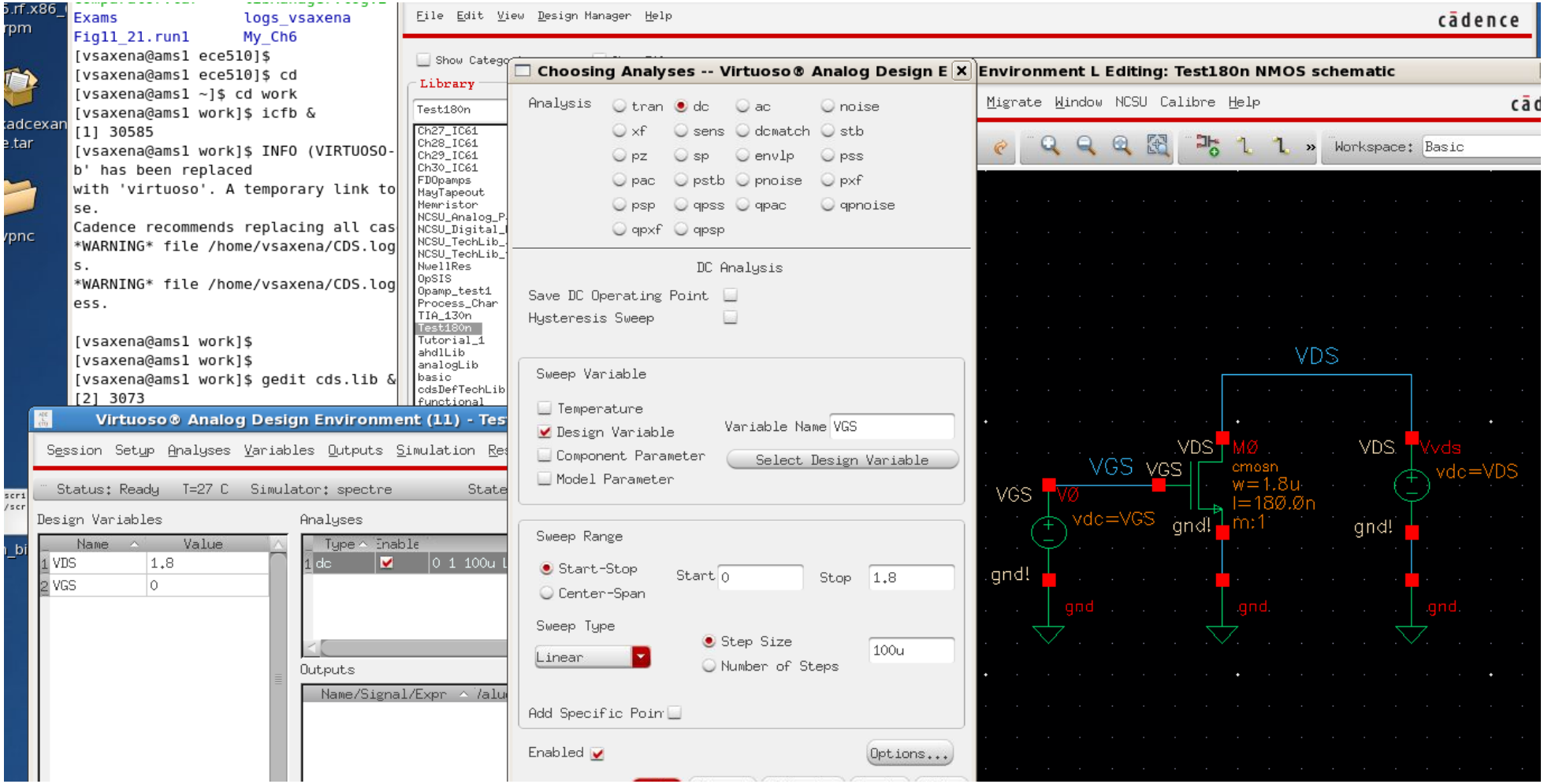
- Create your schematic



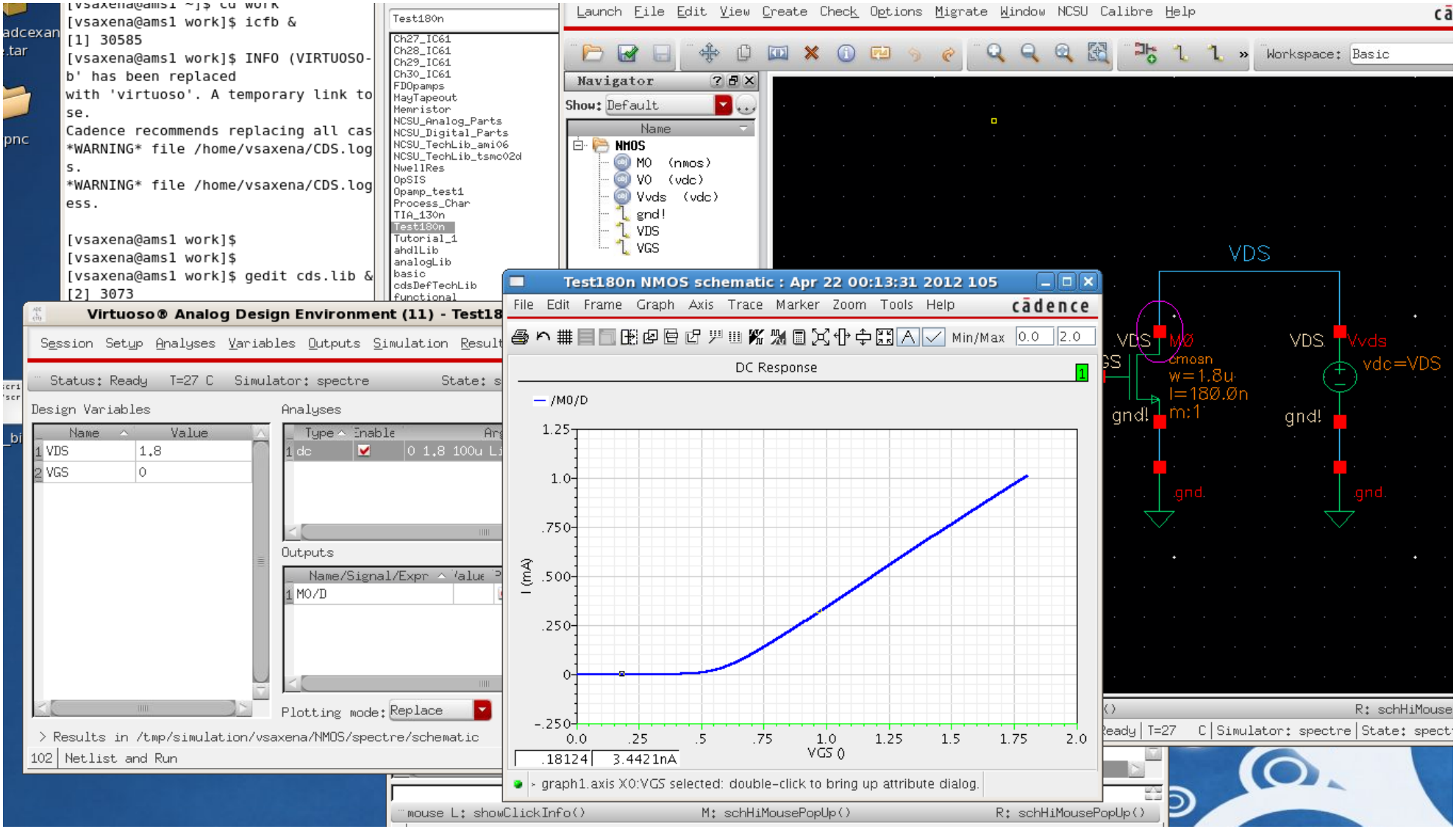
- Model location on AMS servers is at /home/pdks/ncsu-cdk-1.6.0.beta/models/spectre/standalone



- Set the model location (tsmc018.scs)



- Set up your desired analysis (VGS DC sweep in this example)



- Run simulation