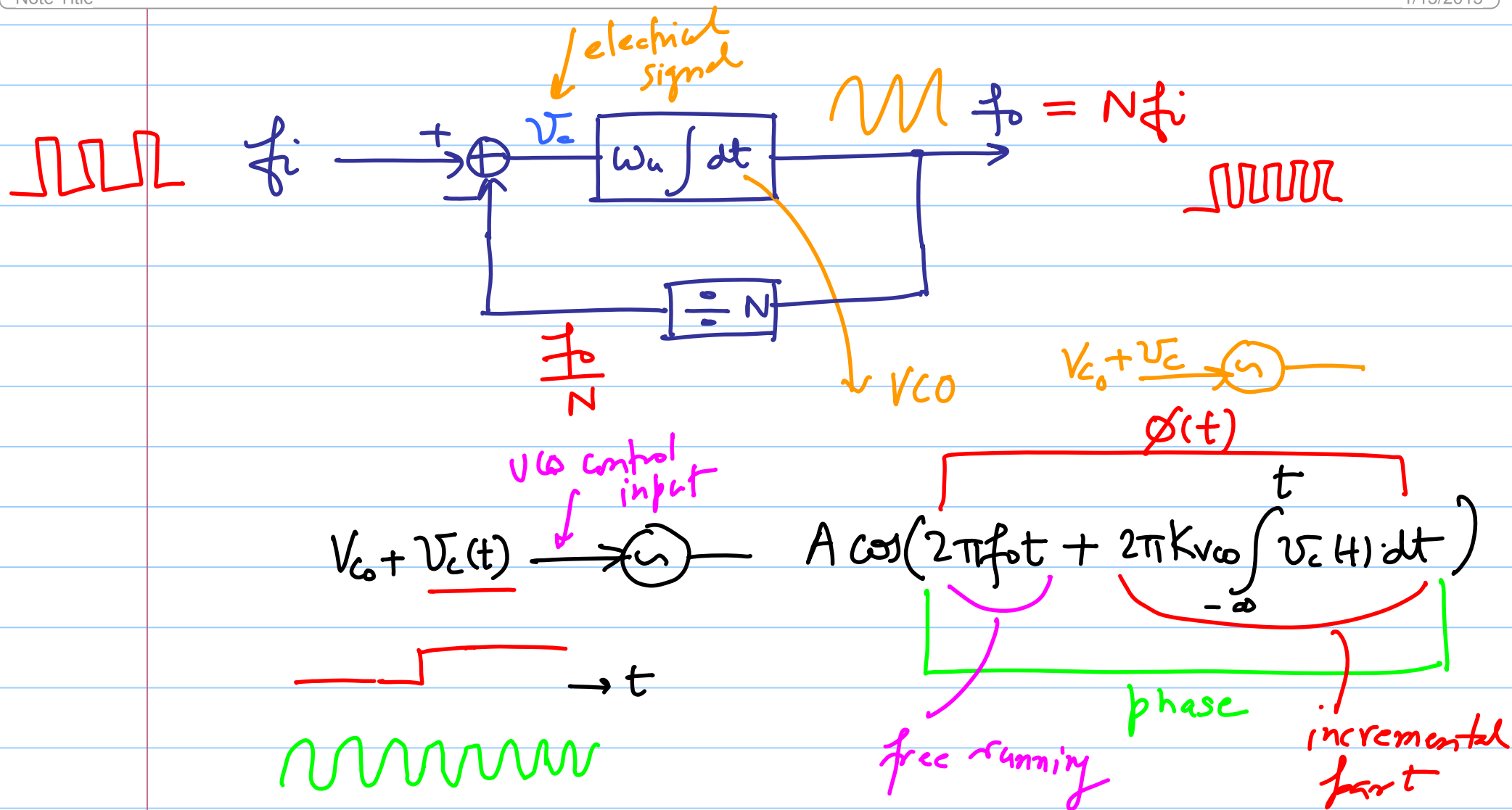


ECE 518 - Lecture 2

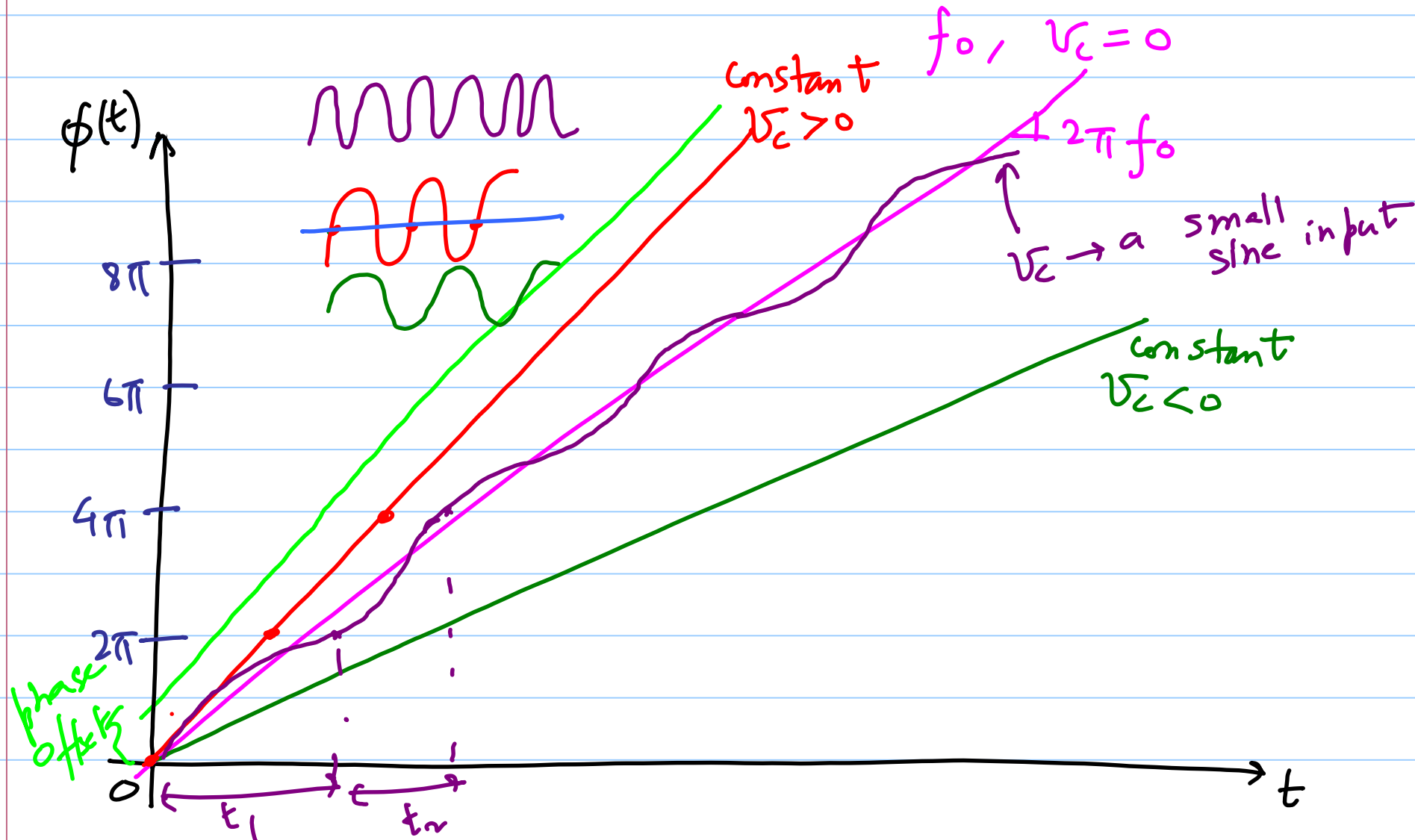
Note Title

1/15/2015

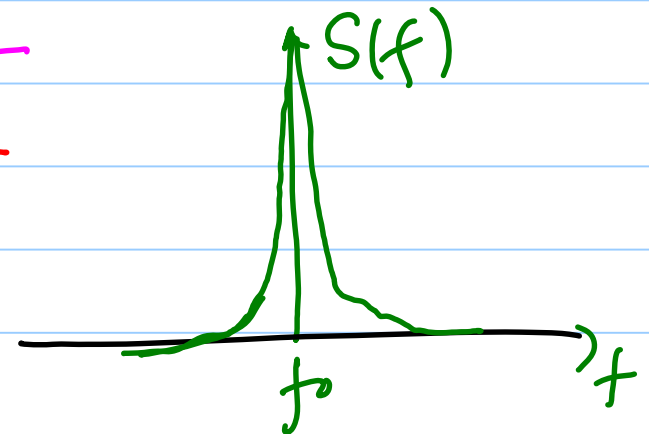
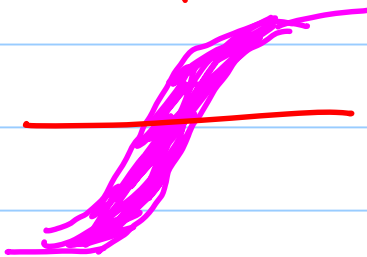
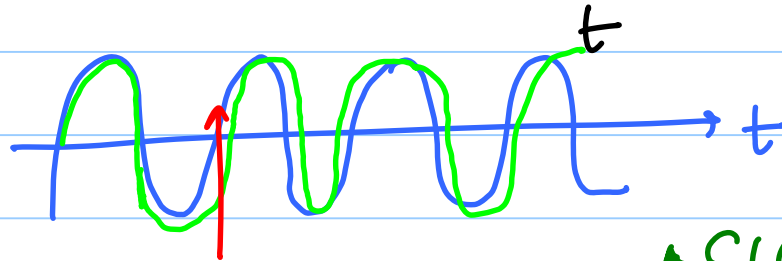
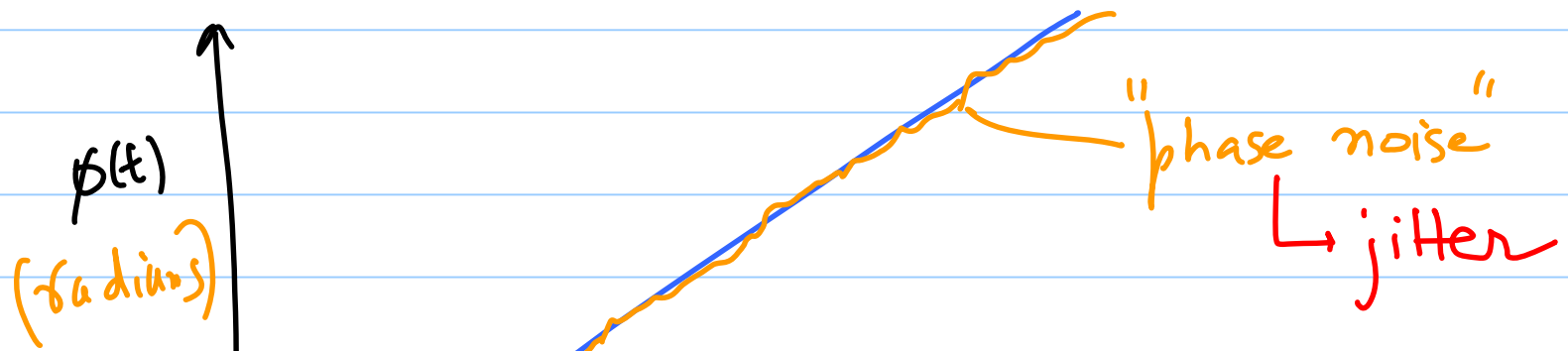


phase of the V_{Lo} output, $\phi(t)$

→ output is periodic with period 2π



VCO $\rightarrow$ NLTV $\rightarrow$ "phase noise"



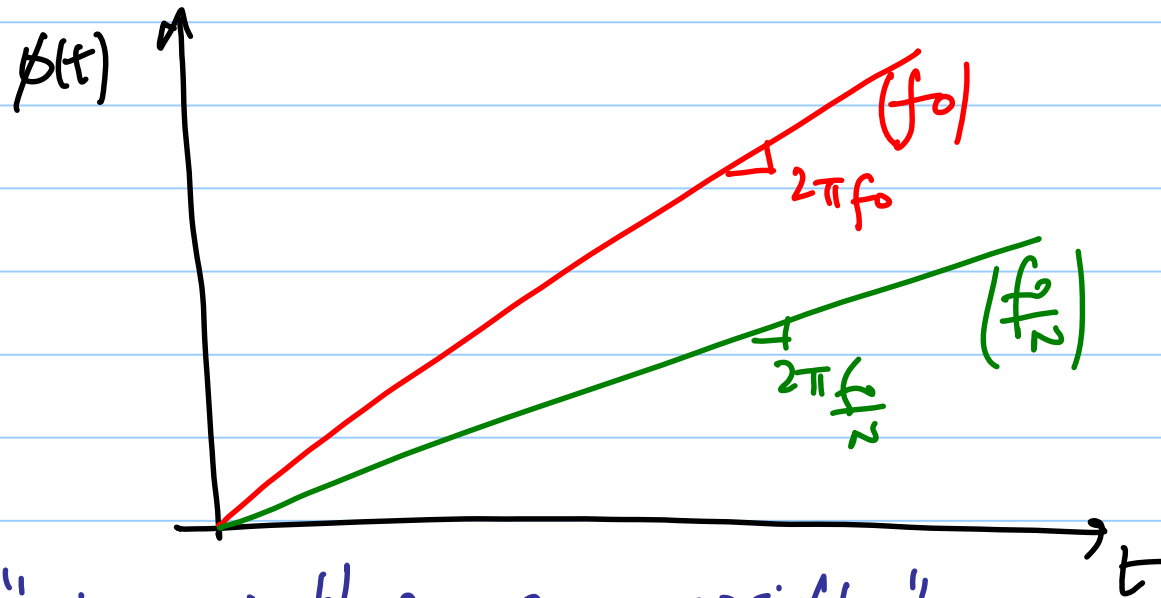
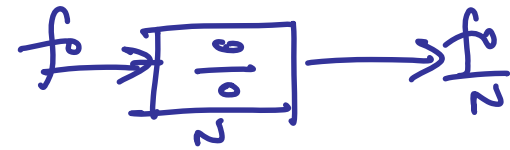
* We choose phase $\phi(t)$ as the basic variable in PLL design.

↳ instantaneous frequency

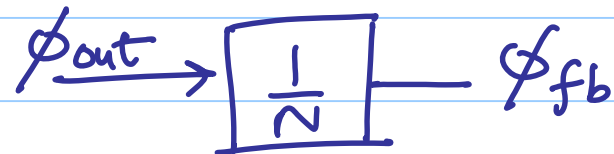
$$f_i(t) = \frac{1}{2\pi} \cdot \frac{d\phi(t)}{dt}$$

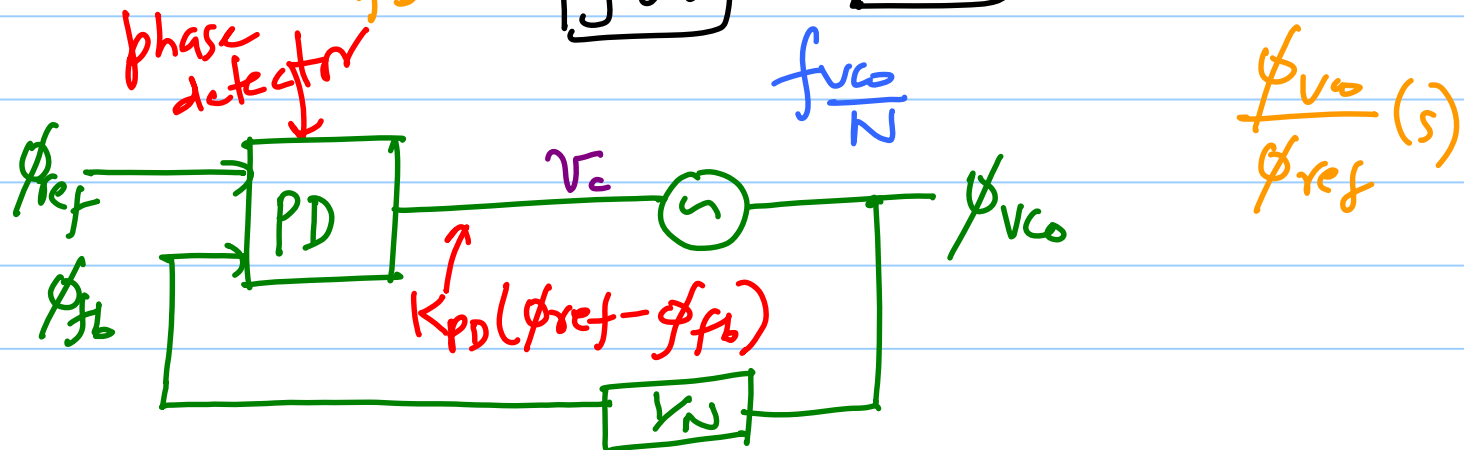
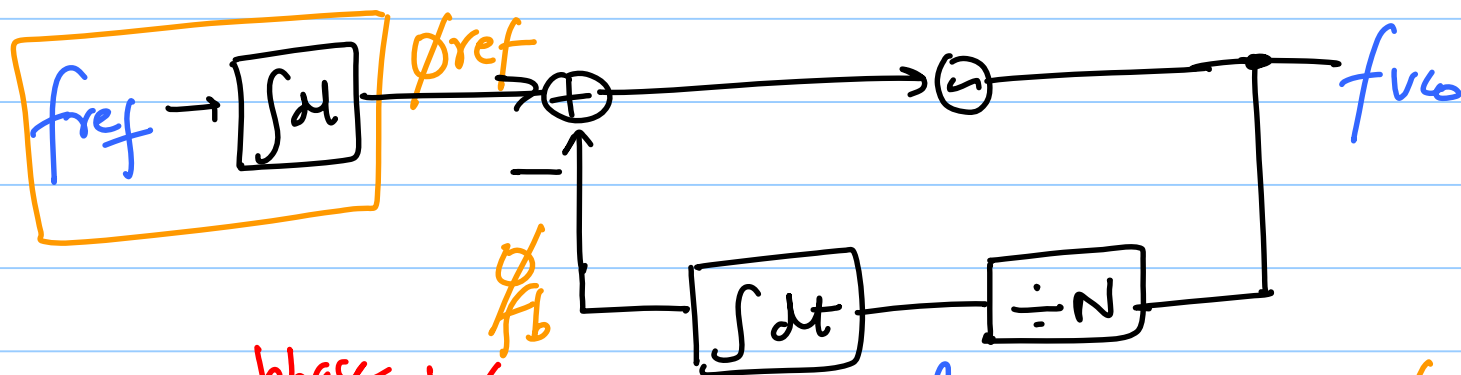
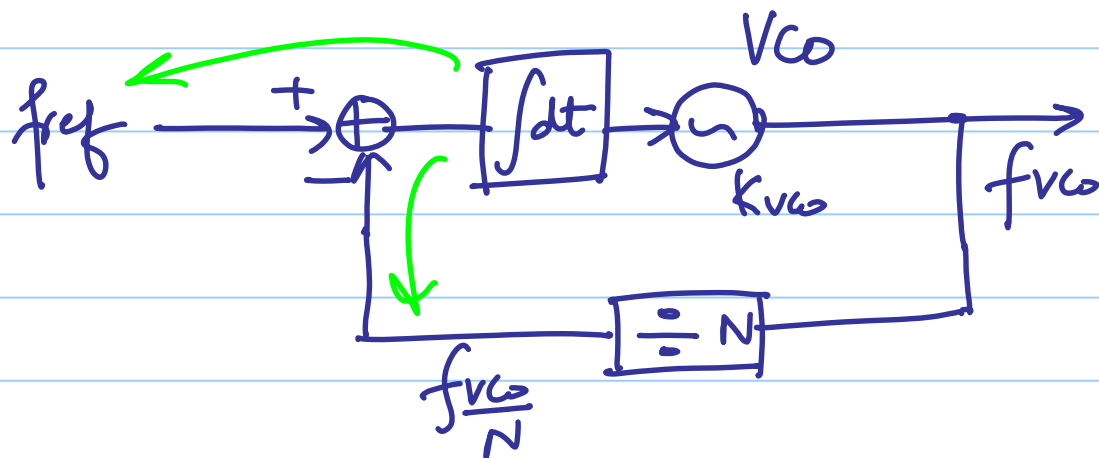
rad/s

Hz

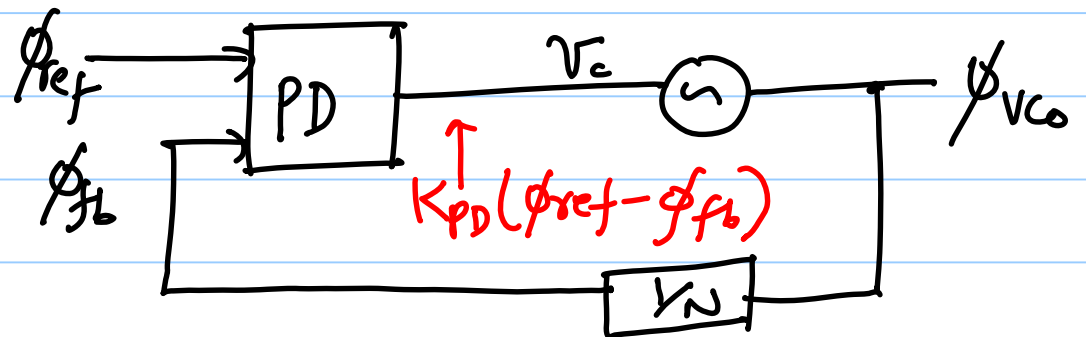
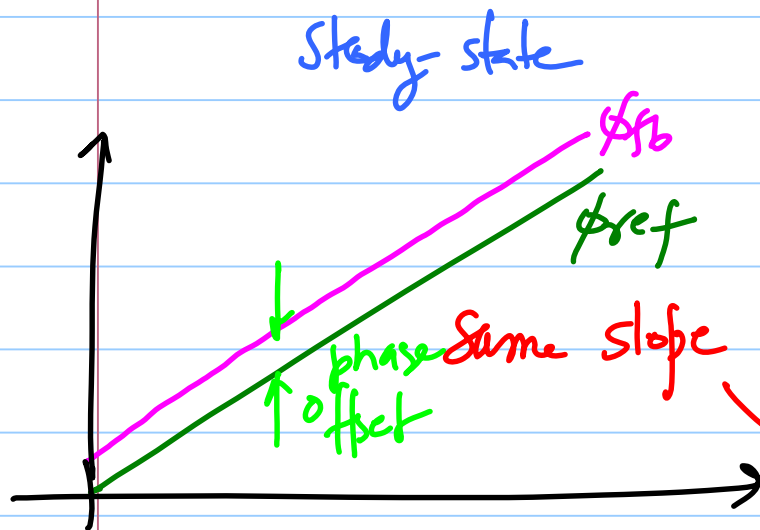
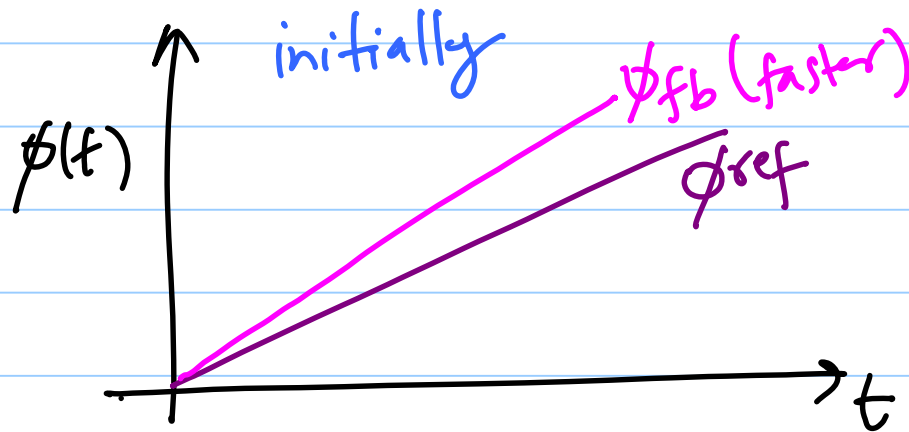


"when using phase as a variable"





phase detector (PD) $\Rightarrow$
 detects the difference between the
 ref Δ the feedback phases.



$$f_{ref} = \frac{f_{vco}}{N} \quad \text{or} \quad f_{vco} = N f_{ref}$$

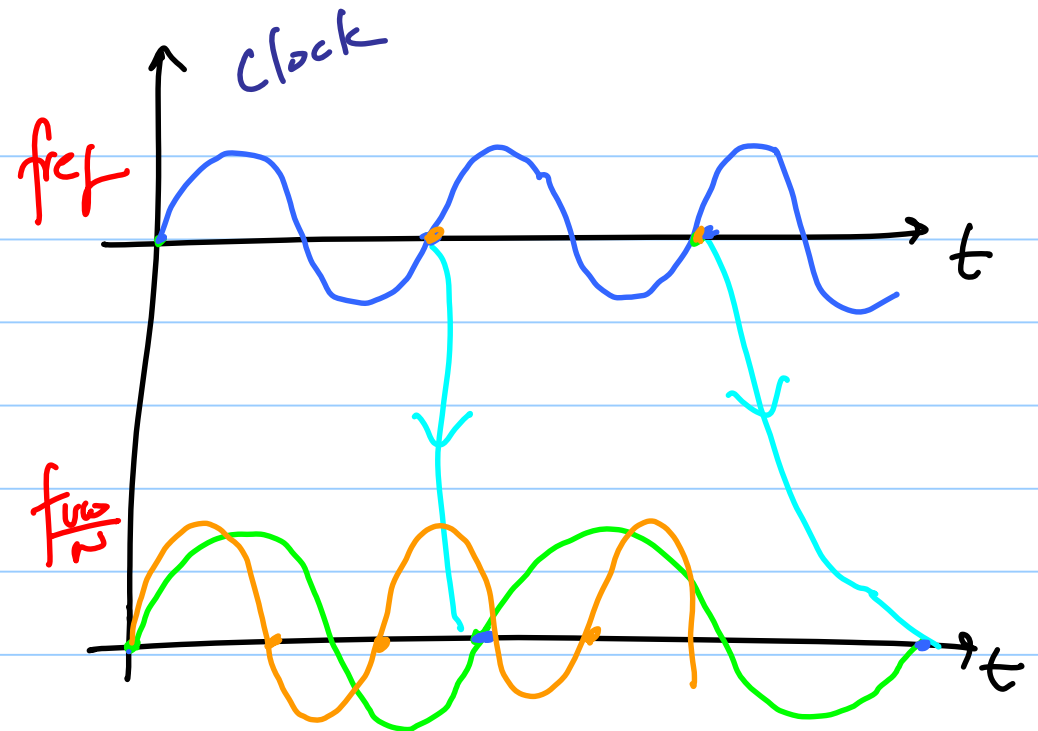
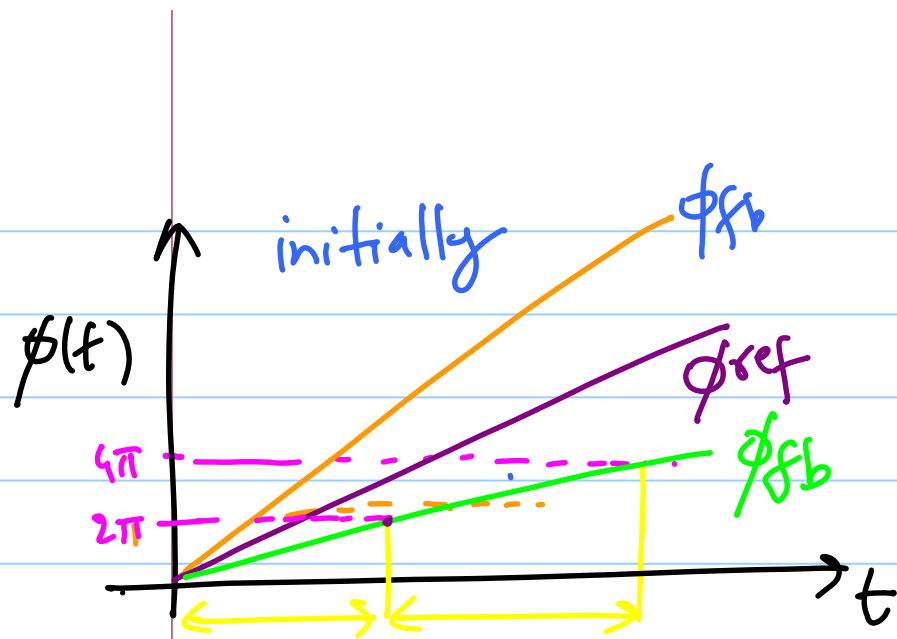
In steady-state,

V_C becomes constant

when both ϕ_{ref} & ϕ_{fb} have the same slope

$$\rightarrow f_{VCO} = N f_{ref}$$

$\rightarrow$ can have a phase offset



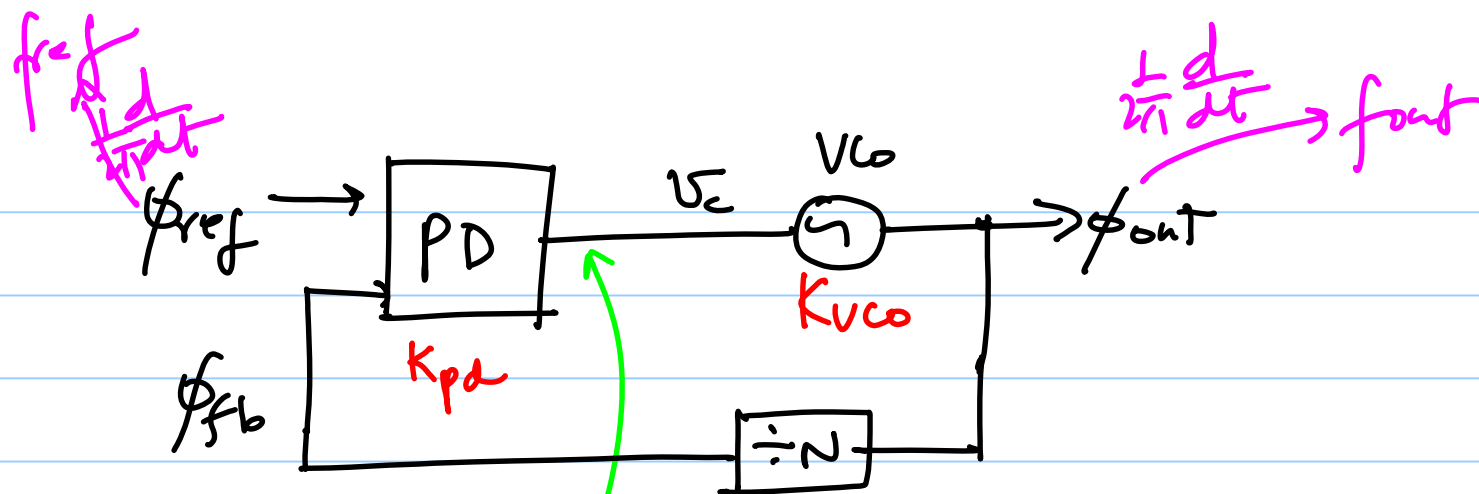
the phase difference will grow

- * We are using the phase difference to control the frequency of the VCO
 - ↳ phase-locked loop

↳ In steady-state $f_{out} = N f_{ref}$

and the phase difference is a
constant value

↳ but not necessarily
zero.



$$K_{pd} (\phi_{ref} - \phi_{fb})$$

$$f_{out} = f_0 + K_{vco} \cdot V_c$$

free running frequency

in steady-state

$$V_c = \frac{N \cdot f_{ref} - f_0}{K_{vco}} \rightarrow \textcircled{1}$$

$$\phi_{ref} - \phi_{fb} = \frac{N f_{ref} - f_0}{K_{vco} \cdot K_{pd}} \rightarrow \textcircled{2}$$

