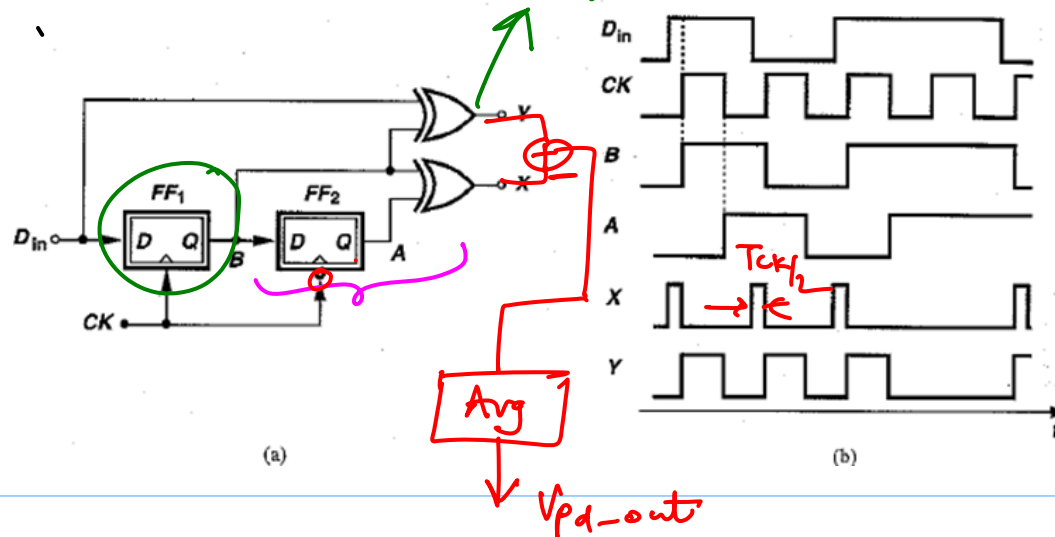


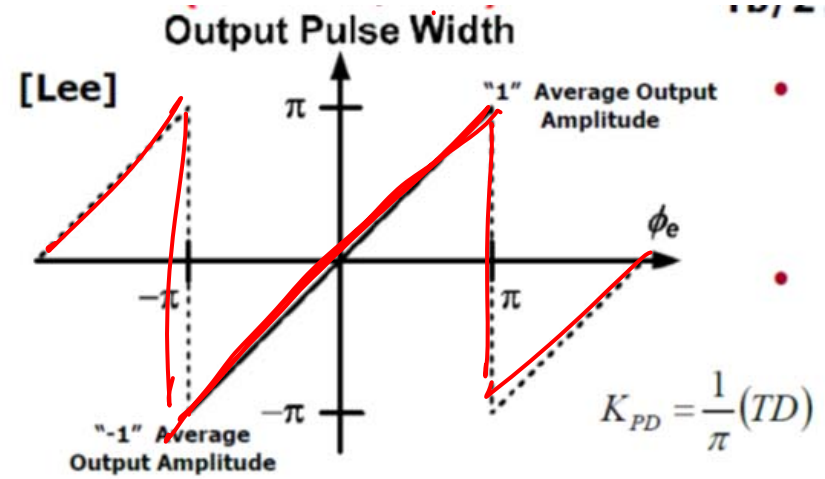
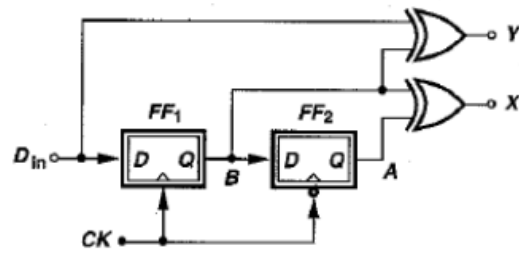
ECE 518 - Lecture 25

Note Title

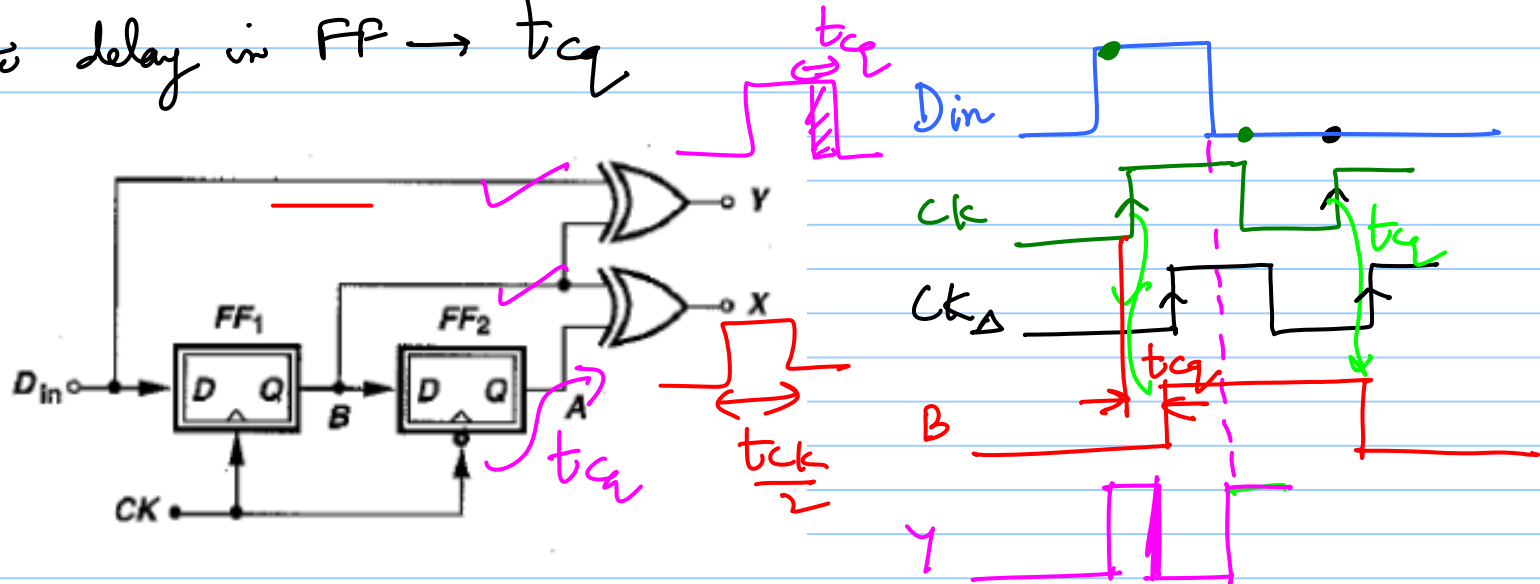
4/23/2015

Hogge PD $\rightarrow$ Linear PD





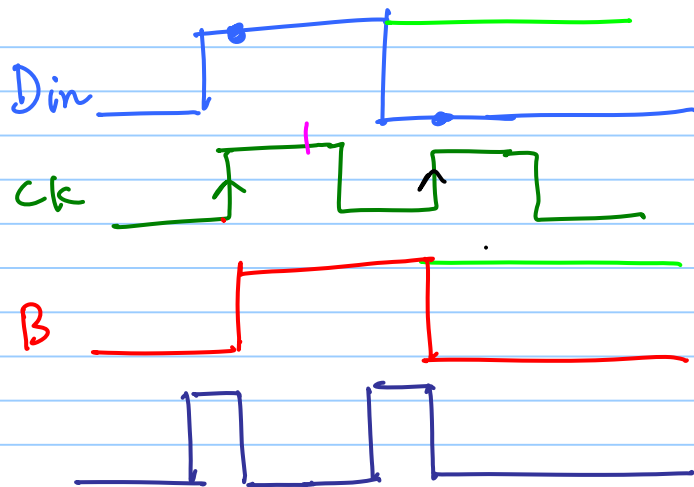
* finite delay in FF $\rightarrow t_{cq}$



t_{cq} of FF₁ $\Rightarrow$ B changes t_{cq} after the clock rises

$\hookrightarrow$ pulse at Y is t_{cq} seconds wider than the actual phase difference b/w D_{in} & CK

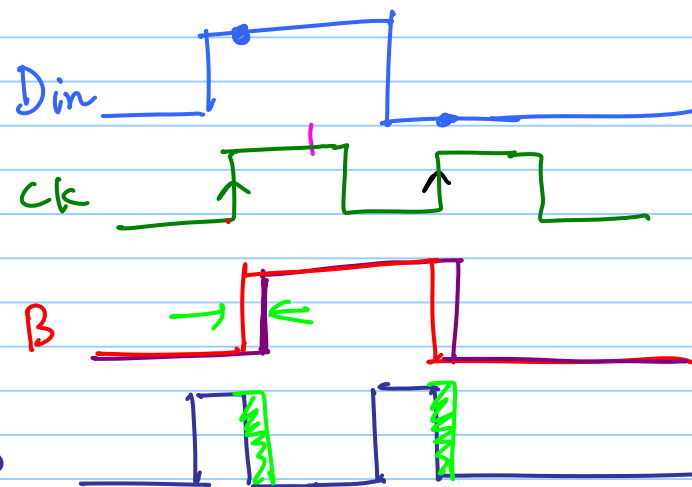
Ideal



$$Y = D_{in} \oplus B$$

* t_{ca} of FF_2 shifts the pulses at A by ΔT
 $\hookrightarrow$ but still produces PW equal to $\frac{T_{ck}}{2}$

Delay in FF_1

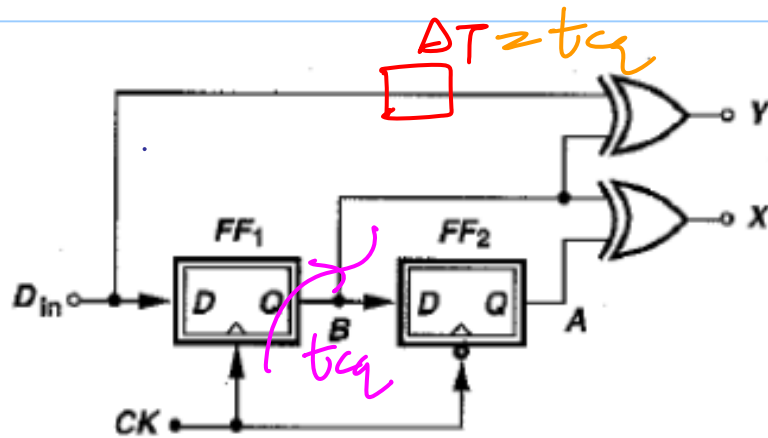


$$Y = D_{in} \oplus B$$

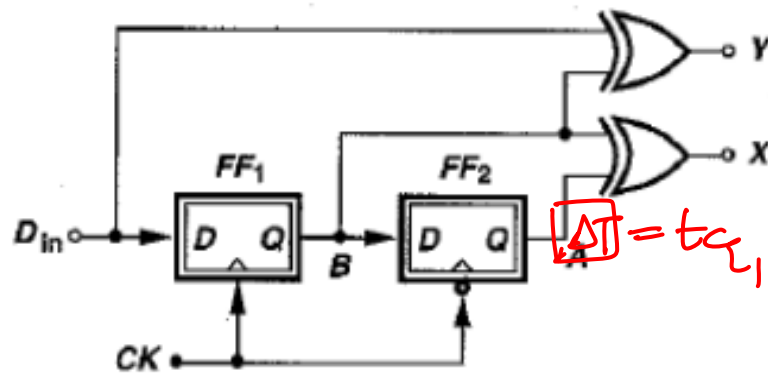
x At high speeds ΔT can be significant fraction of the clock period

↳ Systematic offset when the loop is locked

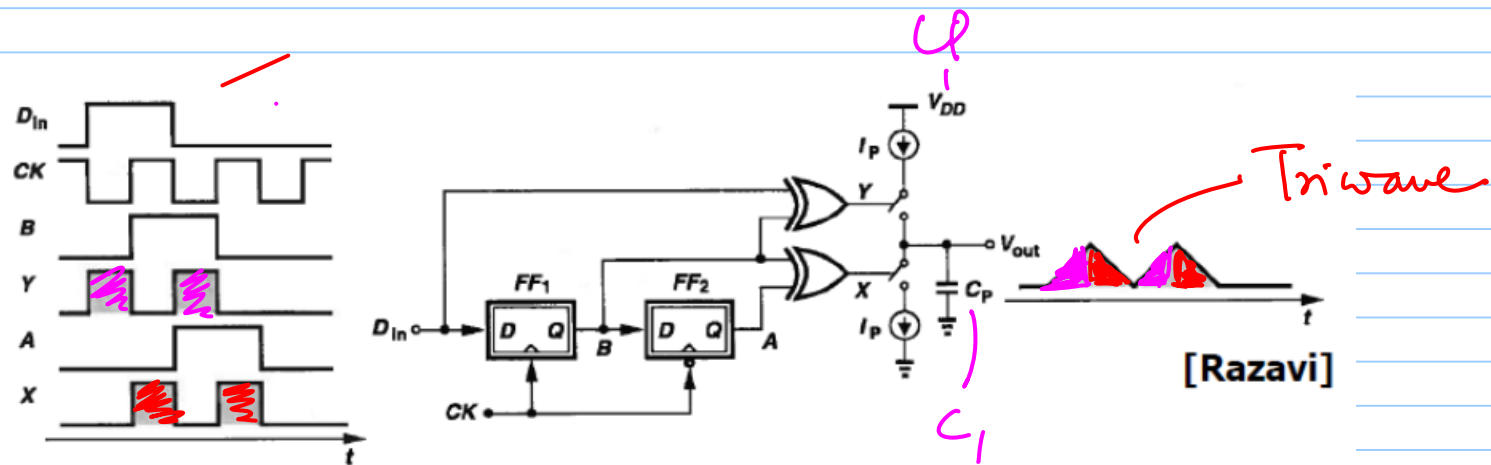
① restore the proportional pulse at y



② widen the reference pulse by ΔT

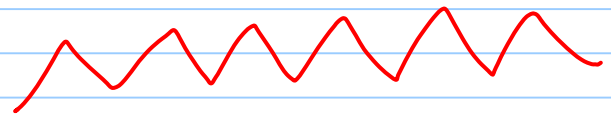


$B \oplus A_{\Delta} \Rightarrow$ pulsewidth of $\frac{T_{ck}}{2} + \Delta T$



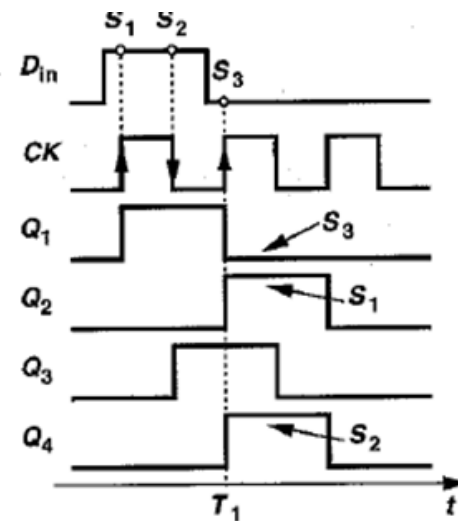
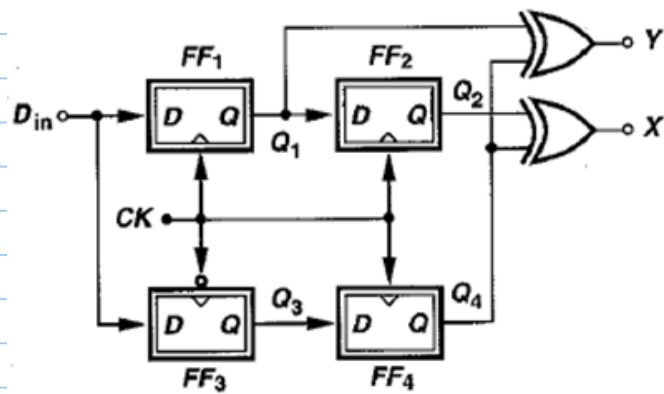
non-zero area

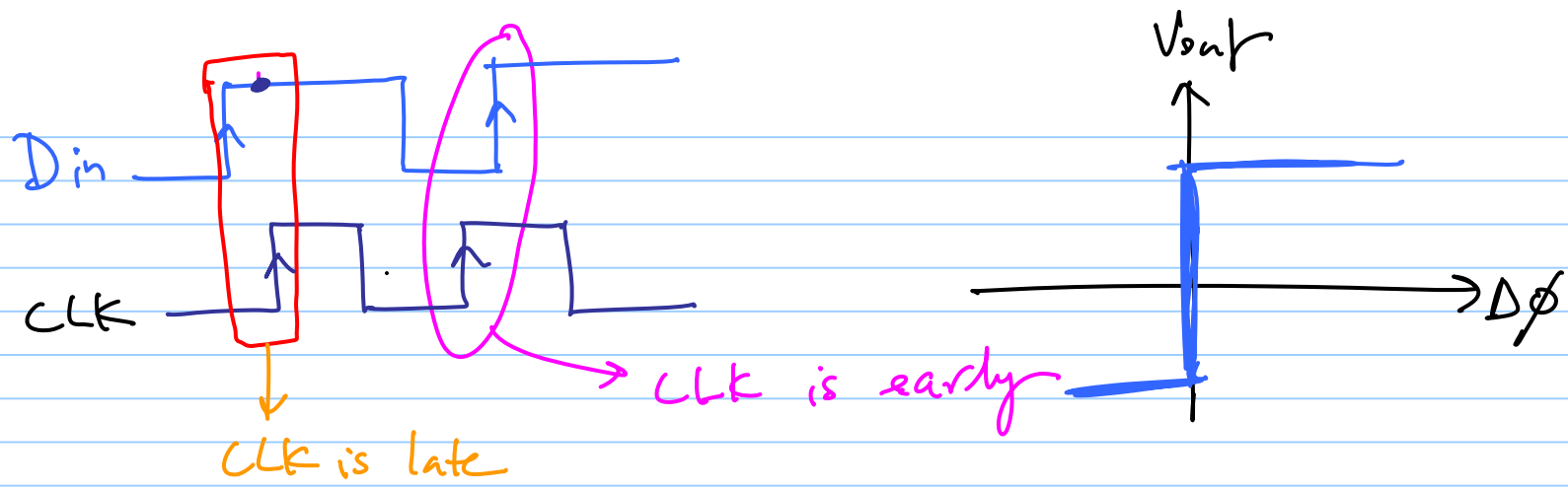
→ severely disturbs V_{out}

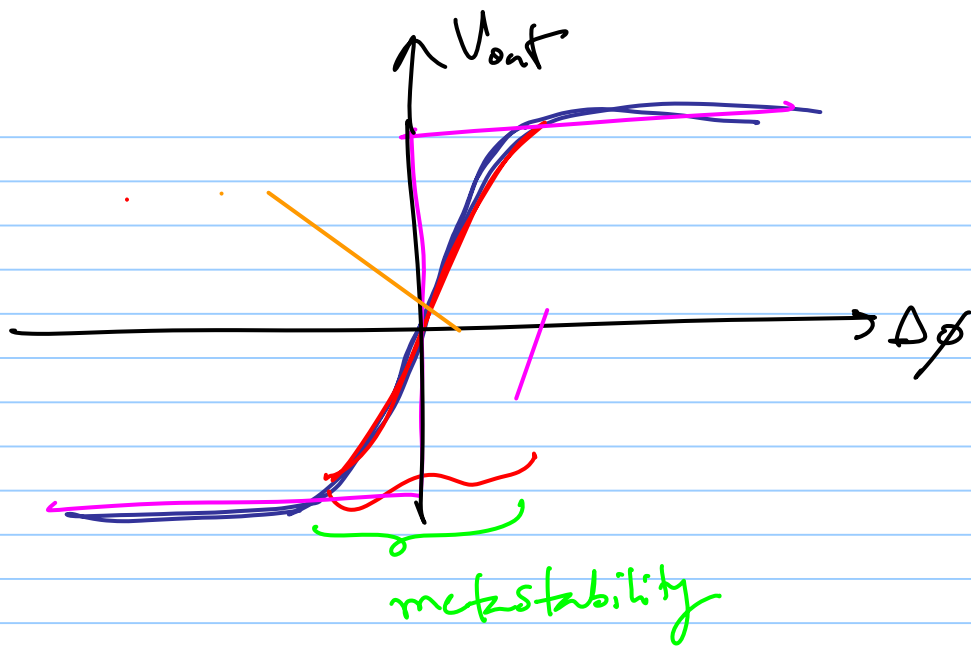


Non-linear (Binary)

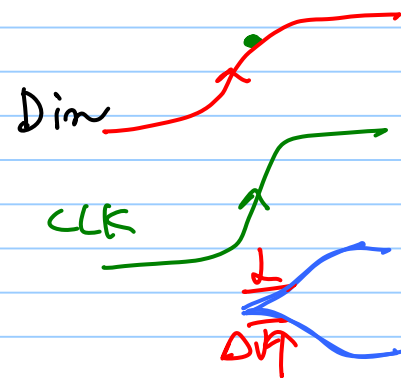
Alexander Phase detector







metastability in the FF.

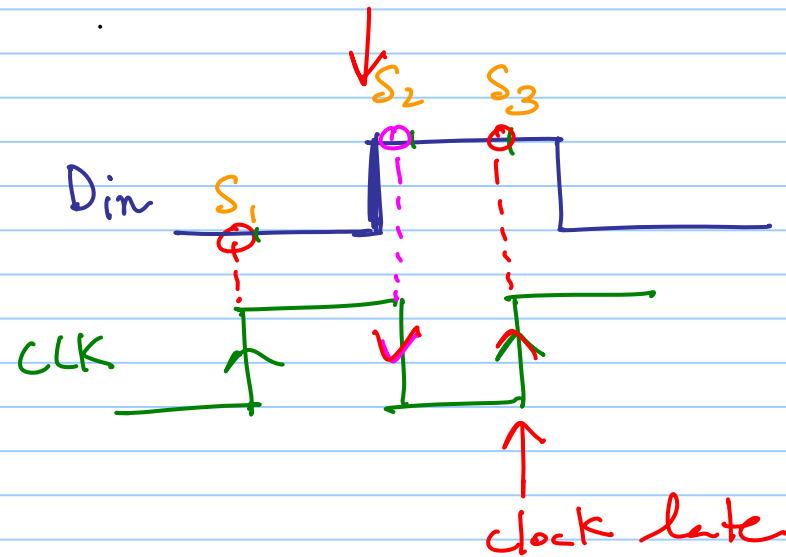
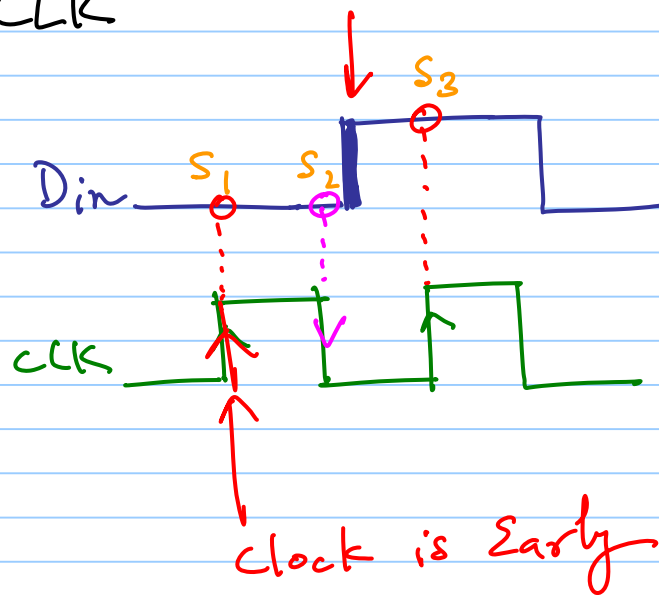


$$\frac{V_{out}}{\Delta\phi} = V_p e^{T_b/T_R}$$

Can approximate as linear PLL

Din

CLK



Take 3-samples $s_1 \rightarrow s_3$ by three consecutive clock edges

↳ The PD detects whether a transition occurred and whether the clock leads or lags the data

