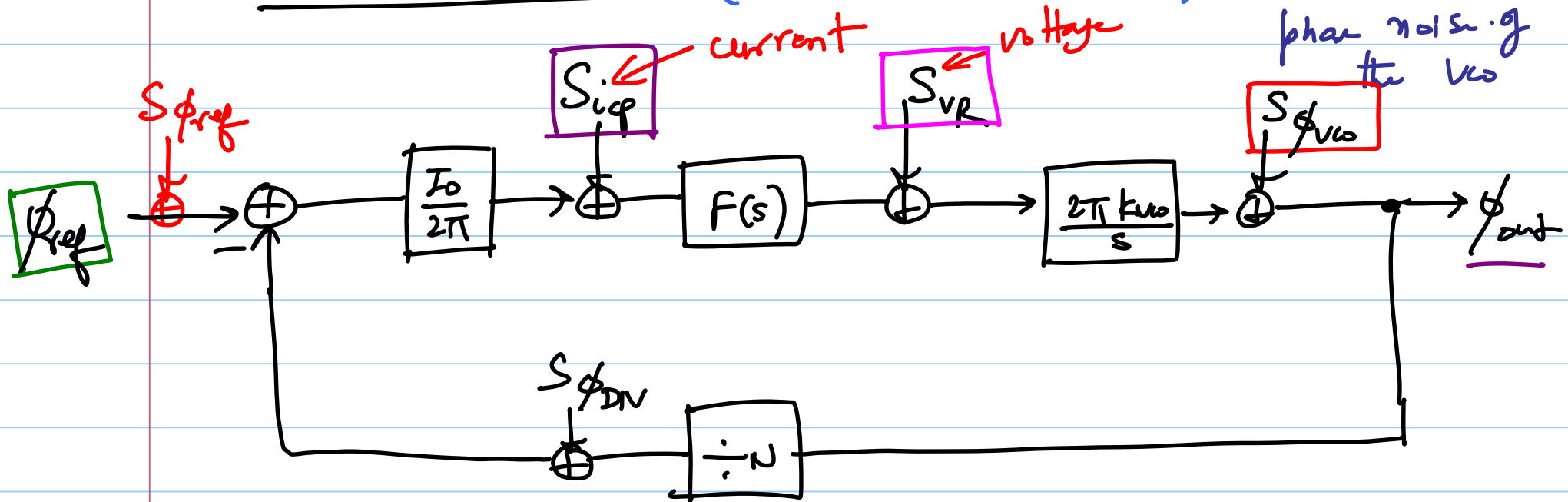


ECE 518- Lecture 20

Note Title

3/31/2015

Noise in PLL (Phase domain Model)



$S_{\phi_{ref}}$ = Reference clock PSD

$S_{i_{cp}}$ = PFD/CP noise PSD

$S_{v_R} \Rightarrow$ loop filter resistor noise PSD

$S_{\phi_{VCO}} \Rightarrow$ VCO phase noise PSD

$S_{\phi_{div}} \Rightarrow$ Divider noise PSD

Noise Transfer functions (NTF)

$$\text{Loop gain} \Rightarrow L(s) = \frac{I_0 K_{VCO}}{N} \cdot \frac{F(s)}{s}$$

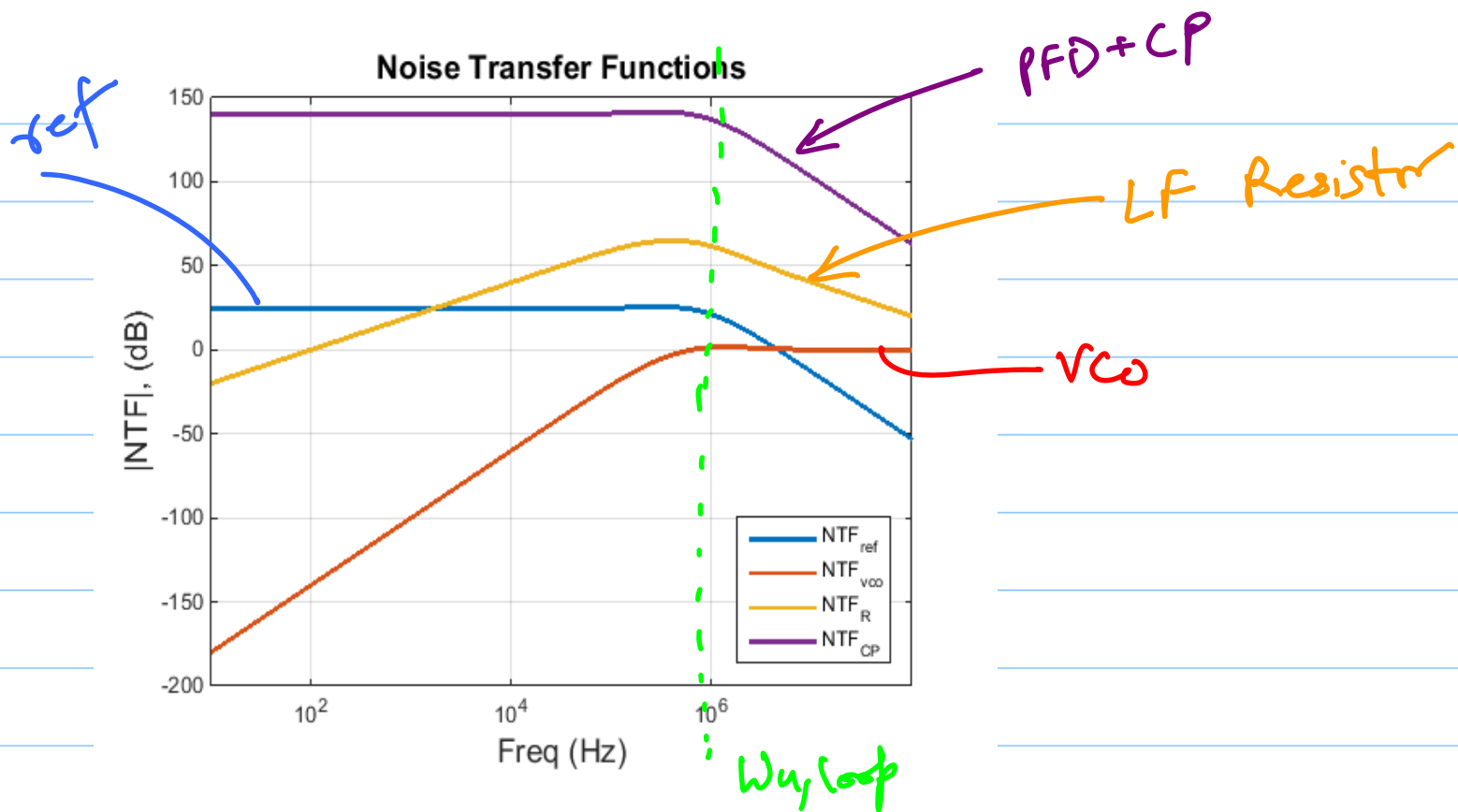
$$NTF_{ref} = \frac{\phi_{out}(s)}{\phi_{ref}} = \frac{N \cdot L(s)}{1 + L(s)} \quad \text{LPF}$$

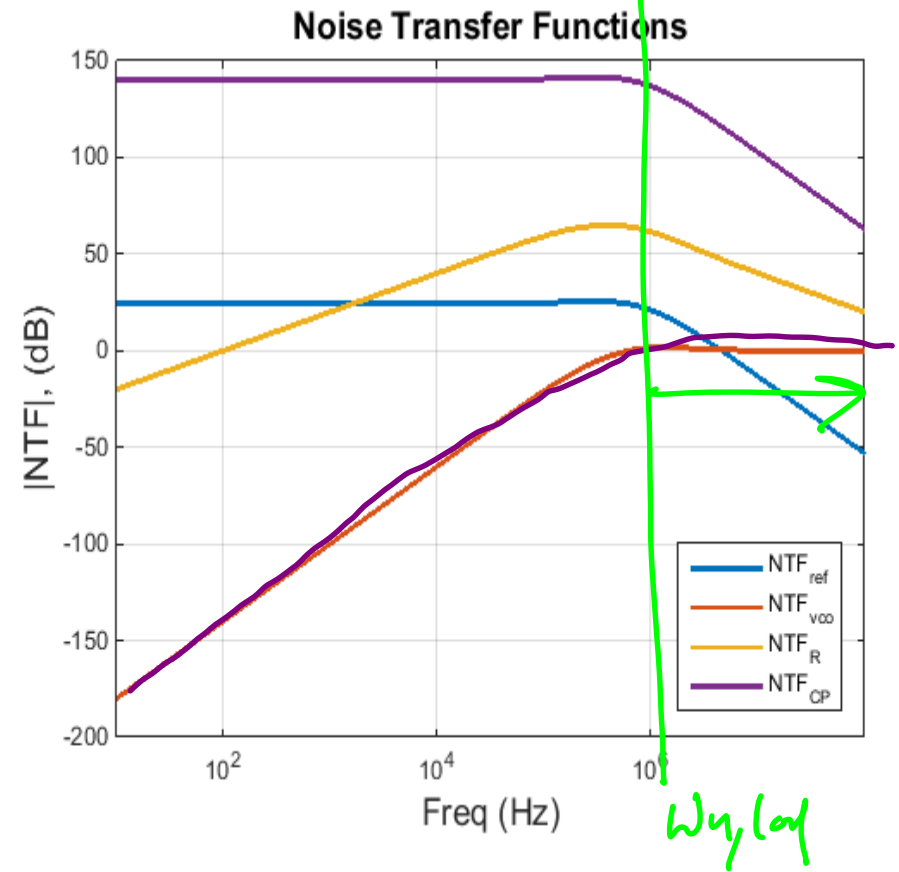
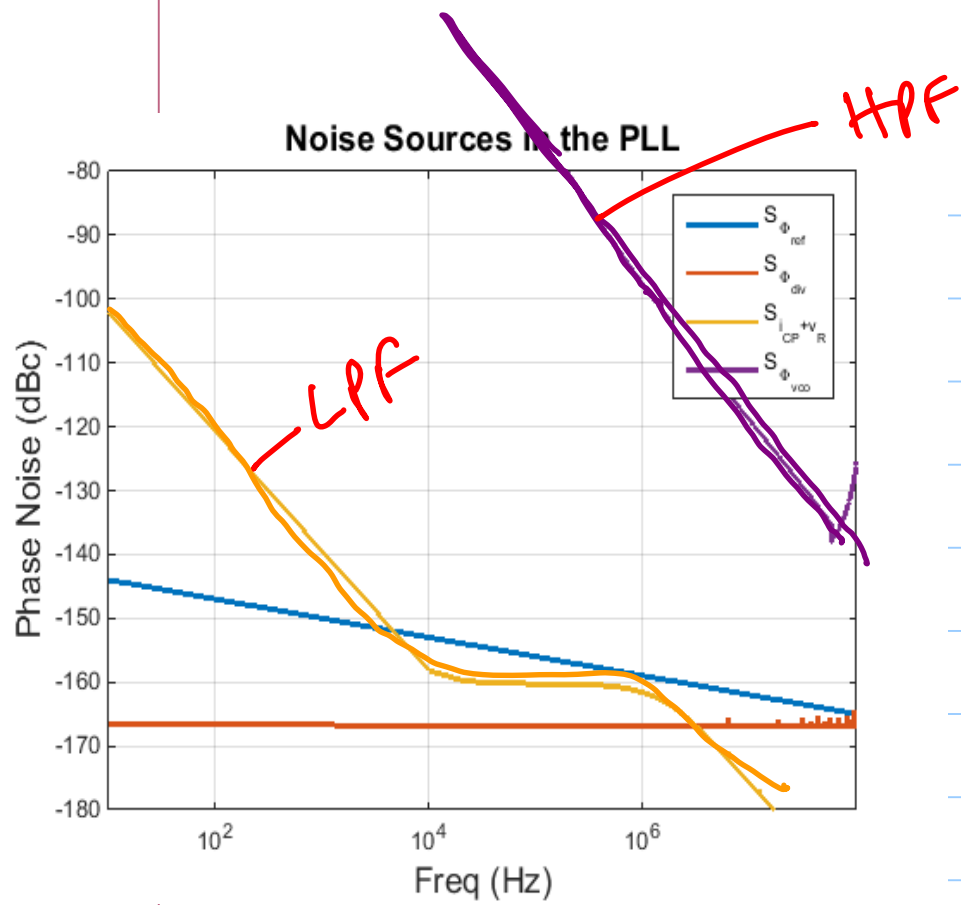
$$NTF_{div}(s) = NTF_{ref}(s) \quad \leftarrow \text{same entry point as } \phi_{ref} \quad \text{LPF}$$

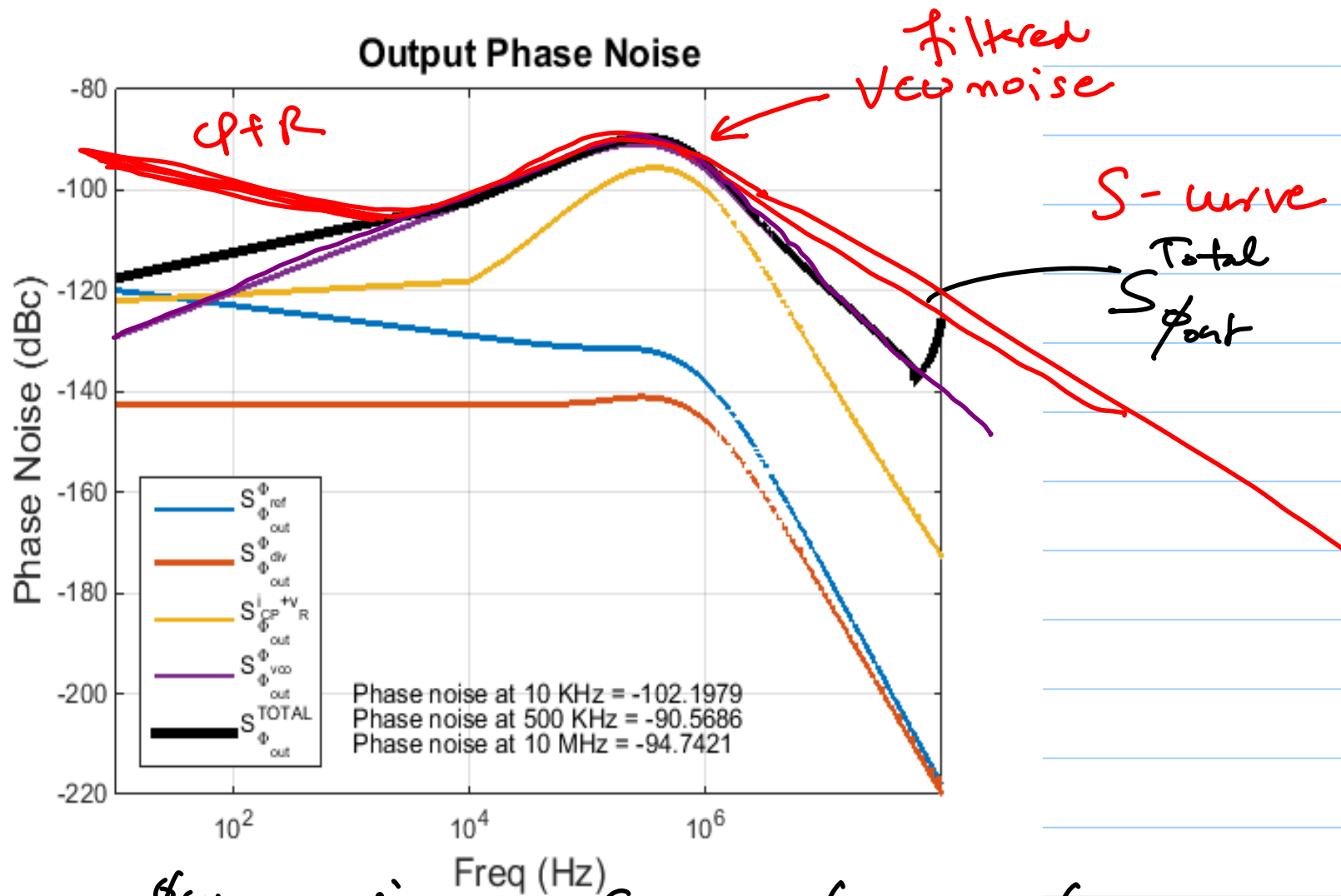
$$NTF_{\phi}(s) = \frac{\phi_{out}(s)}{i_{cp}} = \frac{NTF_{ref}(s)}{I_{cp}/2\pi} \quad \text{LPF}$$

$$NTF_R(s) = \frac{\phi_{out}(s)}{v_R(s)} = \frac{\frac{2\pi K_{vco}}{s}}{1 + L(s)} \quad \text{BPF}$$

$$NTF_{vco}(s) = \frac{\phi_{out}}{\phi_{vco}}(s) = \frac{1}{1 + L(s)} \quad \text{HPF}$$







$$S_{\phi_{out}}^{Total} = S_{\phi_{out}}^{\phi_{ref}} + S_{\phi_{out}}^{\phi_{div}} + S_{\phi_{out}}^{\phi_{cp+vr}} + S_{\phi_{out}}^{\phi_{vco}} + S_{\phi_{out}}^{\phi_{div}}$$

$$\text{Total Phase Noise (power)} = \sigma_{\Delta\phi}^2 = \int_0^{\infty} S_{\phi_{out}}^{\text{Total}}(f) df \quad (\text{radians})^2$$

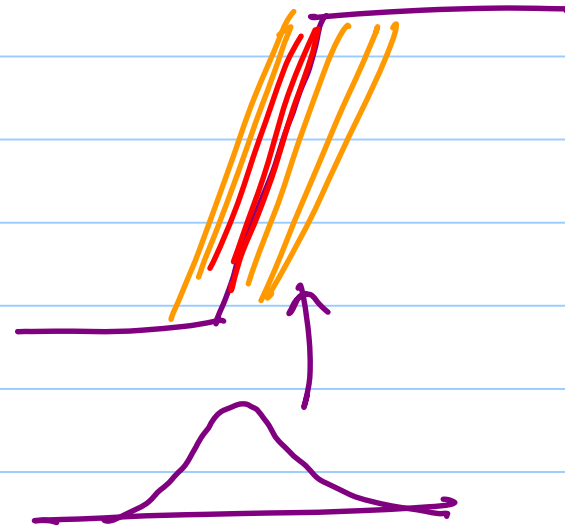
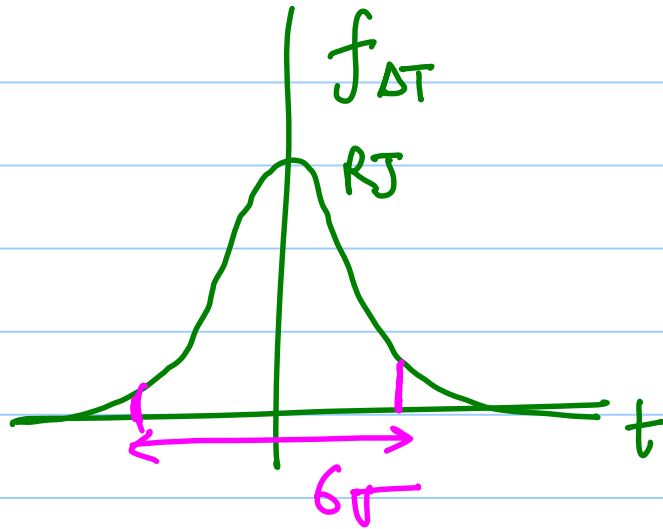
$$\sigma_{\Delta\phi} = \sqrt{\int_0^{\infty} S_{\phi_{out}}^{\text{Total}}(f) df} \cdot \frac{360^\circ}{2\pi} \quad [\text{degrees}]$$

$$\sigma_{\Delta T} = \sqrt{\int_0^{\infty} S_{out}^{\text{Total}}(f) df} \cdot \frac{T_{VCO}}{2\pi} \quad (\text{sec})$$

↳ Evaluated using trapz function in MATLAB

⇒ $\sigma_{\Delta T}$ ~ 2 ps rms RJ: Random jitter

DJ : deterministic jitter



Large $\omega_n, \text{loop} \Rightarrow$

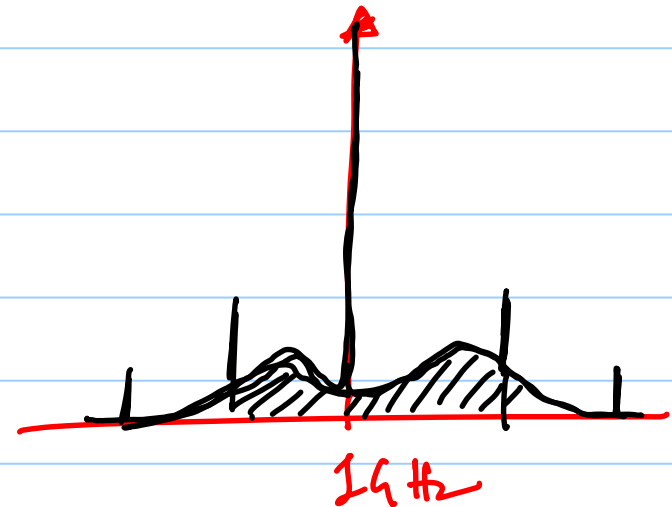
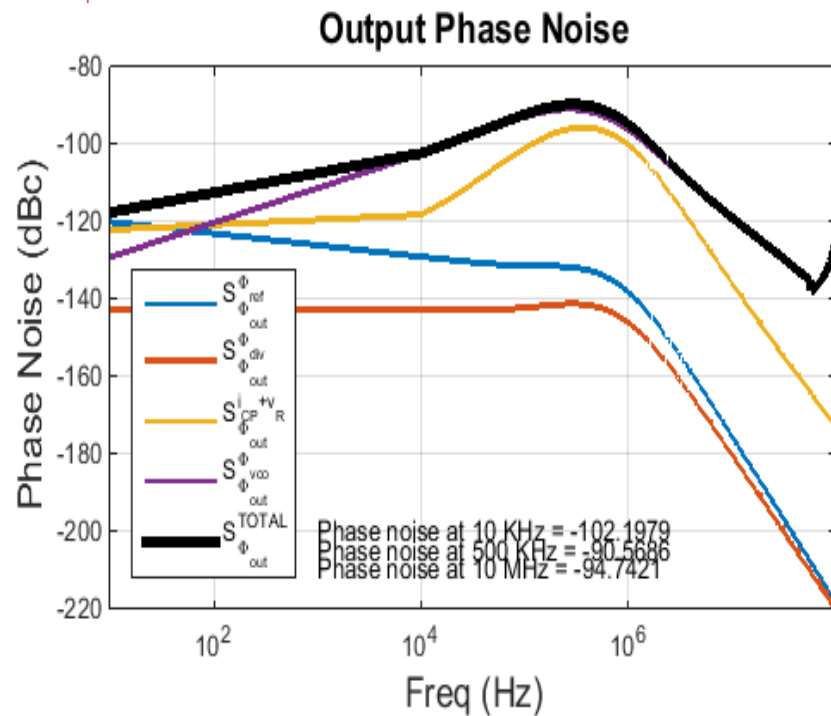
VCO phase noise $\downarrow$

CP+R noise $\uparrow$

faster settling

PLL 1 GHz

Spectrum Analyzer



$$S_{v_R}(f) = 4kTR, \quad S_{i_{cp}}(f) \propto I_{cp}$$

$$R \downarrow \Rightarrow C_1 \uparrow$$

$$\omega_0 = \frac{1}{RC_1}$$

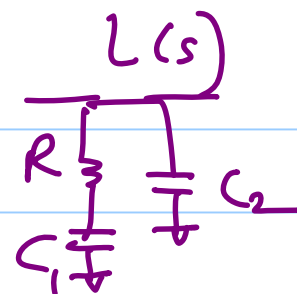
large Area

$$S_{v_R} \cong 4kTR \Rightarrow S_{\phi_{out}}^{v_R} \propto K_{vco}^2 \cdot R$$

$\Rightarrow$ noise from R is increased by $\boxed{K_{vco}^2}$

Drawbacks of a Large V_{ov} gain

$$L(s) = \frac{K_{vo} I_{cp}}{2\pi} \cdot \frac{s + \frac{1}{R C_1}}{s^2 C_2 \left(s + \frac{1}{R C_1 \parallel C_2} \right)}$$



increase $K_{vo} \Rightarrow I_{cp} \downarrow$ for the same loop dynamics

But low $I_{cp} \Rightarrow g_m \downarrow$

$$S_{i_{cp}} \propto \frac{8}{3} K T g_m \quad \checkmark$$

noise drops

$$\boxed{S_{\phi_{out}}^{\sqrt{R}} \propto K_{vo}^2}$$

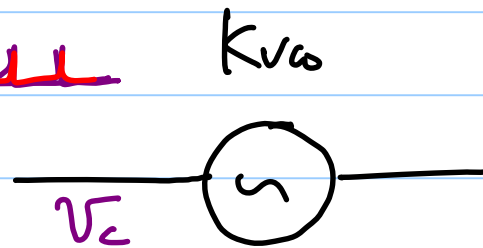
$$\omega_{\text{loop}} \cong \frac{K_{VCO} \cdot I_{CP}}{2\pi} \cdot \frac{1}{C_1 + C_2}$$

↑ ↑ ↑
K_{VCO} const C₁ + C₂

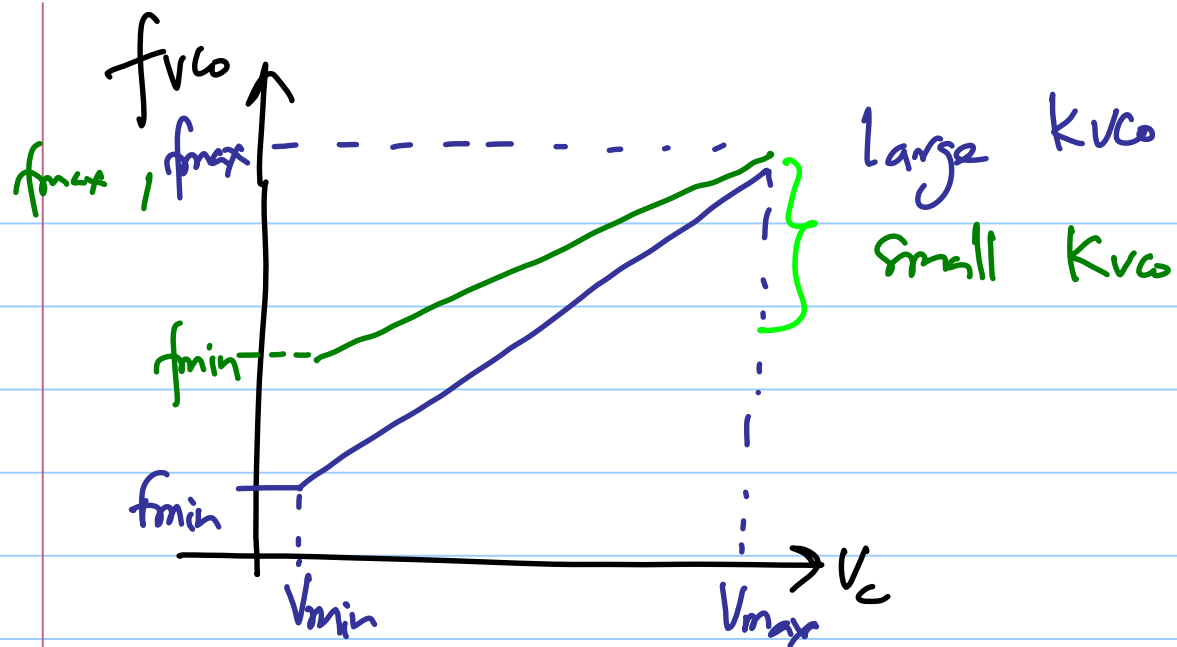
$K_{VCO} \uparrow \Rightarrow C_1 \uparrow$ for I_{CP} const.

$\Rightarrow$ large Area

disturbance in
 V_C due to $\downarrow$ CP mismatch



More reference
 feedthrough
 $\Rightarrow$ more spurs in
 the output.



Smaller K_{VCO}

(-) Reduced VCO operating range

+ Better noise performance (LF noise)

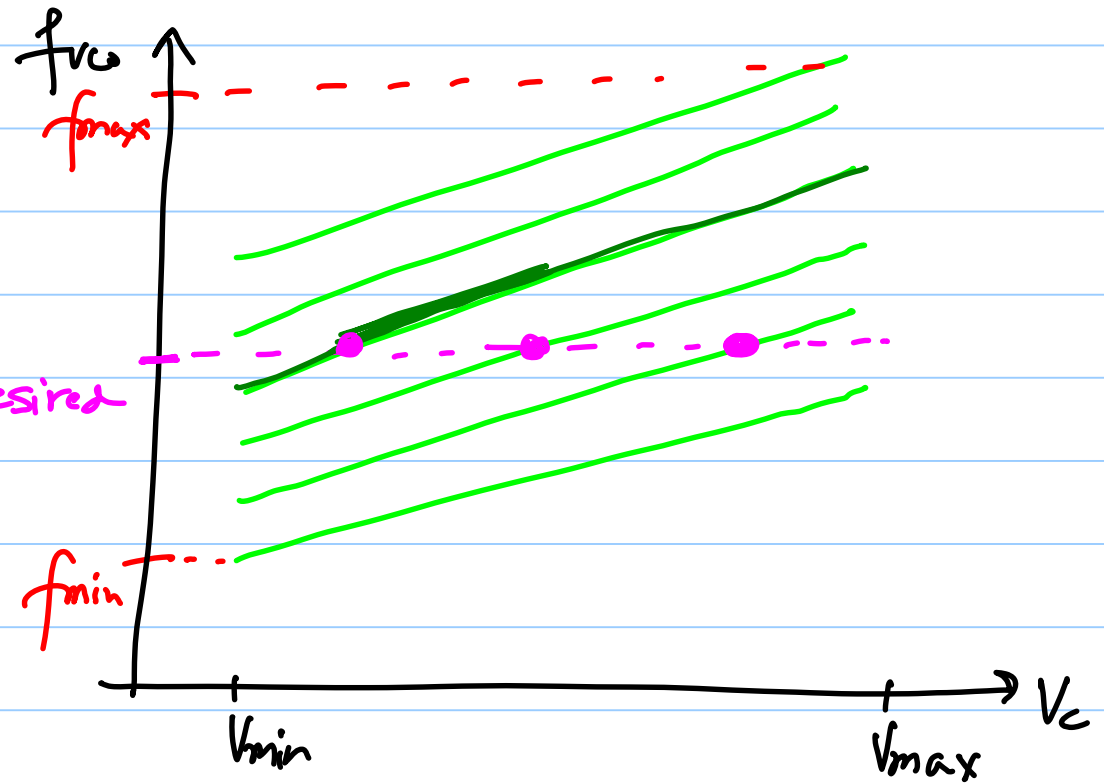
+ Smaller C_1

+ Better spur performance

Increasing operating Range with lower K_{VCO}

* Generate multiple low-gain V_{CO} transfer curves

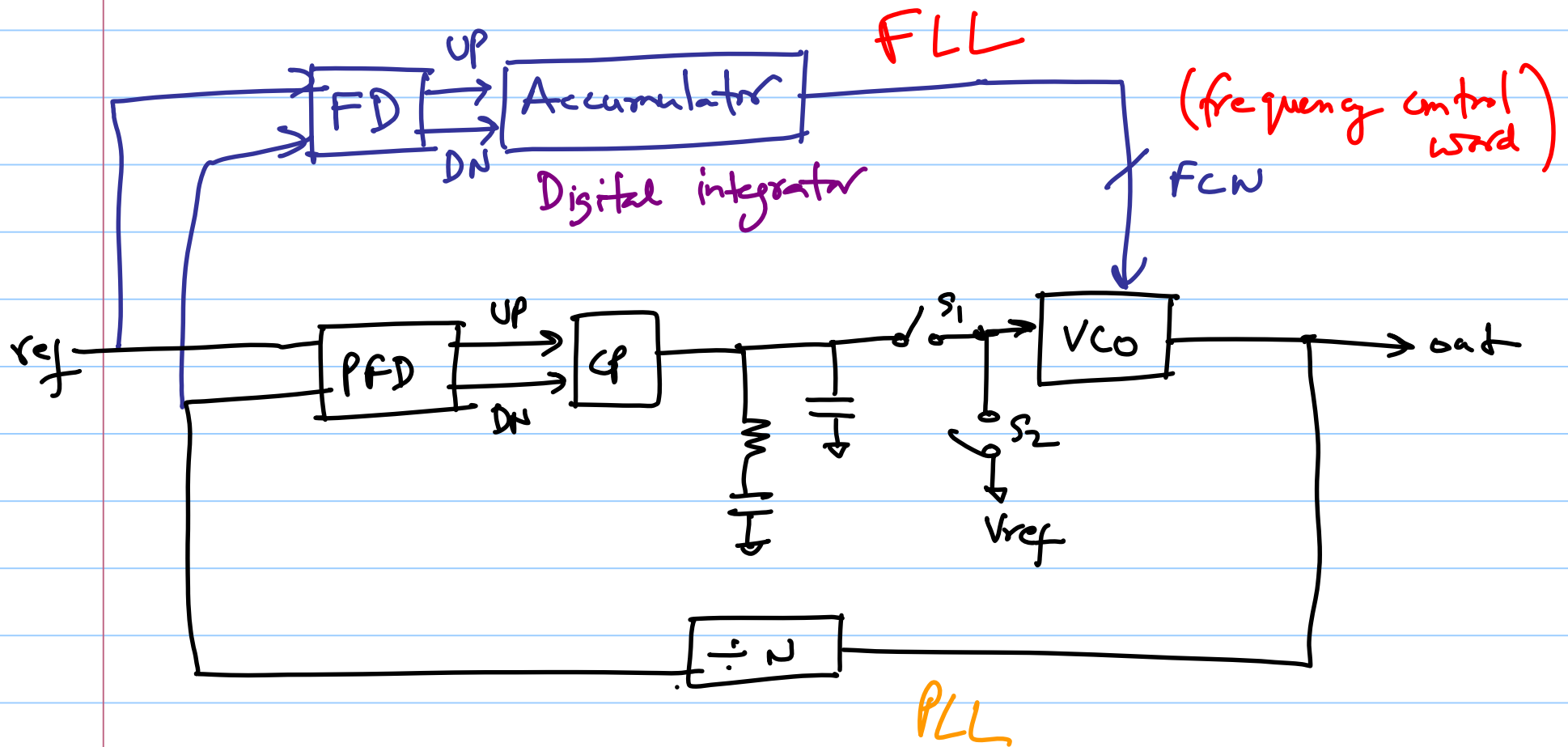
* pick a suitable operating curve



fine tuning $\rightarrow V_C$

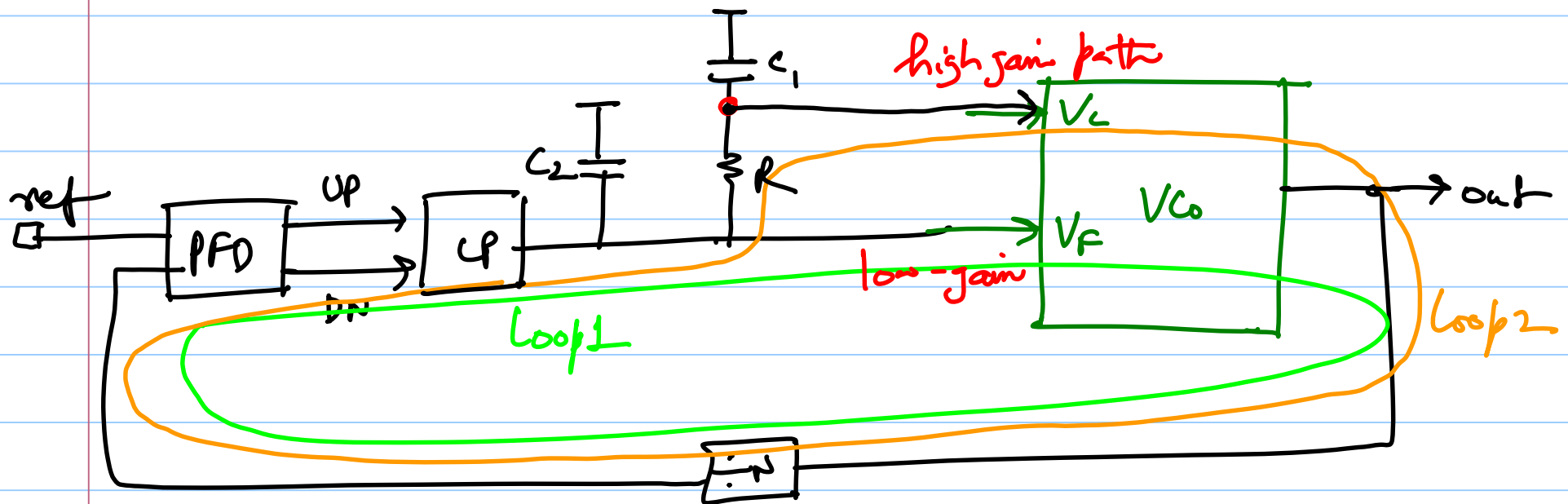
Coarse tuning to select b/w these curves

Split-tuned PLL (with Digital Tuning)



- (-) limited tuning range due to frozen frequency control word
- (-) susceptible to PVT variations

Split tuned with continuous tuning



* V_{CO} controlled using two voltages

high-gain V_C : Coarse control $\leftarrow$ works similar to FLC
low-gain V_F : fine control

* Two coupled feedback loops

↳ potential instability