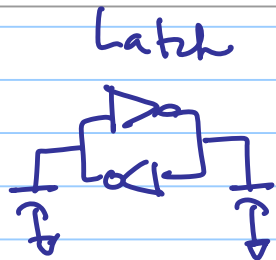


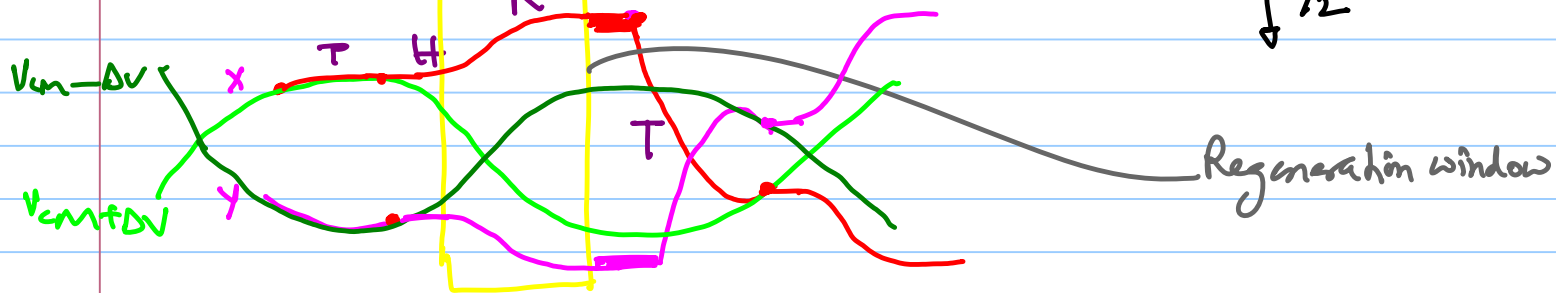
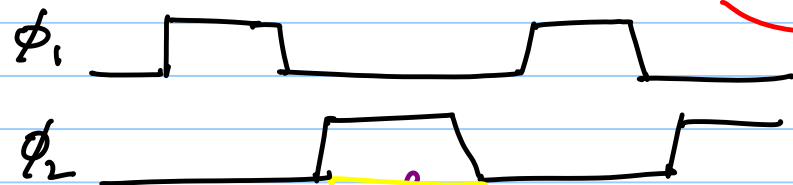
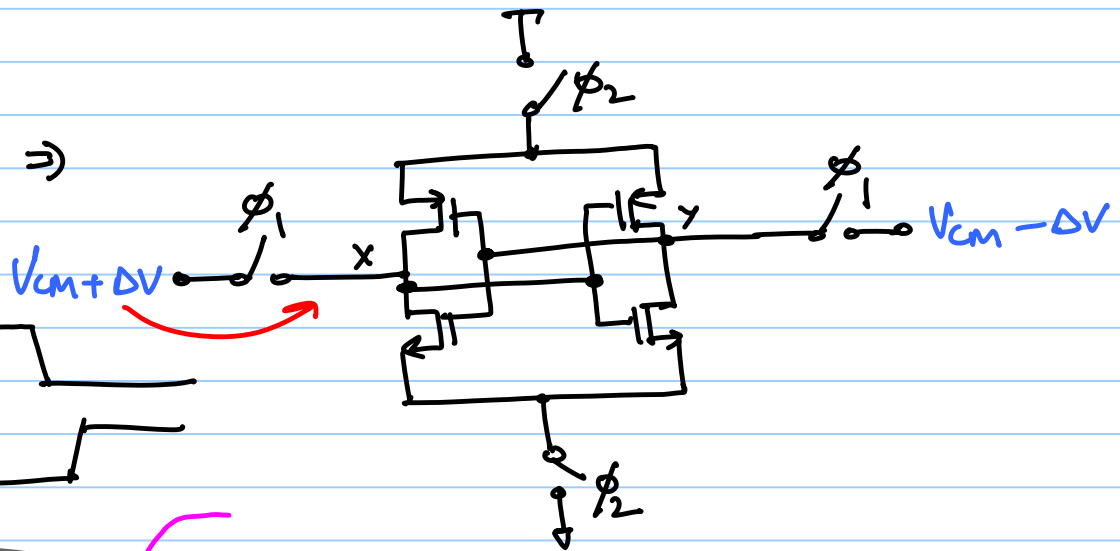
EECE 517 - Lecture 8

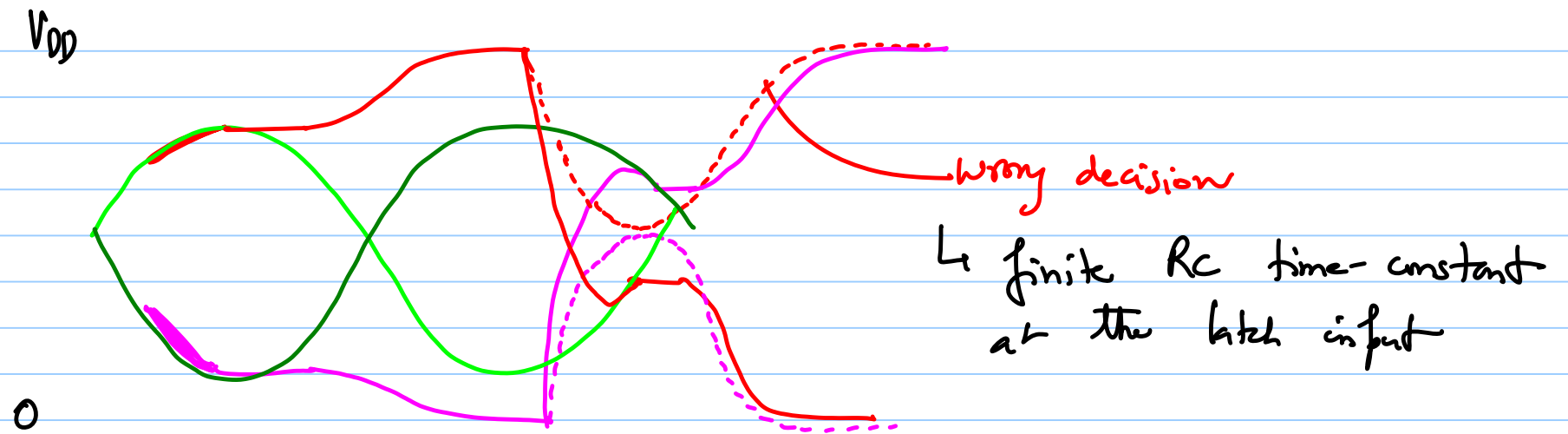
Note Title

2/9/2017

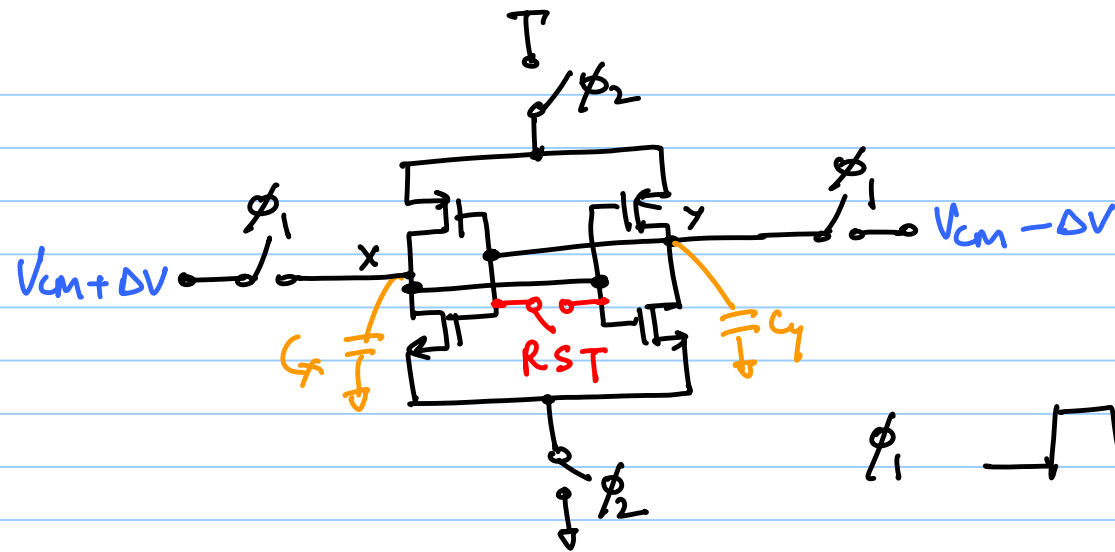


$\Rightarrow$



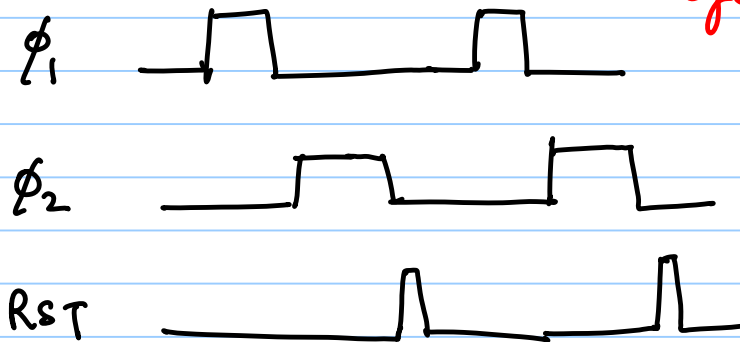


× Latch has hysteresis
↳ remembers the past
which affects future decisions
↳ memory effect



During the RST phase the
latch outputs will be shorted
and reach V_{cm}

→ ready to track the
inputs in the next
cycle



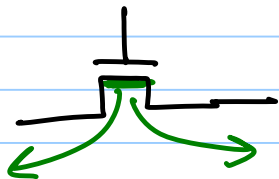
Cross-coupled
⇒ Latch → offset

- static mismatch → random offset (1 σ)
- dynamic mismatch → mismatched capacitive loads

* or large change in the inputs
Common-mode voltage relative
to the natural CM of the
circuits



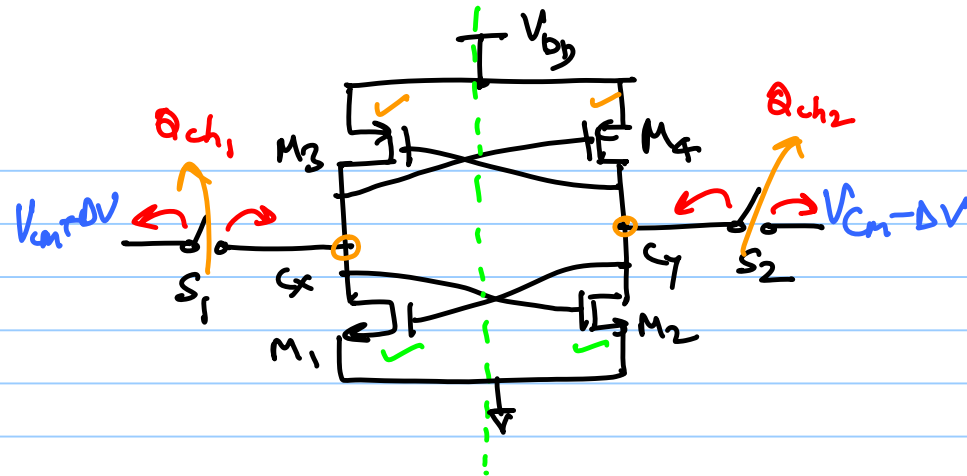
Static offset



charge injection

Q_{ch}

V_{os} due to M_1 & M_2



$\Delta Q = Q_{ch1} - Q_{ch2} \Rightarrow$ mismatch in charge injection from S_1 & S_2

$$V_{os,2} = \underbrace{\Delta \check{V}_{THN,1,2}} + \frac{1}{2} \underbrace{\frac{\Delta(w/L)}{w/L}} \underbrace{(V_{AS,1,2} - V_{THN,1,2})}_{\text{overdrive voltage}} \rightarrow \text{'Razavi Book'}$$

$V_{ov} = V_{AS} - V_{THN}$

Variance

$$\sigma_{V_{os,1,2}}^2 = \sigma_{\Delta V_{THN}}^2 + \frac{1}{2} \frac{\sigma_{\Delta(w/L)}^2}{(w/L)^2} \cdot V_{ov}^2$$

$$\sigma_{V_{os}}^2 = \sigma_{V_{os,1,2}}^2 + \sigma_{V_{os,3,4}}^2 + \underbrace{\left(\frac{\Delta Q}{C_x}\right)^2}_{C_{x,y}}$$

$$\sigma_{V_{os}} = \sqrt{(\quad)} = \underline{\underline{90 \text{ mV}}} \quad \text{Huge!}$$

0.5μm Data

$$\sigma_{\Delta V_{THN}} = 5 \text{ mV} \times$$

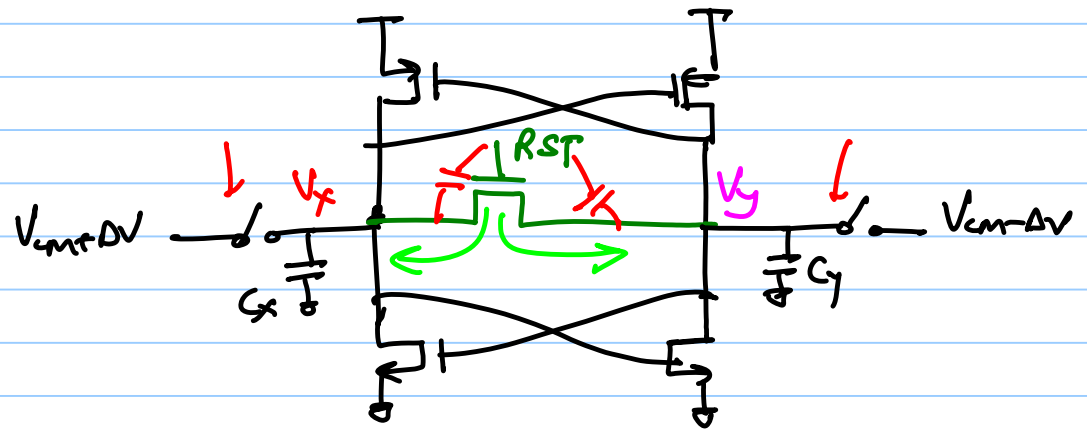
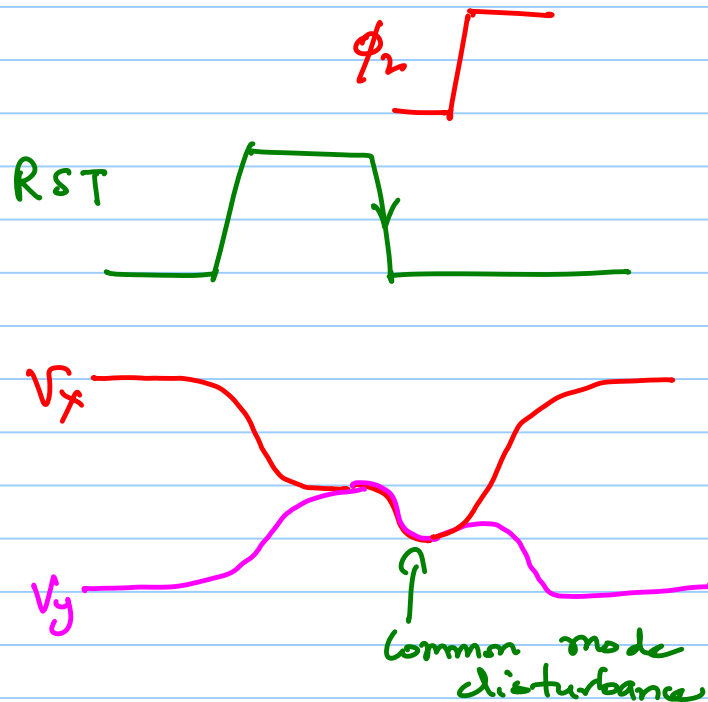
$$\frac{\sigma_{\Delta(w/L)}}{(w/L)} = 0.1 \checkmark$$

$$V_{ov} = 1 \text{ V}$$

$$\Delta Q = 0.5 \text{ fC}$$

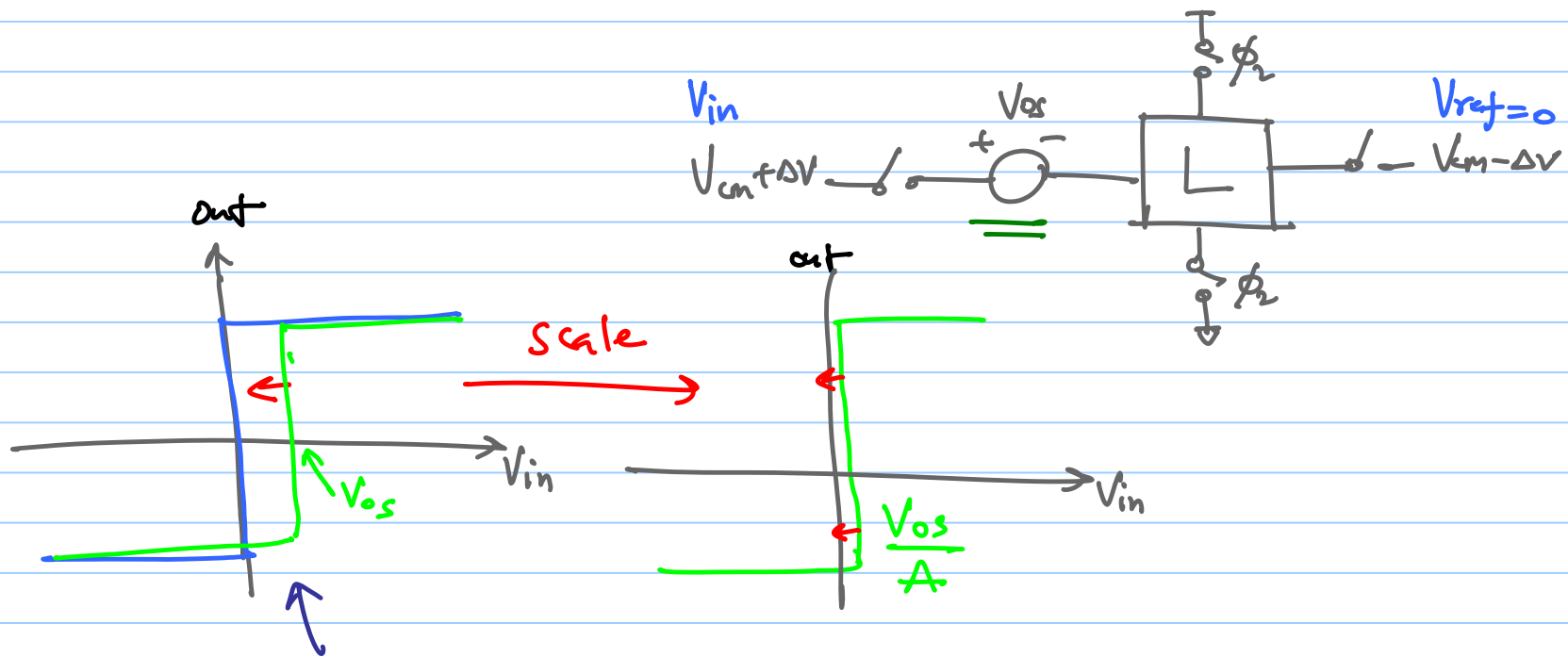
$$C_{x,y} = 100 \text{ fF}$$

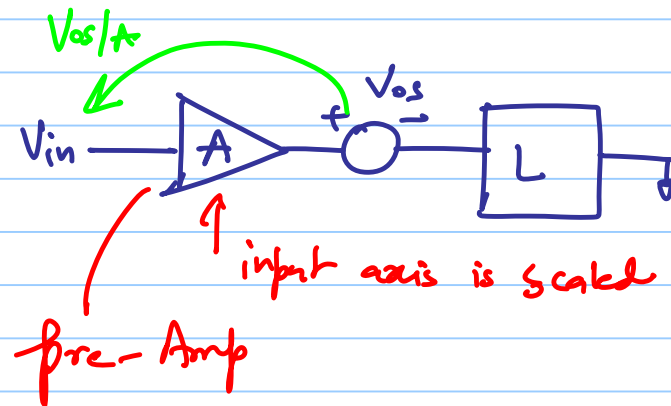
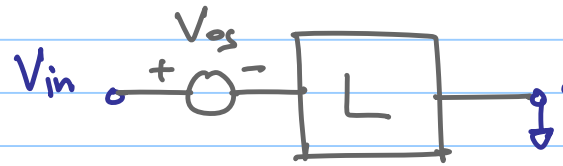
Dynamic offset in a latch



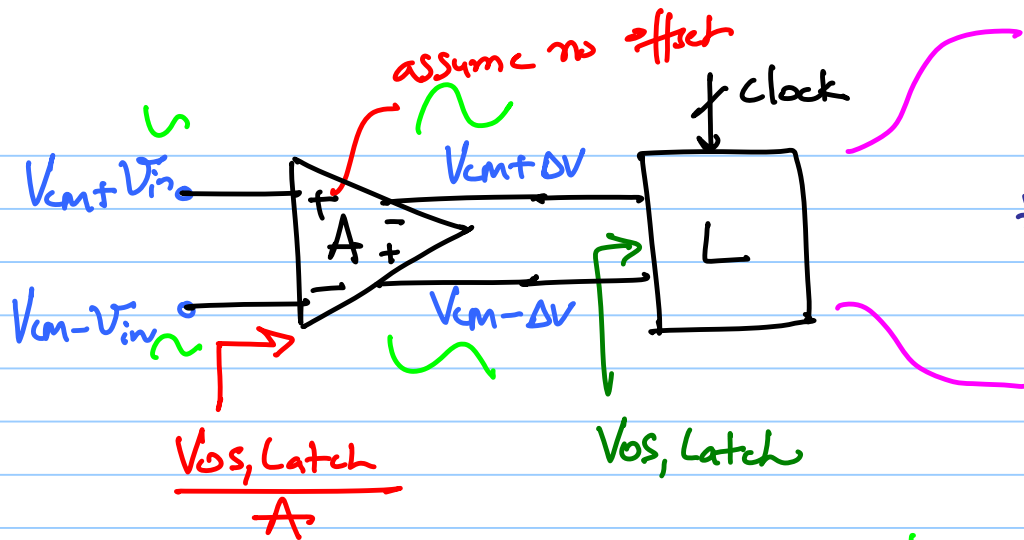
- * charge injection mismatch
- * clock feedthrough

* To use a latch with offset $\rightarrow$ Causes increased INL and DNL in the ADCs



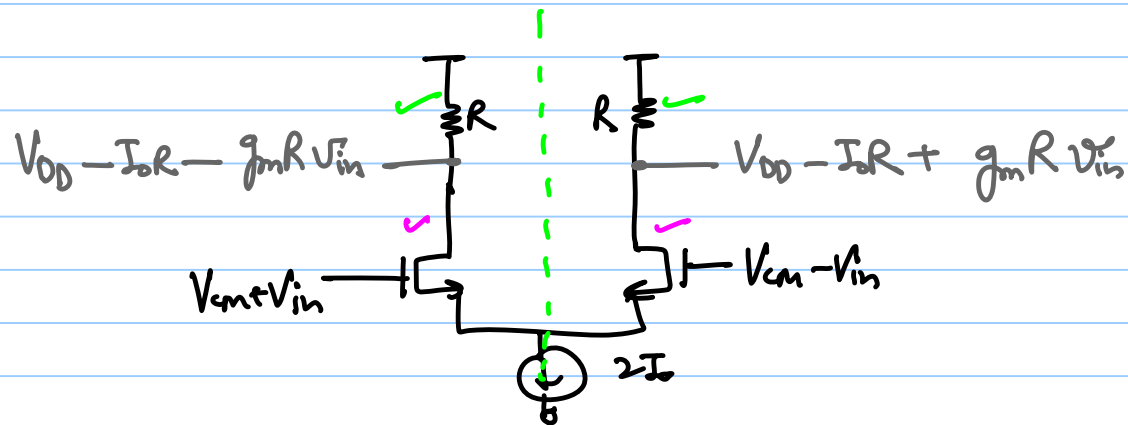


Scale the input axis
by A ,
the threshold
becomes $\frac{V_{os}}{A}$

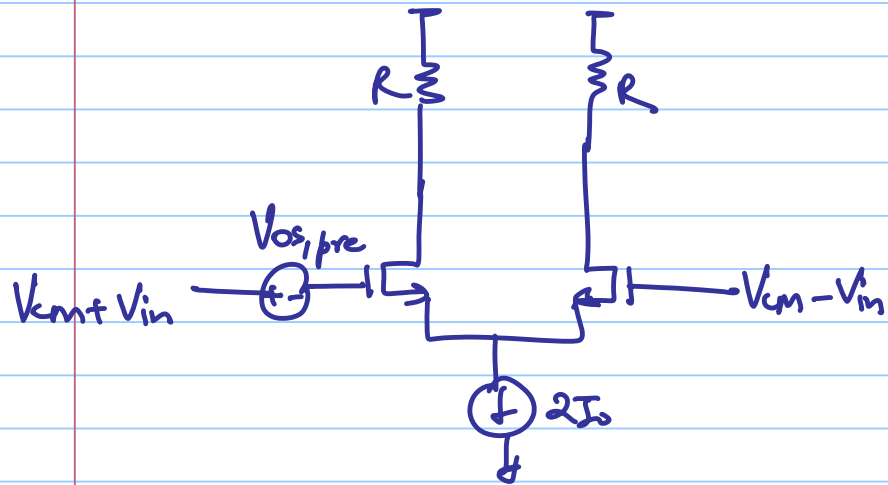


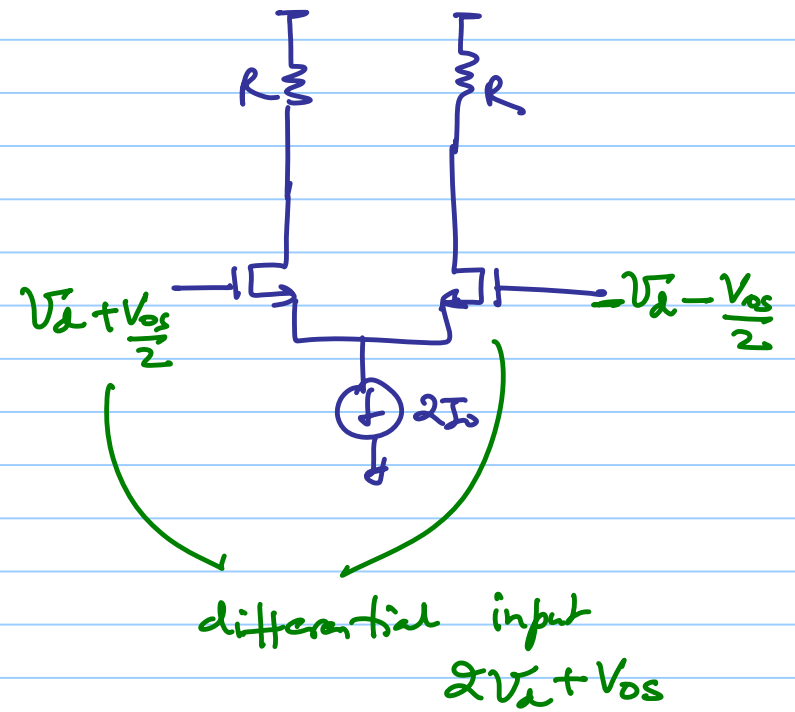
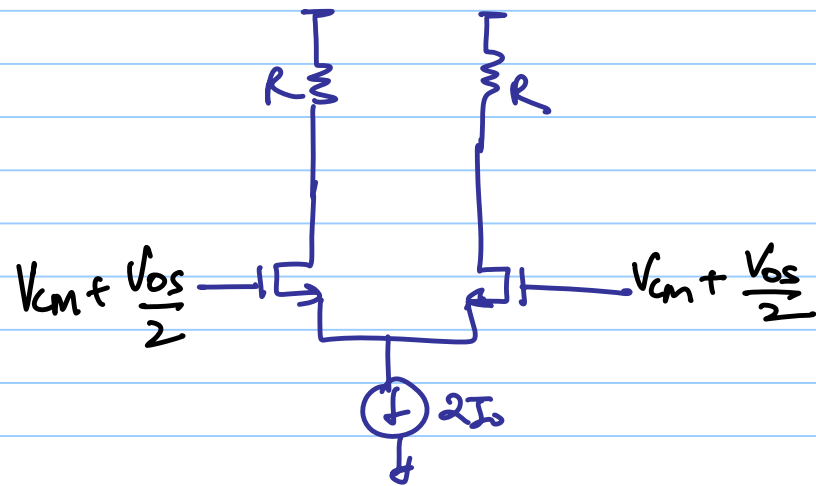
* pre-Amplifier reduces the latch offset

pre-amp :



Issue: Mismatch in the preamp:





random voltage quantities

$$V_{\text{offset}} = V_{\text{os, preamp}} + \frac{V_{\text{os, latch}}}{A}$$

$$\sigma_{\text{offset}}^2 = \sigma_{V_{\text{os, preamp}}}^2 + \frac{\sigma_{V_{\text{os, latch}}}^2}{A^2}$$

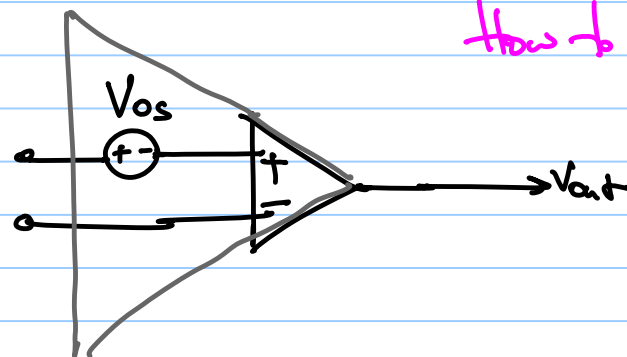
We use large A to reduce Latch offset

↳ reduce $\sigma_{V_{\text{os, preamp}}}$ → Large size of input devices

Preamplifier Offset Cancellation

$$\sigma_{V_{os}} = \sqrt{\underbrace{\sigma_{V_{os, \text{pre-amp}}}^2}_{\text{offset}} + \frac{\sigma_{V_{os, \text{latch}}}^2}{A^2}}$$

* Needs to cancel $V_{os, \text{pre-amp}}$



How to measure V_{os} ?

Assume
 $A \rightarrow \infty$

$V_{os}?$

