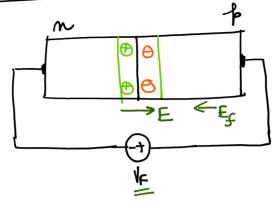
ECE310- Lecture 8

Monday, January 29, 2018 10:28 AM

for junction forward Bias:



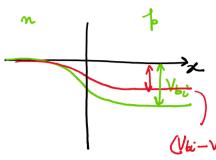
the reverse or zero bias, the potential/Energy barrier
Stopped the flow of carriers

Like howers the potential barrier
by weakoning the electric field

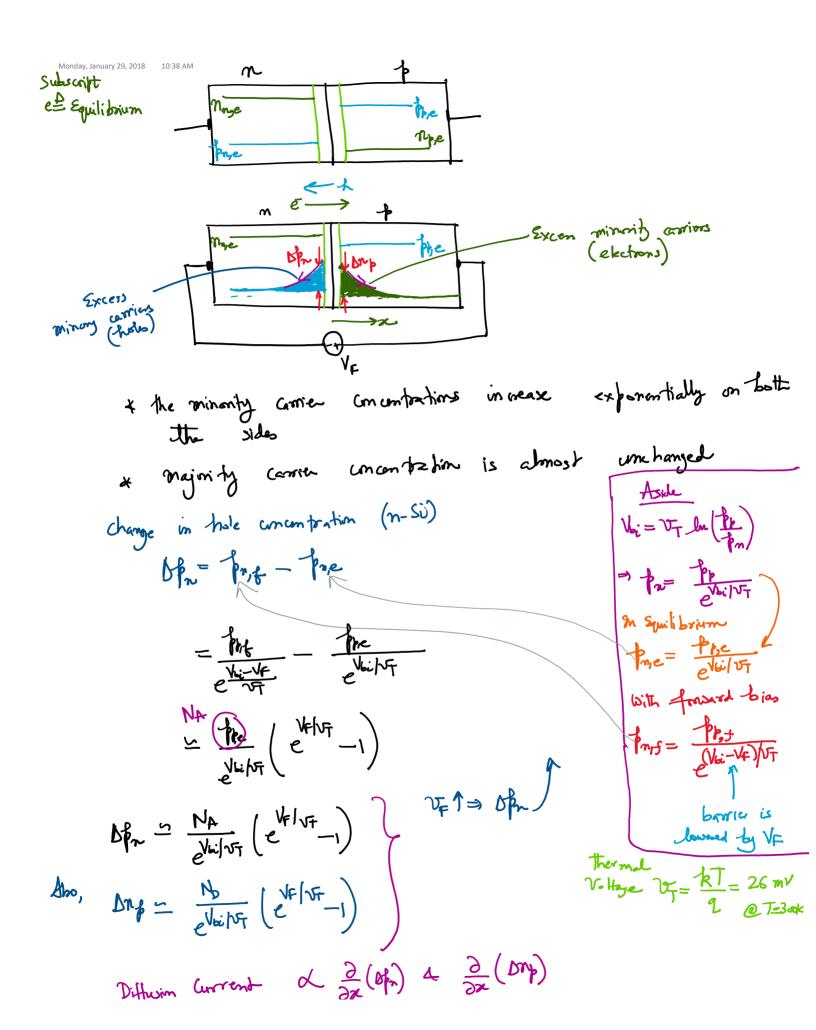
Les Allows greater diffusion arrent

Current flow is due to DIFFUSION.

net electric field (E-Ef)



Energy barrier is reduced by the forward bias NOHoja, VF.

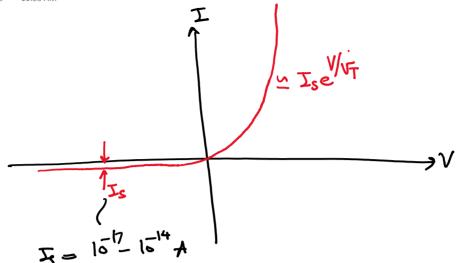


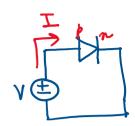
Diffusion Current $\alpha \frac{\partial}{\partial x}(\partial x) + \frac{\partial}{\partial x}(\partial x)$ Ital & ethills (ether -1) + who (ether -1) Device Physics Course / book L= TD. Tc

(arrier lifetime electron and holes

I/V characteristics of the Diode

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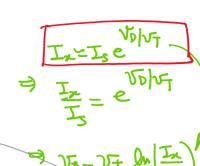


I= Is(e -1)

Fun Blas: I = Ise

REV PIAS: I = -Is , a very very small convent

$$V_{x}=3V$$
 $I_{s}=10^{-16}A$
 $R=1kN$



(a) Assume
$$\sqrt{b} = 0.75V$$

Compark $I_{2} = \frac{V_{2} - V_{0}}{R} = 2.25 \text{ m/A}$

$$V_{3} = V_{7} \ln \left(\frac{z_{n}}{z_{s}}\right) = 799 \text{ mV}$$

$$I_{n} = \frac{\sqrt{x-\sqrt{p}}}{R} = 2.201 \text{ mA}$$

$$\vdots$$

Load-line Amlysis

Va = InR+ VD

$$\frac{Va-Vb}{R} = Iac$$

