Dependent Sources

voltage controlled voltage source VCVS

VCCS

+ 5-

two-part

CCCS

i<sub>l</sub> v

iz=Bi

CCVS

i<sub>l</sub>

\$ 75= Li

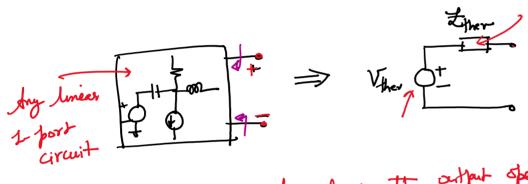
Defendent sources can model the microelectronic devices.

## Therenin and Norton Equivalents

Hevenin & Norton theorems greatly simplify algebra and provide insight into the circuit

## Therenin's Theorem:

d linear one-fort network and replace by an equivalent circuit consisting of one voltage source in series with an impedance

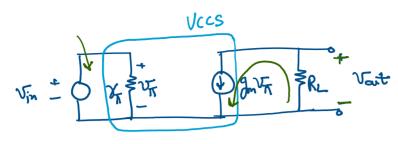


of there is obtained by leaving the output open and calculating the open circuit voltage at this post.

\* Ther is determined by Setting ALL independent voltage and convent sources to zero and then calculate the impedance between the two nodes. > Short All independent vottage sources

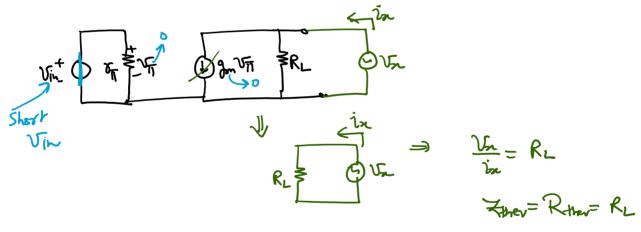
spon All independent current sources & Be careful with dependent sources

Ex.



\* Vant = - 9 VT RL

\* for their calculation, we set vin=0 and apply a test voltage source the at the output post Lis determine the current, in = there is



Exercise => Repeat Ex 1.9 4 1.10

## Norton's Theorem =

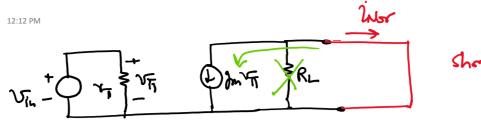
North Equivalent Circuit

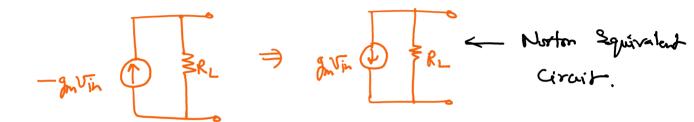
North Equivalent Circuit

\* From Is Calculated as the same way as the Finew Last Set All indep sources to zero and calculate the impedance at the output port.

\* The equivalent current, liver, is obtained by Shorby the output fort and computing the "short circuit" current that flow through it.

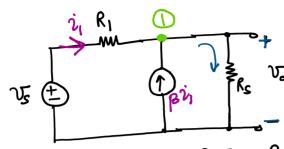
Ex.



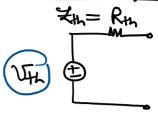


See Ex. 1.12





Find Theresin Equivalent circuit



B=50, R=20ku, R=1km

VILO open circuit voltage at the output terminals

Appy KCL at node 1

$$\beta \dot{i}_1 + \dot{i}_1 - \frac{y_0}{R_0} = 0 \longrightarrow 0$$

$$\dot{v}_{i} = \frac{v_{s} - v_{s}}{R_{i}} \longrightarrow 2$$

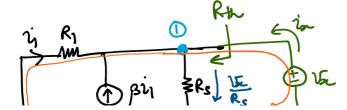
Eliminite in in & D and D:

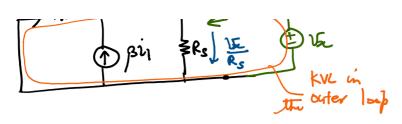
$$\left(\beta+1\right)\left(\frac{V_3-V_6}{R_1}\right)-\frac{V_6}{R_5}=0$$

$$\frac{(\beta H)V_S}{R_1} = \left[\frac{\beta H}{R_1} + \frac{1}{R_S}\right]V_0$$

$$\Rightarrow \boxed{\mathcal{V}_{1k} = \mathcal{V}_{0} = \frac{(\beta+1)R_{s}}{(\beta+1)R_{s}+R_{1}}} \mathcal{V}_{s} = 0.718\mathcal{V}_{s}$$

for calculating RAD > Short indep voltge sources





Apply KCL at 1):

Need to eliminate in

Taking kvl in the outer look we get 
$$-V_{k} - v_{i}R_{i} = 0$$

$$\Rightarrow v_{i} = -\frac{V_{k}}{R_{i}} \longrightarrow 0$$

from q. D4D we gets

$$i_{R} - (\beta+1) \frac{y_{R}}{R_{1}} - \frac{y_{R}}{R_{1}} = 0$$

$$= i_{R} = \sqrt{n} \left[ \frac{\beta+1}{R_{1}} + \frac{1}{R_{2}} \right]$$

$$= \frac{R_{1}}{R_{1}} \times R_{2}$$

$$= \frac{R_{1}}{R_{2}} \times R_{3}$$

$$= \frac{R_{1}}{R_{3}} \times R_{3}$$

$$= \frac{R_{3}}{R_{4}} \times R_{3}$$

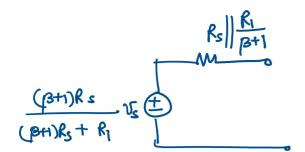
$$= \frac{R_{1}}{R_{4}} \times R_{3}$$

$$= \frac{R_{3}}{R_{4}} \times R_{4}$$

$$= \frac{R_{3}}{R_{4}} \times R_{4}$$

$$= \frac{R_{4}}{R_{4}} \times R_{5}$$

$$= \frac{R_{5}}{R_{4}} \times R_{5}$$

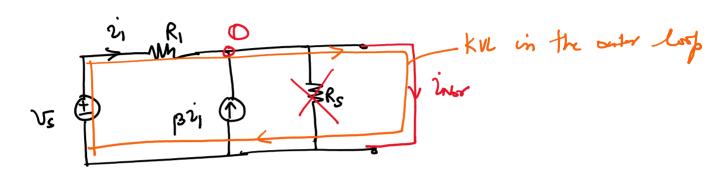


## Method Wortm

 $R_{nor} = R_{therr} = R_{c} \left| \frac{R_{l}}{(R_{c} + l)} \right|$ 

Find Wor

Shord-circuit output current



KCL at node 1

$$-2inr + \beta i_1 + 2i_1 = 0$$

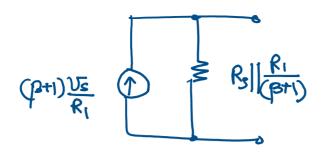
$$\Rightarrow 2inr = (\beta + 1) 2i_1 \longrightarrow D$$
Need to find if

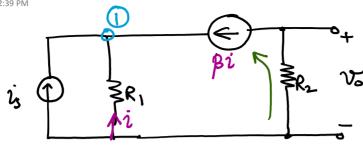
KVL in the outer loop

$$-v_s + i_1R_1 = 0$$

$$\Rightarrow \quad \hat{v}_1 = \frac{\hat{v}_1}{R_1} \longrightarrow \mathcal{E}$$

$$in = (\beta+1) \frac{V_s}{R_1} = 2.55 \text{ m/s}$$





Open circuit voltage at the

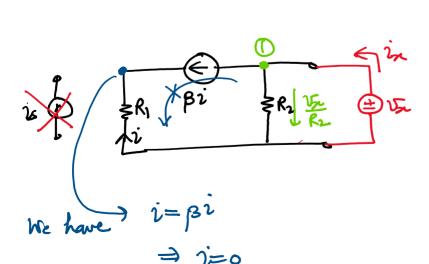
KCL at note 1

$$= \dot{j} = -\frac{\dot{j}_s}{(\beta+1)} \longrightarrow 2$$

from D4D veget

$$V_{5} = \frac{\beta R_{2}}{\beta + 1} \cdot \hat{v}_{5} \Rightarrow V_{7} = \left(\frac{\beta}{\beta + 1}\right) \hat{v}_{5} R_{2}$$

for Rth, open "is" =) independent current source



$$\frac{1}{2} \frac{\sqrt{2}}{2} = R_2$$

$$\frac{1}{2} \frac{R_1}{R_2} = R_2$$

$$\frac{1}{2} \frac{1}{2} = \frac{1}{2} \frac{1$$