ECE 310-Lecture 13
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Half wave Rectifier:

$$PIV = 2Vp$$

Full-wave Rectifier:
 $V_R = \frac{V_p - V_{pon}}{R_L C_1 fin} = \frac{I_L}{C_1 fin}$
 $V_R = \frac{V_p - V_{pon}}{R_L C_1 fin} = \frac{I_L}{C_1 fin}$

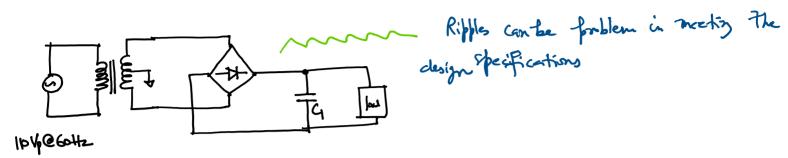
Diode Peak Current:

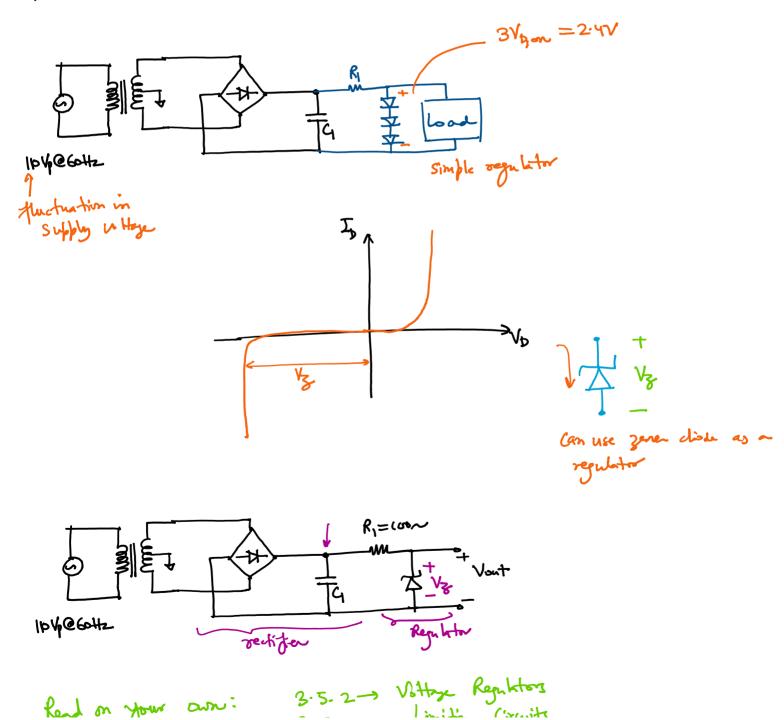
$$T_{p} = \frac{V_{e}}{R_{L}} \left(R_{L} G_{V_{in}} \sqrt{\frac{2V_{in}}{V_{p}}} + 1 \right) \qquad \leftarrow P_{ge} q_{2} - q_{3}$$

$$R_{L} G_{1}^{\uparrow} \Rightarrow T_{p}^{\uparrow}$$

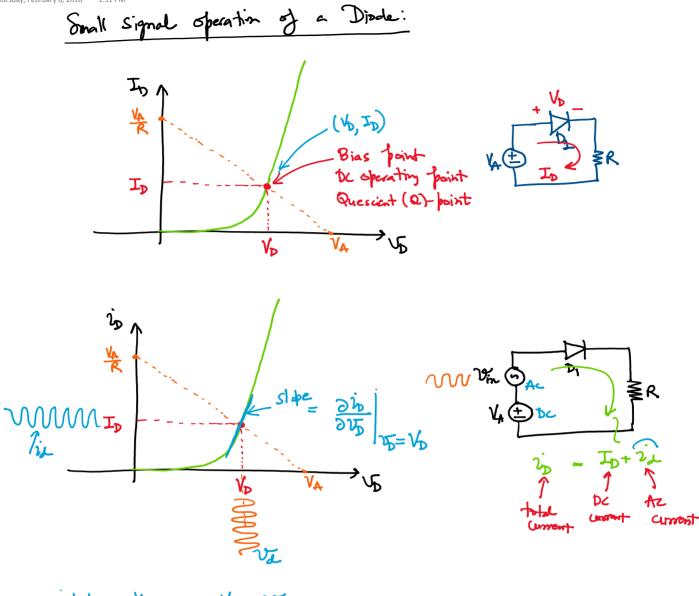
Book Ex 3-22 => Ip = 577 A & Too large] Read Book Syamples: 327 & 3.28

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hend on your own:	3.5.2-> Vottage Regulators 3.5.3-> Limiting Circuits 3.5.4-> Vottage Doublers.
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$$\Delta \dot{i}_{D} = \frac{\partial \dot{i}_{D}}{\partial V_{D}} \cdot \Delta V_{D}$$

$$= V_{D}$$

$$= V_{D}$$

$$= \frac{\partial \dot{i}_{D}}{\partial V_{D}} \cdot V_{D}$$
Smill signel voltige
$$= (\frac{\partial \dot{i}_{D}}{\partial V_{D}}) \cdot V_{D}$$

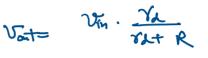
$$= V_{D}$$

$$= (a_{c})$$

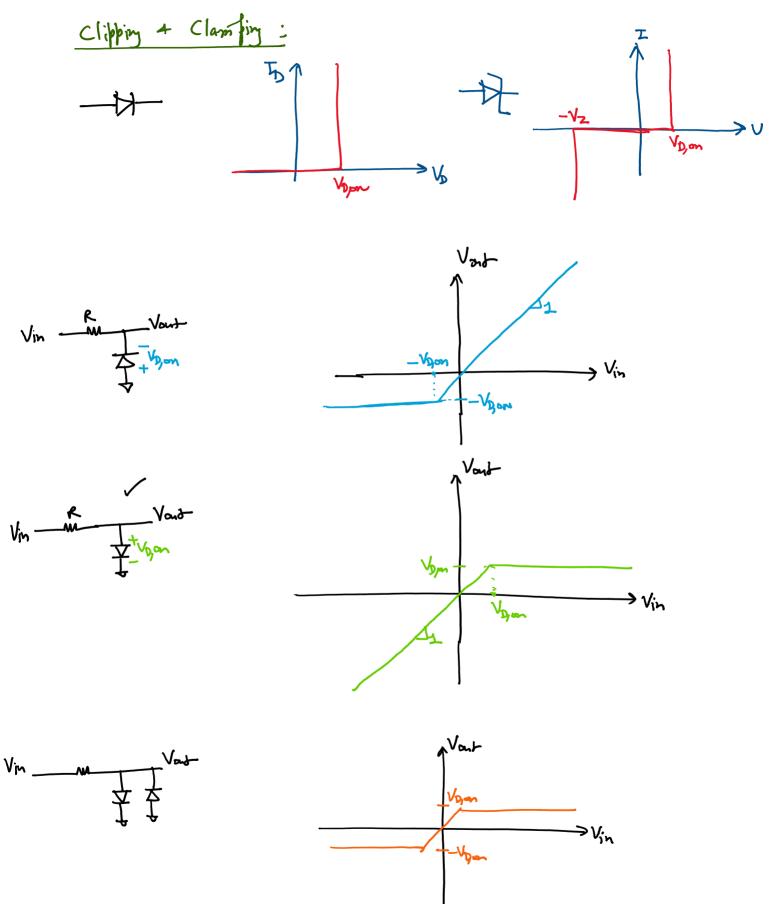
$$\frac{1}{2} \frac{2}{2} \frac{1}{2} \left(\frac{2}{2} \frac{1}{2} \frac{1}{12} \frac{1}{2} \frac{1}{2}$$

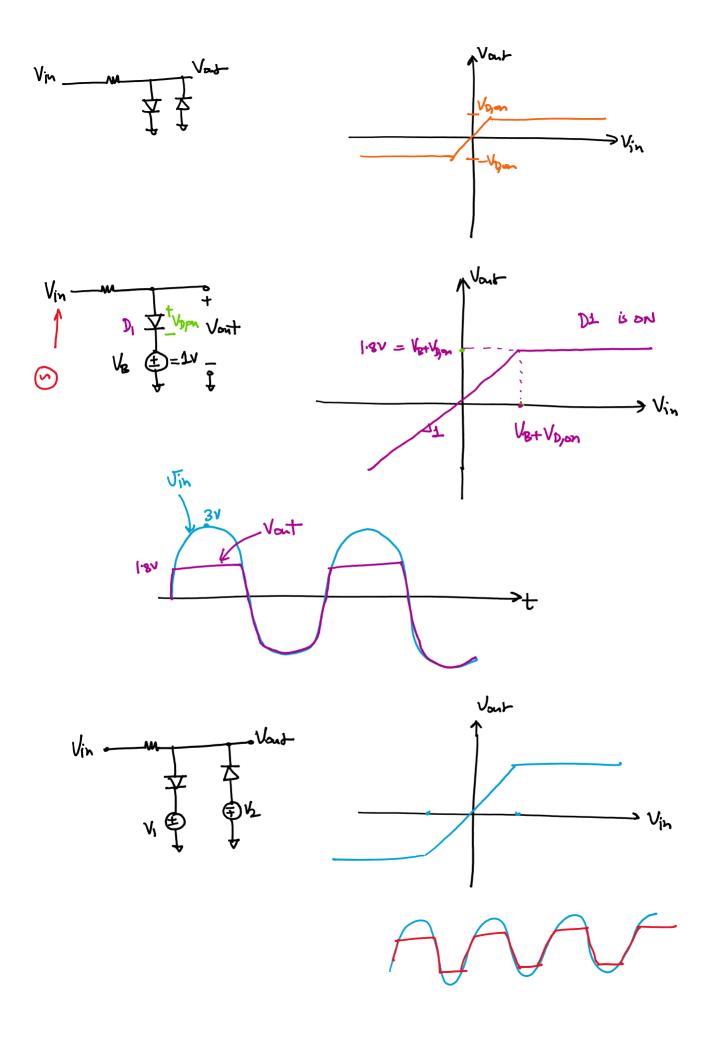
Combine results to get find answers. " Sonall Signal Anlyris" method



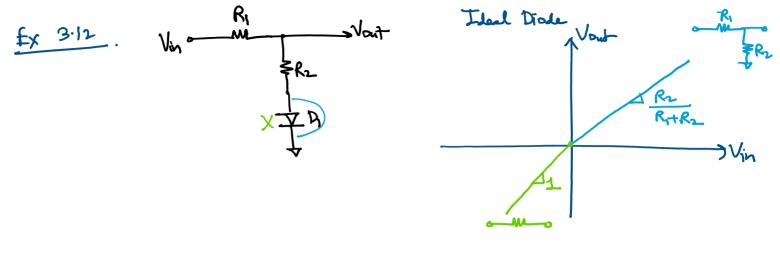


Total output vottige = IDR + Vin Var Total



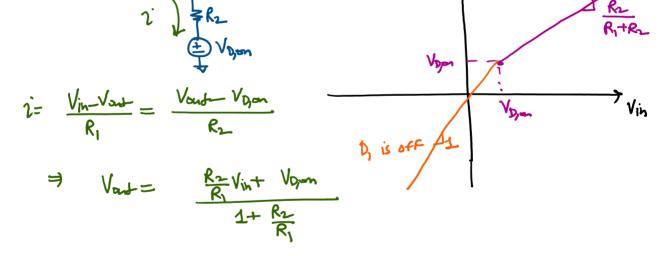


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a)
$$f_{V} V_{in} \langle V_{0,on}, D_{I} \text{ is off } \Rightarrow V_{out} = V_{in}$$

b) $f_{V} V_{in} > V_{0,on}, D_{I} \text{ is on} \Rightarrow$
 $V_{in} \sim \frac{R_{i}}{V_{in}} = V_{out}$
 $V_{in} \sim \frac{R_{i}}{V_{in}} = V_{out}$



* Do Ex 3.15 & 3.16 on your owne Concluses Chapter 3. * Stard with Chipter 6 on Wechesday