1. **(100 points)** Show scheduling of the following code using Tomasulo’s algorithm. Assume two LOAD units, two FP multipliers and three FP adders. Load unit takes one execution cycle for address calculation and a second one for memory access, FP multipliers take 7 cycles and FP adders take 4 cycles.

L.D F2, 0(R2)
L.D F4, 100(R3)
ADD.D F8, F2, F2
MUL.D F6, F4, F8
SUB.D F6, F2, F4