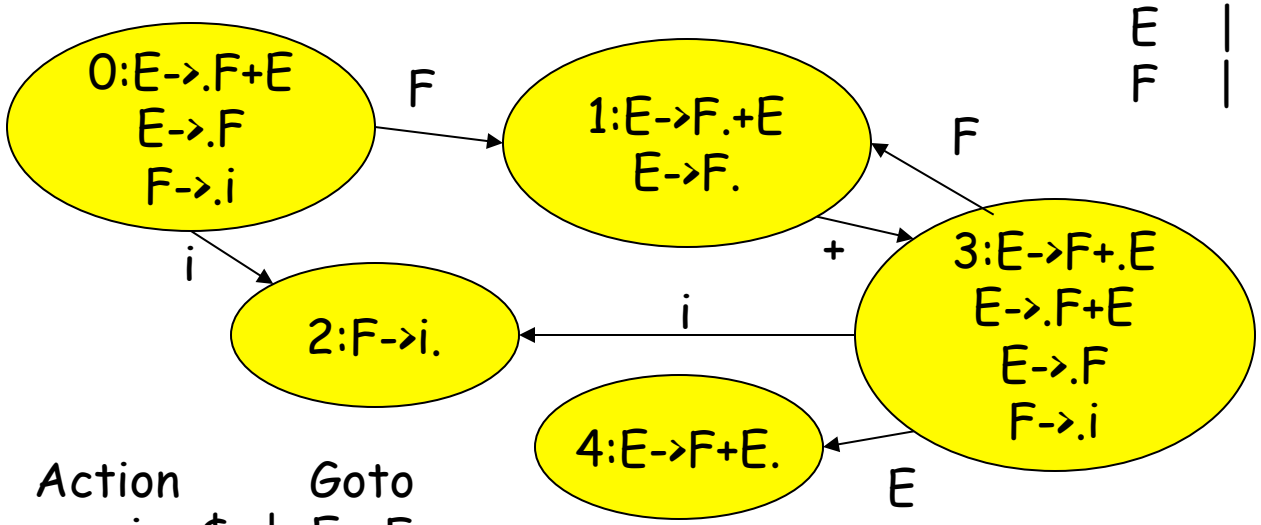
Why augment the grammar?

Consider E-> F + E | F
F -> i

FOLLOW

E	$
F	$, +



	Action			Goto	
	+	i	$	E	F
0		s2		1	
1	s3		?		
2	r3		r3		
3		s2		4	1
4			?		

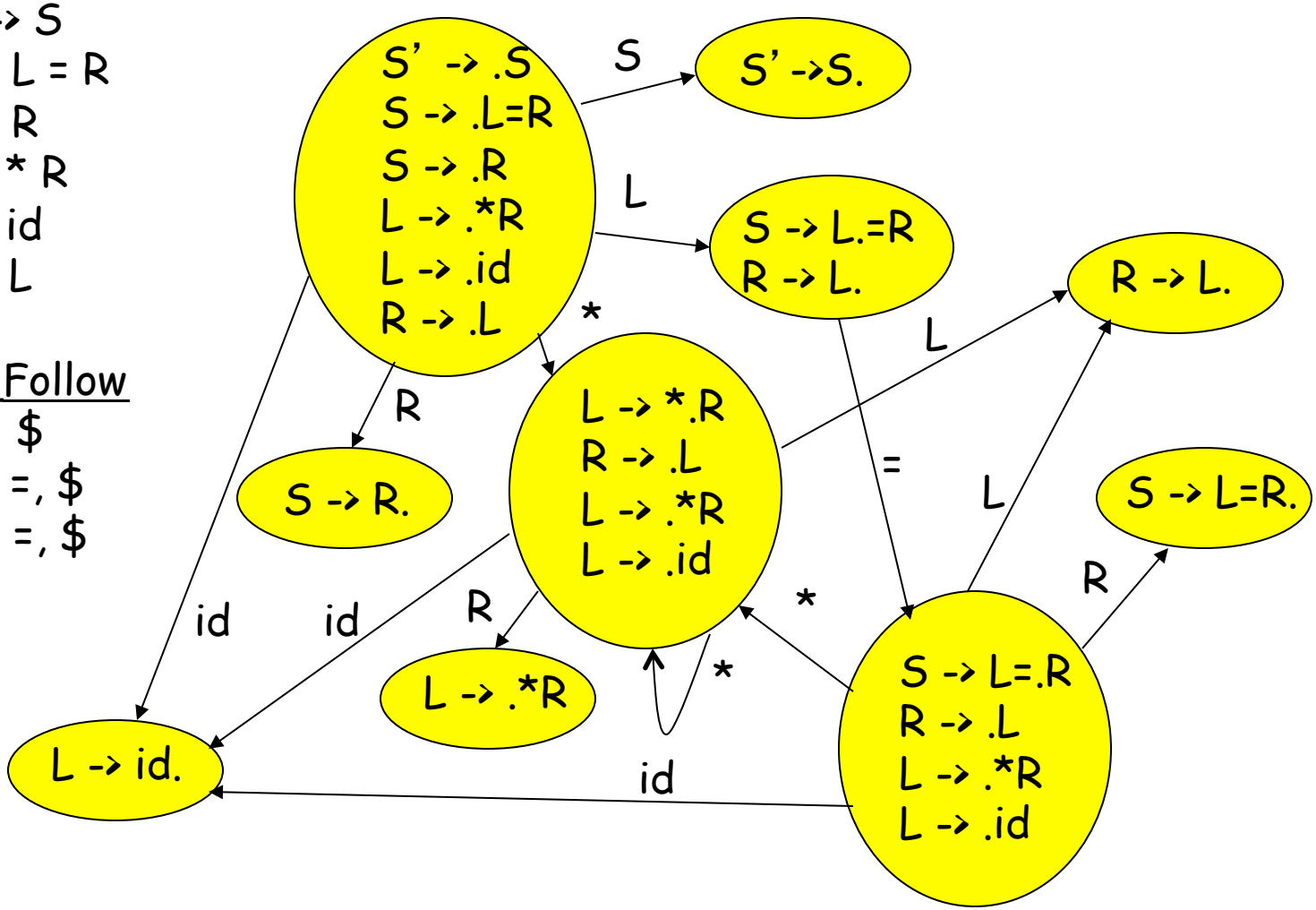
? Reduce E->F or Accept

? Reduce E-> F + E or Accept

Example – not SLR(1)

S' -> S
S -> L = R
S -> R
L -> * R
L -> id
R -> L

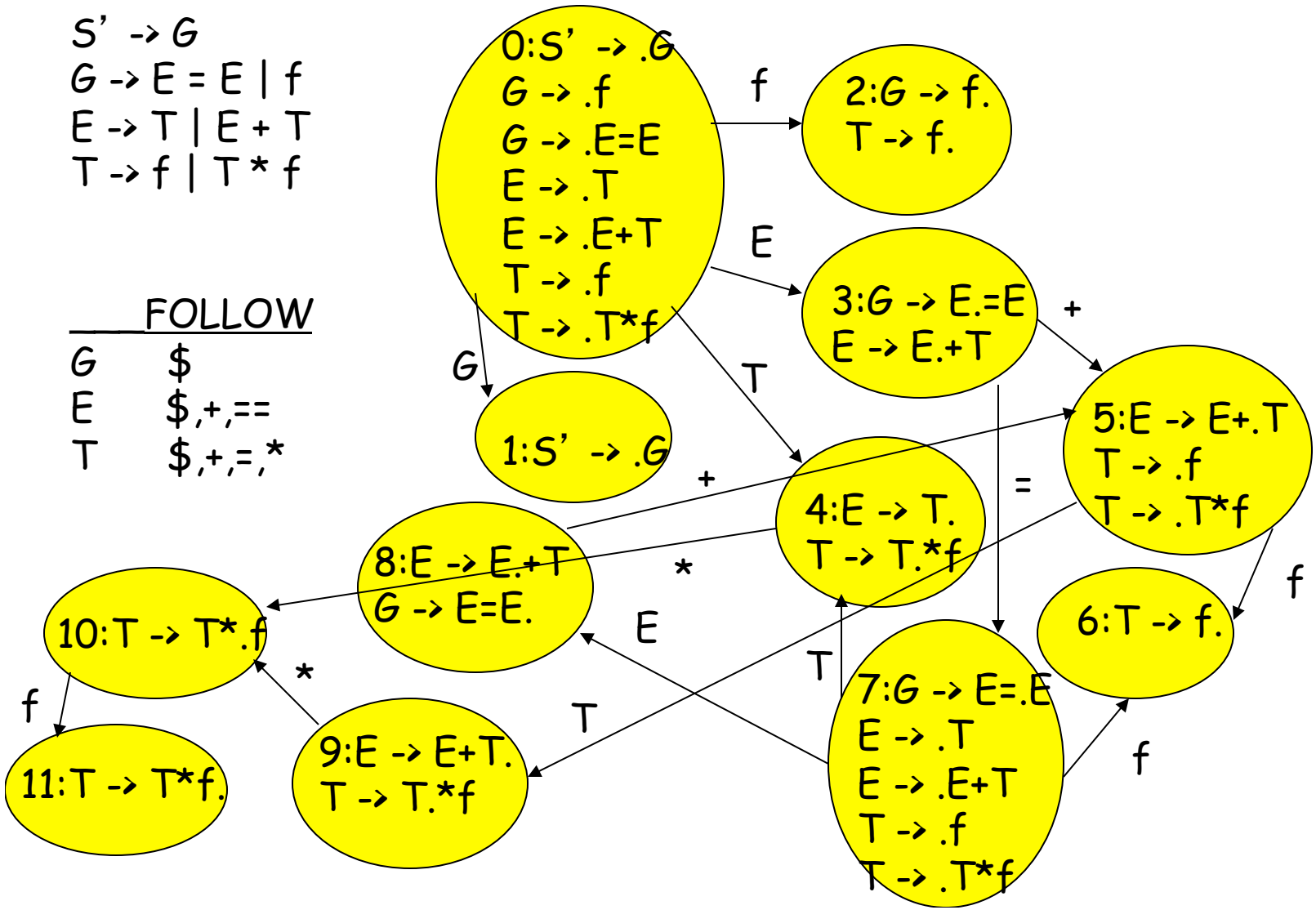
	Follow
S	$
L	=, $
R	=, $



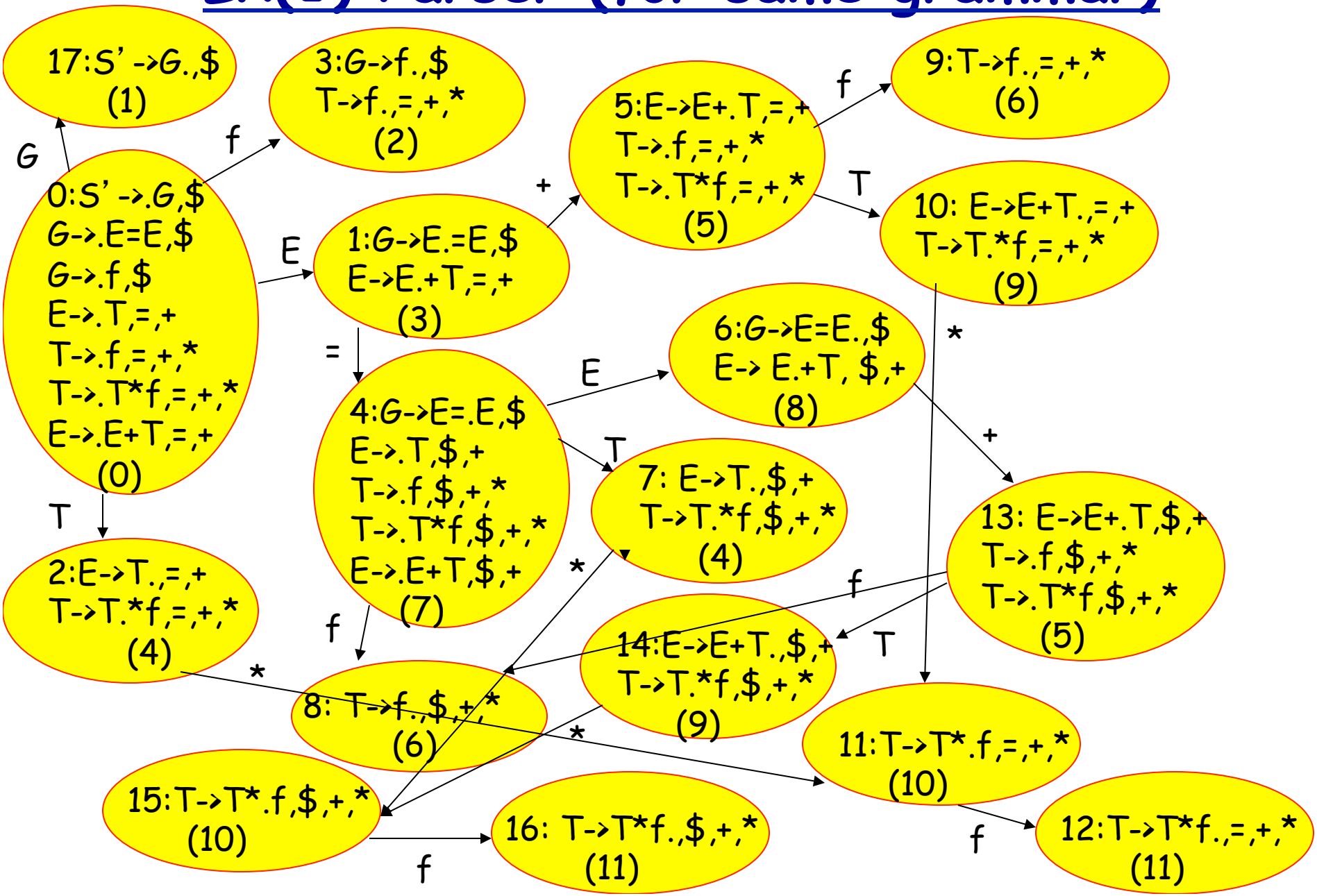
Another Example – not SLR(1)

S' -> G
G -> E = E | f
E -> T | E + T
T -> f | T * f

	FOLLOW
G	$
E	$,+,==
T	$,+,=,*



LR(1) Parser (for same grammar)



LR(1) Parser (for same grammar)

- LR(1) Item

$$[A \rightarrow \alpha.\beta, c]$$

- $A \rightarrow \alpha\beta$ is a production
- c is the lookahead

Constructing DFA for LR(1) Parser

Basic Idea of closure:

For item [A -> α.Xβ, c], where X is nonterminal and production X->δ exists, there exists a rightmost derivation

G => φAuw => $\varphi\alpha$Xβuw

$\Rightarrow$item [X ->. δ, v] is valid for viable prefix $\varphi\alpha$ with v in FIRST(βuw)

$\Rightarrow$If β => the empty string, then v = u.

Closure of set of Items I:

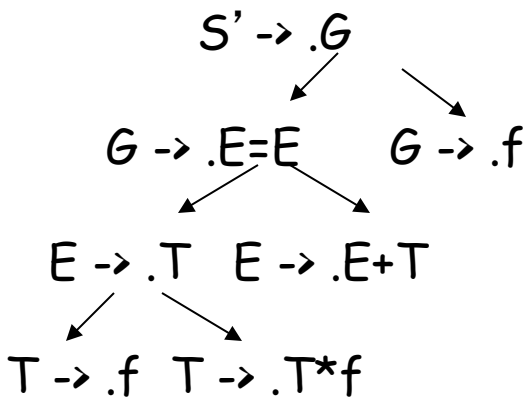
```
cset = I;
repeat
   for each item [A-> α.Xβ, a]  in cset where X is a nonterminal,
         Add all items [X-> . δ,b] for all b in FIRST(βa) to cset
               (if not already in cset)
until no more items added;
```

GOTO(I,X): Assume [A-> α.Xβ, a] in I. Then GOTO(I,X) = closure of items [A-> αX.β, a].

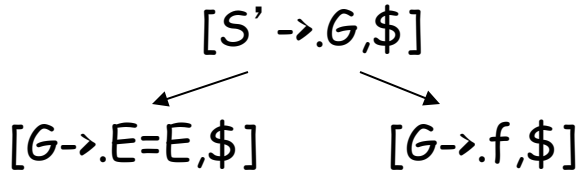
Computing Closure of LR(1) items

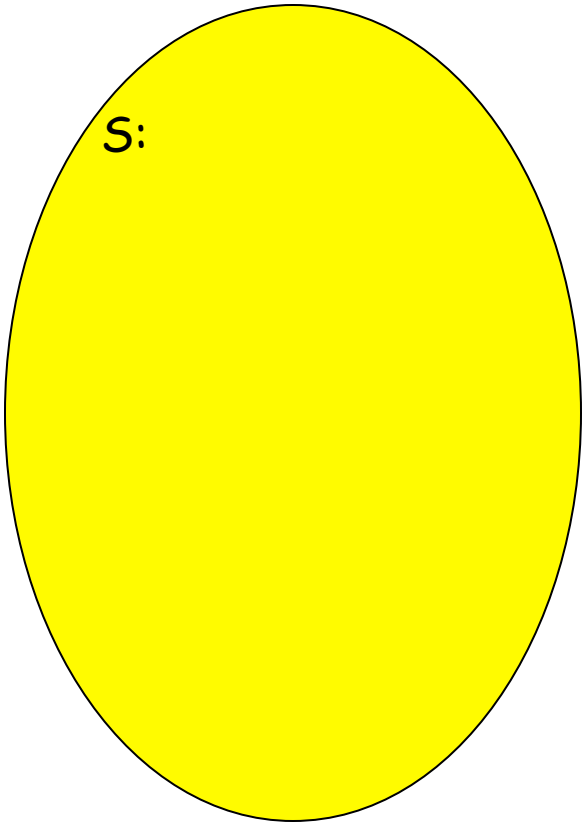
S' -> G
G -> E = E | f
E -> T | E + T
T -> f | T * f

In LR(0) machine:

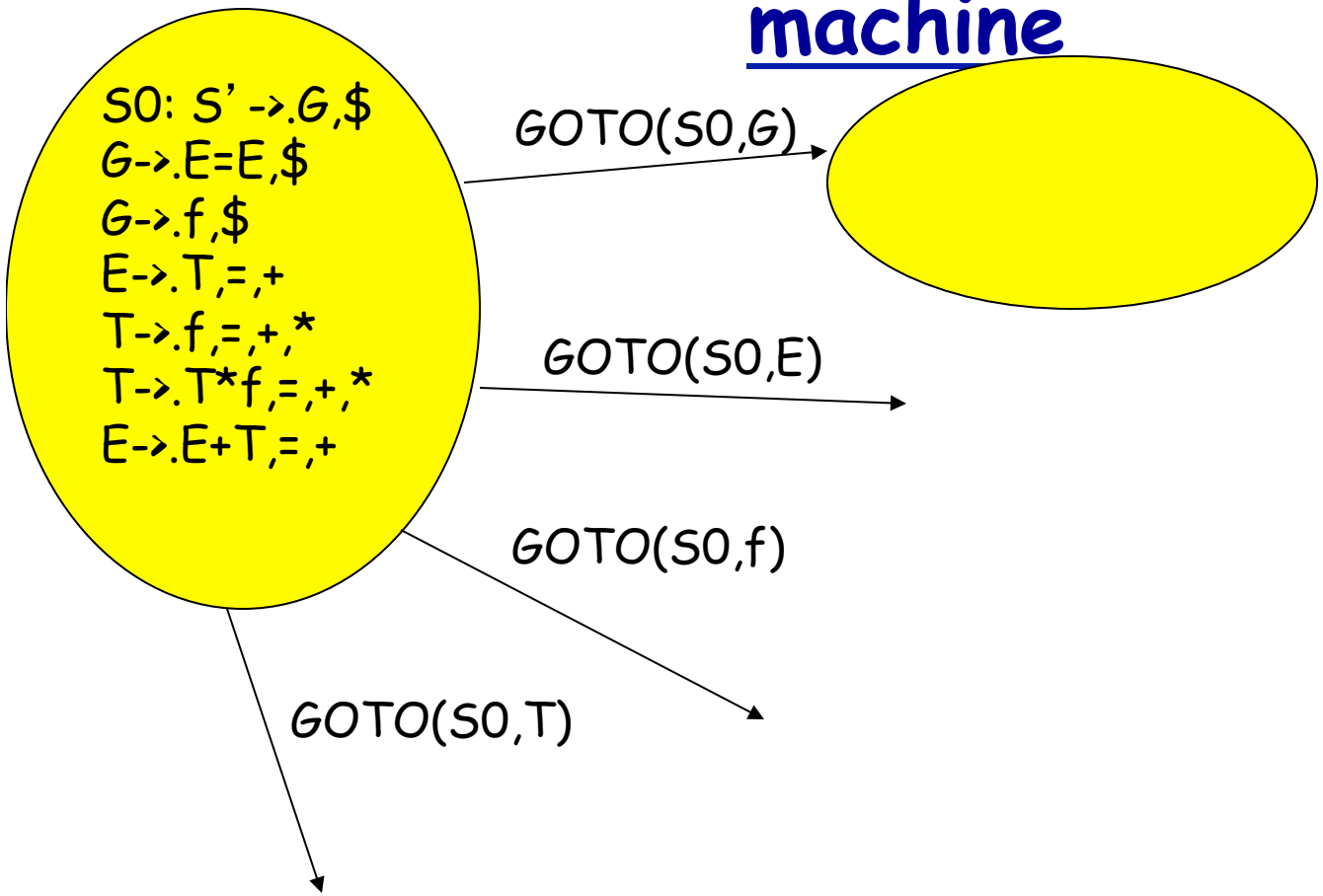


In LR(1) machine:

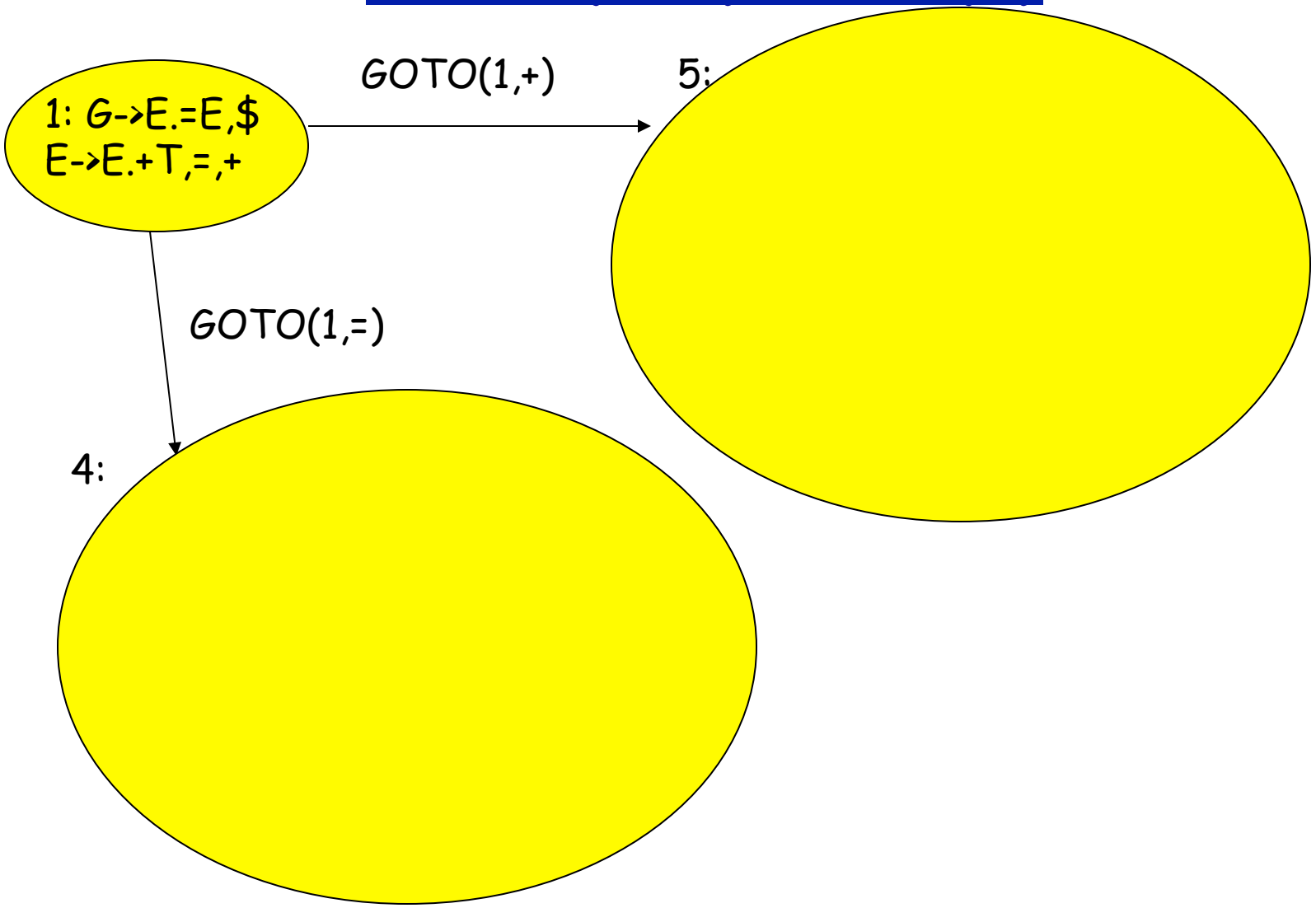




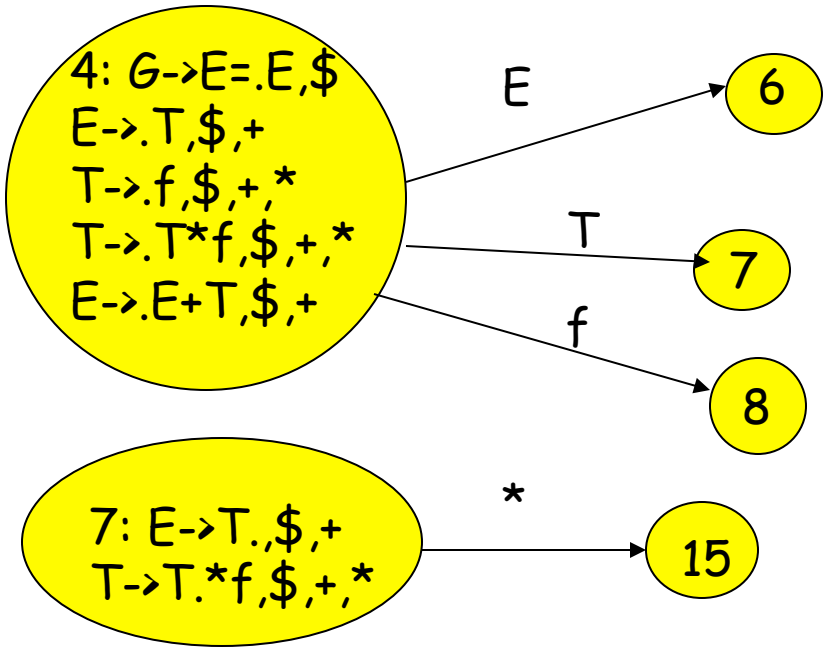
Computing GOTO(I,X) in LR(1) machine



More Extensive Example of GOTO(I,X) in LR(1)

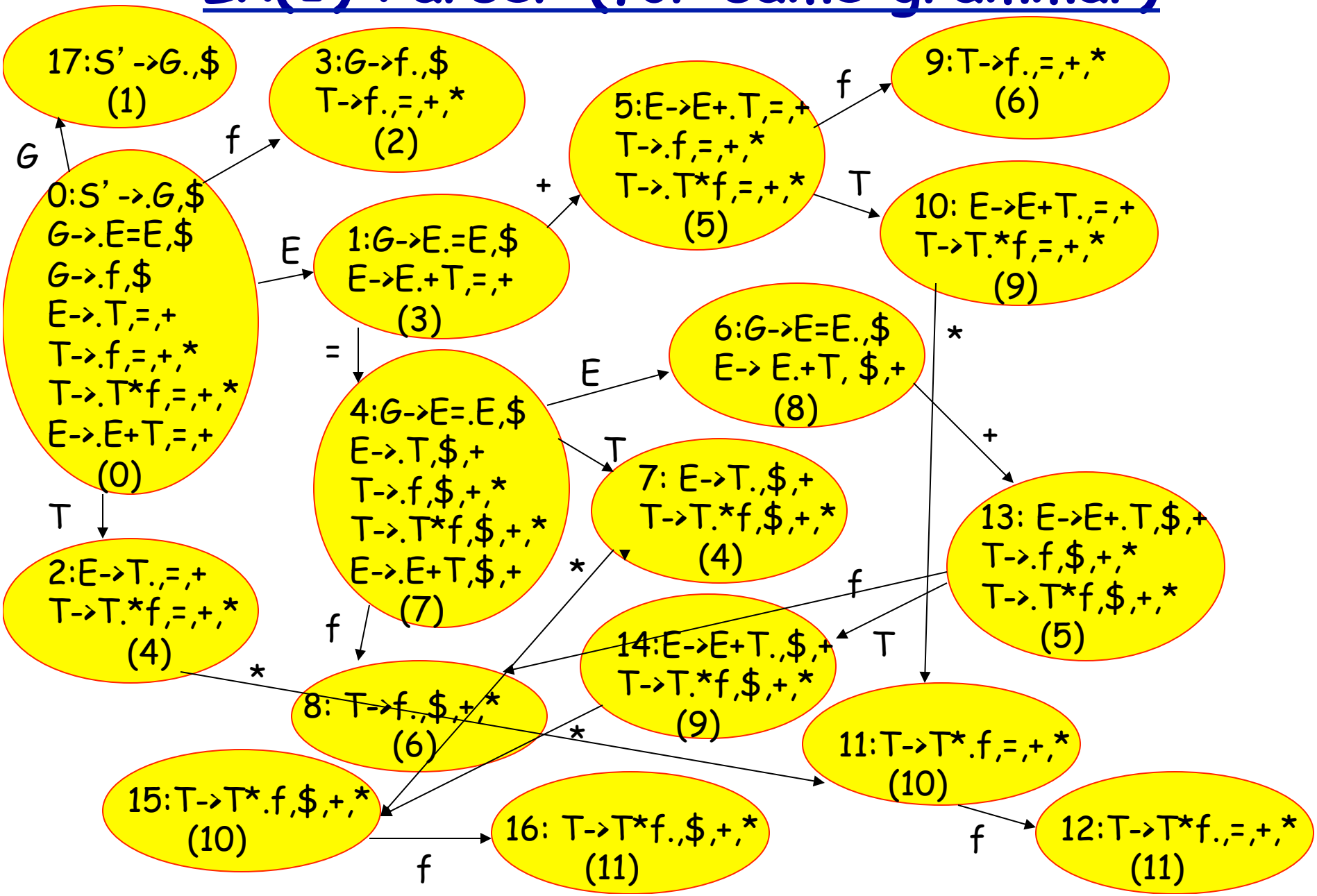


LR(1) Table Construction – Partial Example



State	Action					GOTO		
	f	=	+	*	$	G	E	T
4								
7								

LR(1) Parser (for same grammar)



LALR(1) Grammars and Parsing

Characteristic: Same number of states as SLR(1) with more power due to lookahead in states.

But, less power than canonical LR(1) because less states.

2 Approaches to Table Construction:

* Construct LR(1) sets of items (DFA) and merge states with same core.
* Construct LR(0) sets of items and generate lookahead information for each of those states.

Properties of LALR we will see:

* May perform REDUCE rather than ERROR like LR(1), but will catch error before any more input is processed.
* LALR derived from LR with no shift-reduce conflict will also have no shift-reduce conflict (Shift-reduce conflicts arise from core, not lookahead therefore merging has no effect.)
* LALR merging can create reduce-reduce conflicts not in LR from which LALR derived.

Constructing LALR from LR(1)

1. Construct LR(1) DFA.

2. Identify all sets of states with same core.

   In our example: (2,7), (5,13), (8,9), (10,14), (11,15), (12,16)

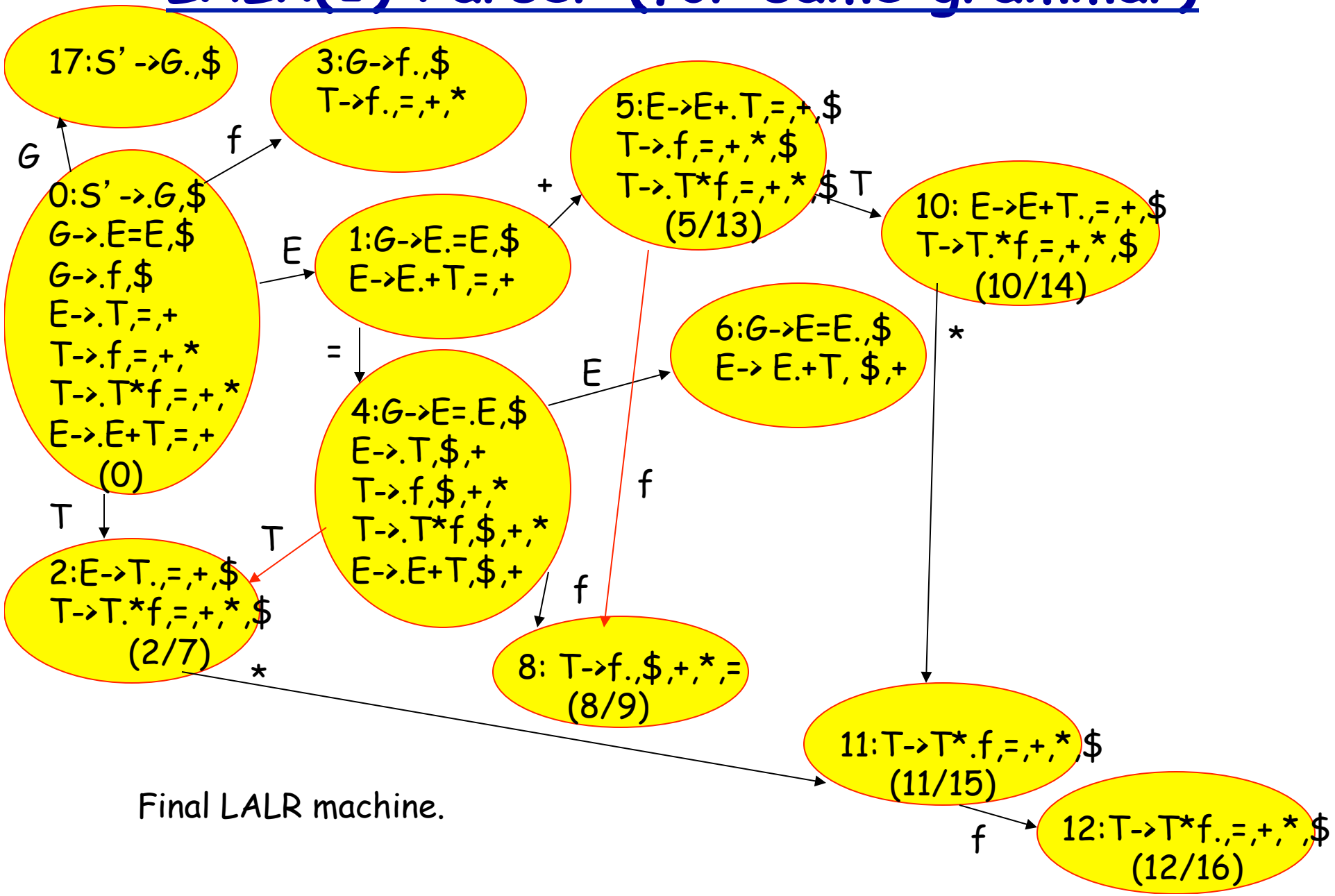
3. Merge the states with the same core into a single state in LALR:
   1. create single state with that core
   2. merge lookaheads from all LR(1) states with that core

   Example:
   LALR(1) state 2: from LR(1) states 2 and 7:

   E -> T.,
   T -> T.*f,

   Add edges to LALR machine based on edges of LR machine.

LALR(1) Parser (for same grammar)



Final LALR machine.

Is a grammar LR(1)? LALR(1)?

* Construct LR(1) (or LALR(1)) parse table using lookahead information.
If there exists any multidefined entries, then the grammar is NOT LR(1) (LALR(1)).

or

* Construct LR(1) (or LALR(1)) DFA. If there exists any inadequate states for which lookahead does not resolve the local ambiguity as below, then the grammar is NOT LR(1) (LALR(1)).

If for all states including $A \rightarrow \alpha., \{a1,a2,\ldots,an\}$
$B \rightarrow \beta., \{b1,b2,\ldots bm\}$

$$\{a1,a2,\ldots an\} \cap \{b1,b2,\ldots,bm\} = 0$$

AND

If for all states including $A \rightarrow \alpha., \{a1,a2,\ldots,an\}$
$B \rightarrow \beta.a\delta, \{b1,b2,\ldots bm\}$

$$\{a1,a2,\ldots,an\} \cap \{a\} = 0$$

then the grammar is LR(1) (LALR(1)).

Property 1: May reduce before error.

Consider string: f+f

LR(1):

Stack	Input	
0	f+f$	
0f3	+f$	
0T2	+f$	
0E1	+f$	
0E1+5	f$	
0E1+5f9	$	ERROR!

LALR(1):

0	f+f$	
0f3	+f$	
0T2	+f$	
0E1	+f$	
0E1+5	f$	
0E1+5f8	$	
0E1+5T10	$	
0E1	$	ERROR! - 2 extra reductions.

Property 2: No shift-reduce in LR => no shift-reduce conflict in LALR.

Assume when merge, we get a shift-reduce conflict in some state:

LALR state: A -> α., a => reduce on a
B -> β.aδ,b => shift on a

This implies that some set Si from LR(1) machine has the item

A -> α., a to be included in this merge.

Since the cores of all states merged together are the same, Si must also contain

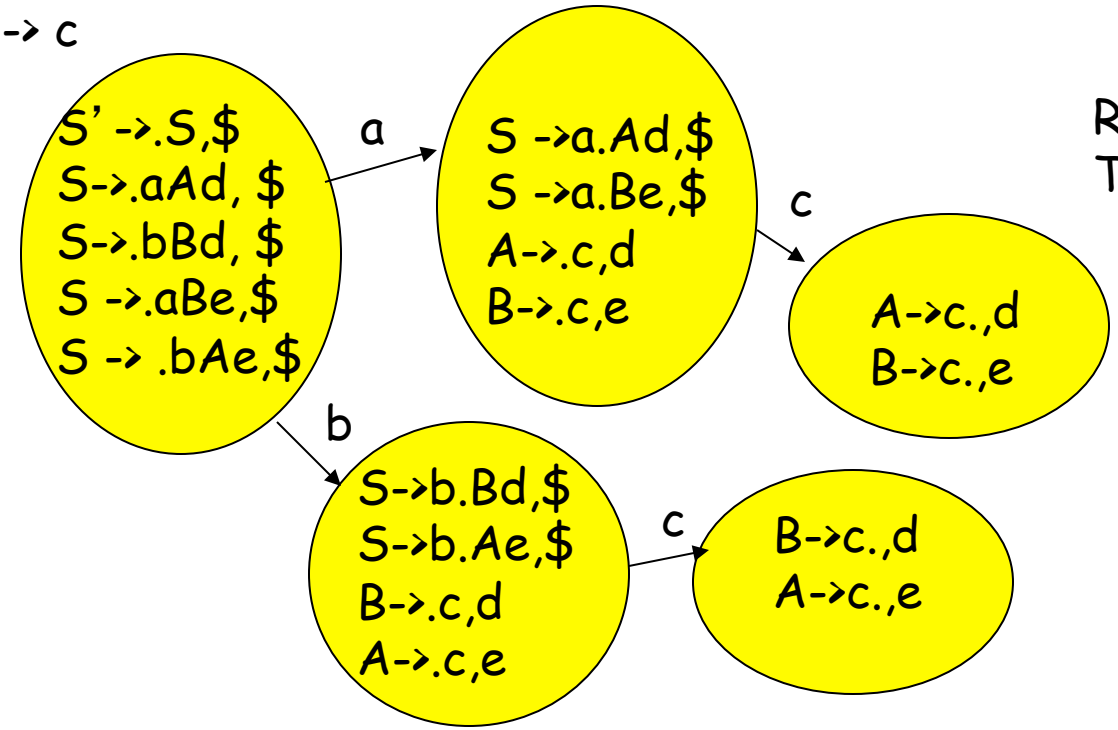
B -> β.aδ,c for some lookahead c.

This implies that the shift-reduce conflict also must exist in Si within the LR(1) machine. Contradiction.

KEY: Merging states cannot cause shift-reduce conflicts in LALR.

Property 3: Merging states for LALR(1) can produce reduce-reduce conflicts.

Consider:
S1->S
S -> aAd | bBd | aBe | bAe
A -> c
B -> c



To create LALR,
merge:
A->c., d,e
B->c., e,d

Reduce-reduce conflict!!
Therefore,
not LALR grammar