

Handout 0: Course Structure and Policies

September 2009

Welcome to CPEG 222. This course covers the introduction to microcontrollers and assembly-language programming. It is targeted primarily for students intending to major in CPEG, ELEG or CISC, though others are welcome.

1 General Information

- Instructor:
 - Michael Davis
 - o Email: davis+class@ece.udel.edu (*Note the "+", this is an anti-spam mailbox.)
 - o Phone: 831-8756
- Teaching Assistants (TAs):
- Graders: TBD
- Textbook: Fabio Pereira, [HCS08-Unleashed-Designers-Guide-Microcontrollers](#), Amazon, 2008
- Lecture: Tue. & Thur., 11:00AM – 12:15PM McDowell Hall (MDH) Room 112
- Project Assignments: Five throughout the Semester. Projects are due along with a project notebook. Evans (EVN) 132. Students may apply for an ECE ACAD Computer account by filling out the “Apply for Account” request form online here: <http://www.eecis.udel.edu/>. Hardware Lab kits will be signed out and kept with you for the entire Semester.
- Office Hours:
 - o Michael Davis: By Appointment Only, 110 Evans Hall
 - o TAs: TBD
- Web Pages: This handout, most subsequent handouts, data files, and all project assignments for this course, are or will be made available through a page on the World Wide Web. It is located at:
<http://www.ece.udel.edu/~davis/cpeg222>

2 Course Structure

There will be two lectures per week. Students are responsible for all material covered in the lectures. Much, **but not all**, of the material covered in lecture is discussed in the textbook and references. Attendance is not taken, but it is the **students' responsibility** to get copies of any missed class notes from a classmate, not from the instructor. Quiz/Exam material will be taken from both the references and the lectures, with the later being the largest influence.

2.1 Suggested Reading

Suggested reading assignments will be given on the calendar for each lecture. In general, you should consider the textbook a reference to aid you in understanding and reviewing material covered in lectures and project assignments. Sections of the textbook and references that cover the material discussed in lectures will be listed on the calendar

<http://www.ece.udel.edu/~davis/cpeg222/index.html>
in the course web pages.

2.2 Study/Homework Problems

For each project, a number of problems will be assigned to direct topics of study that relate to topics in the project. Students will be required to hand in solutions to these problems as part of their project, but are graded separately. If you have difficulty solving any of the problems, please do not hesitate to come to office hours so that we can work through your questions.

2.3 Projects

In addition to the problems, there will be a series of 4-5 project assignments, most of which will be graded. The first project assignment (Project 0) will not be graded. This ungraded assignment is designed to introduce students to the project kit and project software. Although it is not graded, **it is required**, since subsequent projects will use concepts developed in it.

Each of the graded projects will require students to design a complex project through to completion. The project designs will then have to be constructed/tested using real circuits on the project kit. There is no formal laboratory session to build the projects; instead, the students can build their circuits when they please. The laboratory period you'll sign up for, will be for demonstrating and grading the final, completed project. While completing their projects, students will be required to keep a notebook detailing how they designed the circuits and why they chose to do so in that way. The format of these notebooks will be discussed in a subsequent handout.

Students are required to demonstrate their Projects to a TA, during the Project time you register for prior to the demonstration times available.

3 Grading

Final grades will be based on the following:

- Project assignments: 40%
 - Homework Part A – (10% of Project)
 - Homework Part B – (10% of Project)
 - Demonstration – (50% of Project)
 - Notebook/Doc. – (30% of Project)
- Midterm: 25%
- Final exam: 25%
- Quizzes: 10%

Grades are Averaged and Curved at the end of the course! Any grades assigned during the Semester are estimates only.

4 Late Policy

Due to the fact that you work in groups and have a week or more to complete project assignments, there will be NO late projects accepted. The same goes for project notebooks, which are due along with your project. Please see the Student Handbook for valid excused absences, and procedures you are responsible for following to validate these excuses.

Please Carbon Copy (CC:) the Instructor on any/all Email correspondence between you and your TA in dealing with concerns/rescheduling makeups/etc. or you may have no recourse should your TA not receive your email! This is YOUR responsibility! Additionally, if you use an Email account outside of the University, it is your responsibility to make extra sure the Instructor/TA receive your email.

5 Collaboration Policy

We expect that everyone involved in this course is a responsible individual. Thus, we do not expect to have any problems with academic dishonesty. However, in order to avoid any misunderstandings with regard to what is and is not acceptable, we have laid out some specific policies below.

1. Students may find that working on the Homework problems in study groups, will help explain topics that may be difficult for a few students. Students are required to hand in their own work for grading. Avoid the temptation to copy solutions from other students, you will get no benefit from the practice for the exams, and the Homework grades are usually not enough to sway a poor exam grade up.
2. Project assignments are different. While it is acceptable for students to discuss the projects with their group, it is NOT acceptable for students to collaborate on the actual design and construction of the circuits with anyone else. Think of the Projects as puzzles or brain teasers, and you are in competition with the rest of the class to complete the puzzle in the most creative way. Guard your design as a trade-secret and you'll have your grade and design skills to boast about with your classmates when everyone is done. ***Each group is required to construct, demonstrate, etc. their own projects on their own kit, without assistance from anyone else!***

Helping Other Students will only water down YOUR grade, by causing the class average to rise, and your grade to suffer!!!!

Course Grades are curved at the end of the Semester. Your course grade will be based upon your total course performance, as compared to your classmates.

3. Each group must write a notebook for each project, with no assistance from any one else. Using drawings, tables, diagrams, etc. from the project software may make writing the project electronically, a much easier task.
4. On the exams (usually open book, open notes), there is to be no collaboration whatsoever. You may be asked to show a photoID when turning in exams, please remember to bring one along with you. When you enter the class to take a quiz or exam, leave at least one empty seat between you and your neighbor.

Violations of these policies will be handled with severe penalties. ***Any student engaging in academic dishonesty will be directed to the Office of the Director of Judicial Affairs, regardless of their performance on any other assignments or exams.***