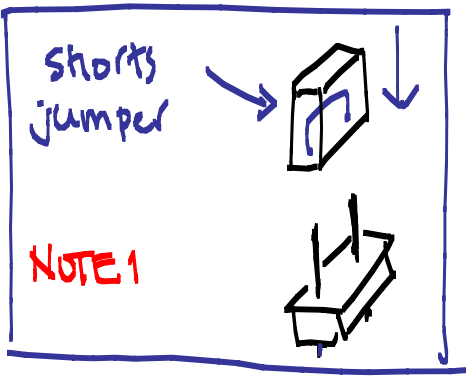
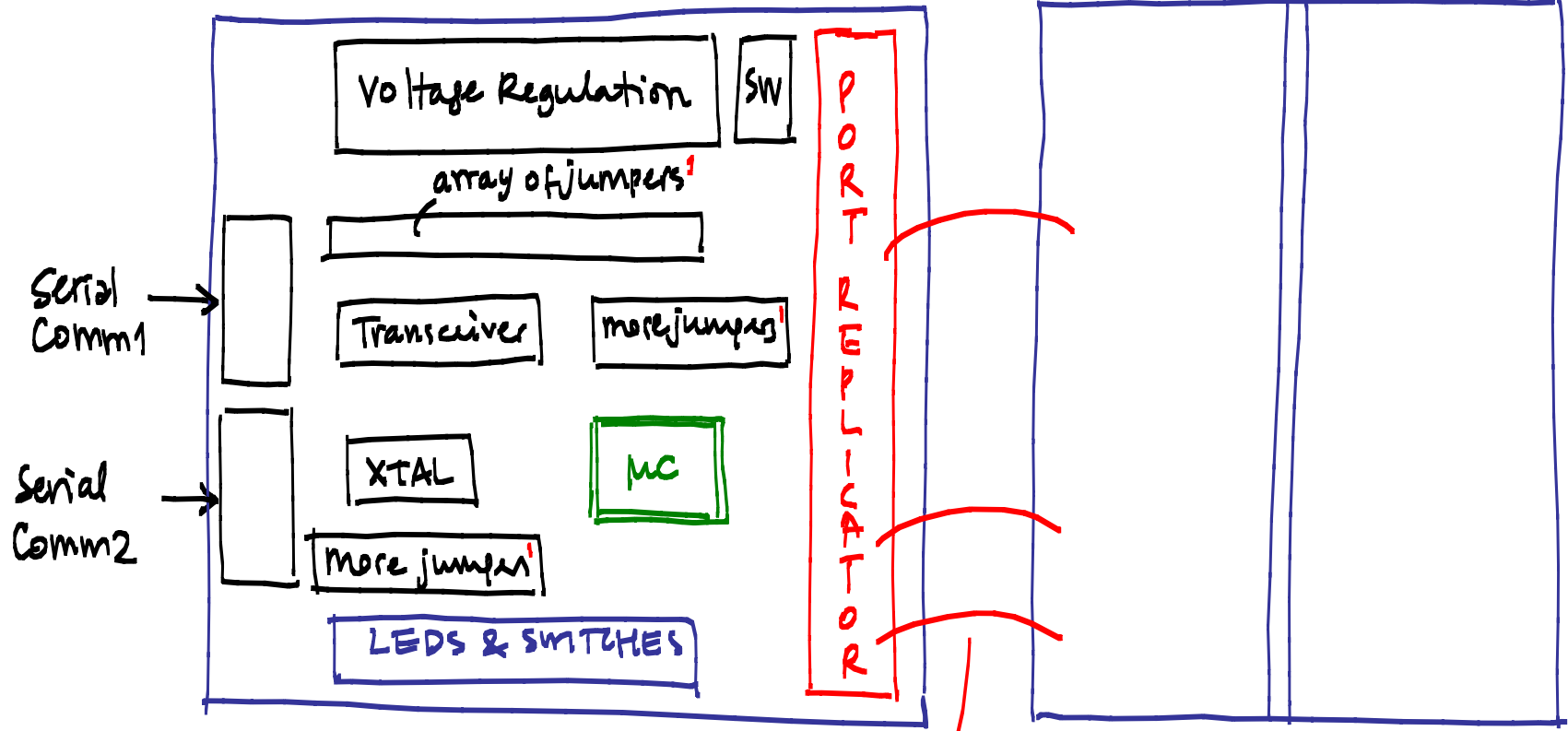


THE DEMONSTRATION/REFERENCE BOARD

EVALUATION BOARD

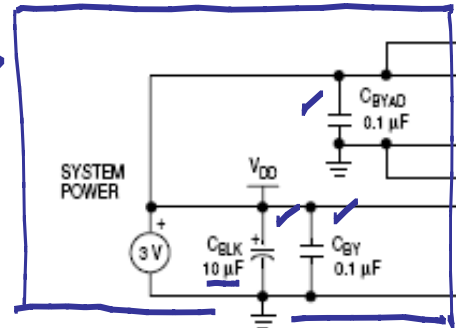
BREADBOARD



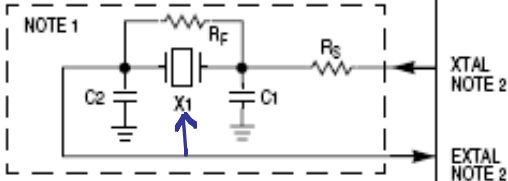
Wires that connect to external Hardware

BLOCK DIAGRAM OF THE MICROCONTROLLER I/O PINS

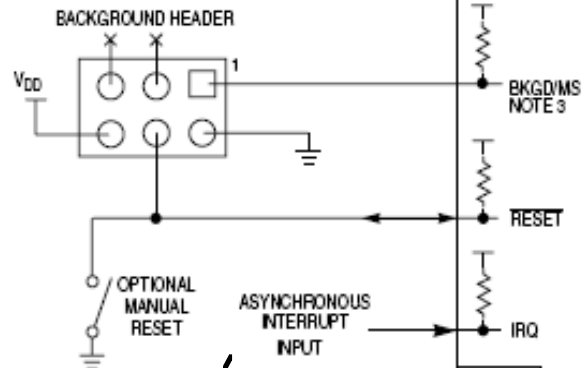
Power cleaning circuitry



XTAL



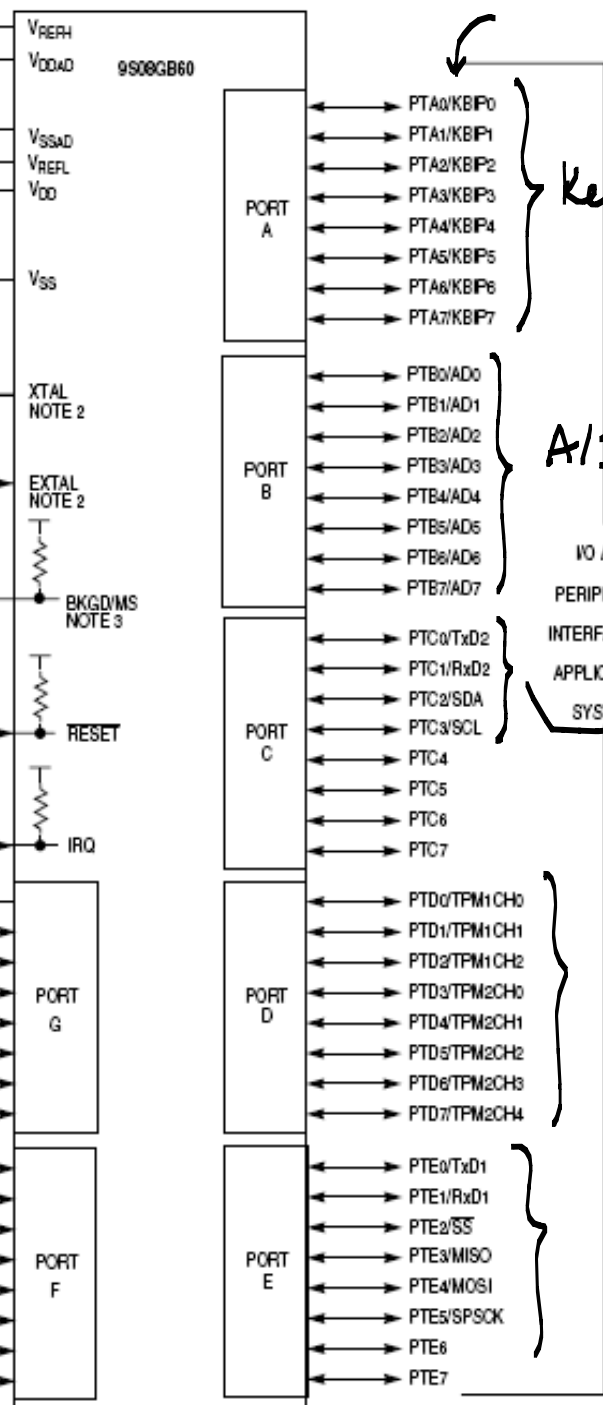
Operation mode configuration



XTAL

only I/O

- NOTES:
1. Not required if using the internal oscillator option.
 2. These are the same pins as PTG1 and PTG2.
 3. BKGD/MS is the same pin as PTG0.



Key pad functionality

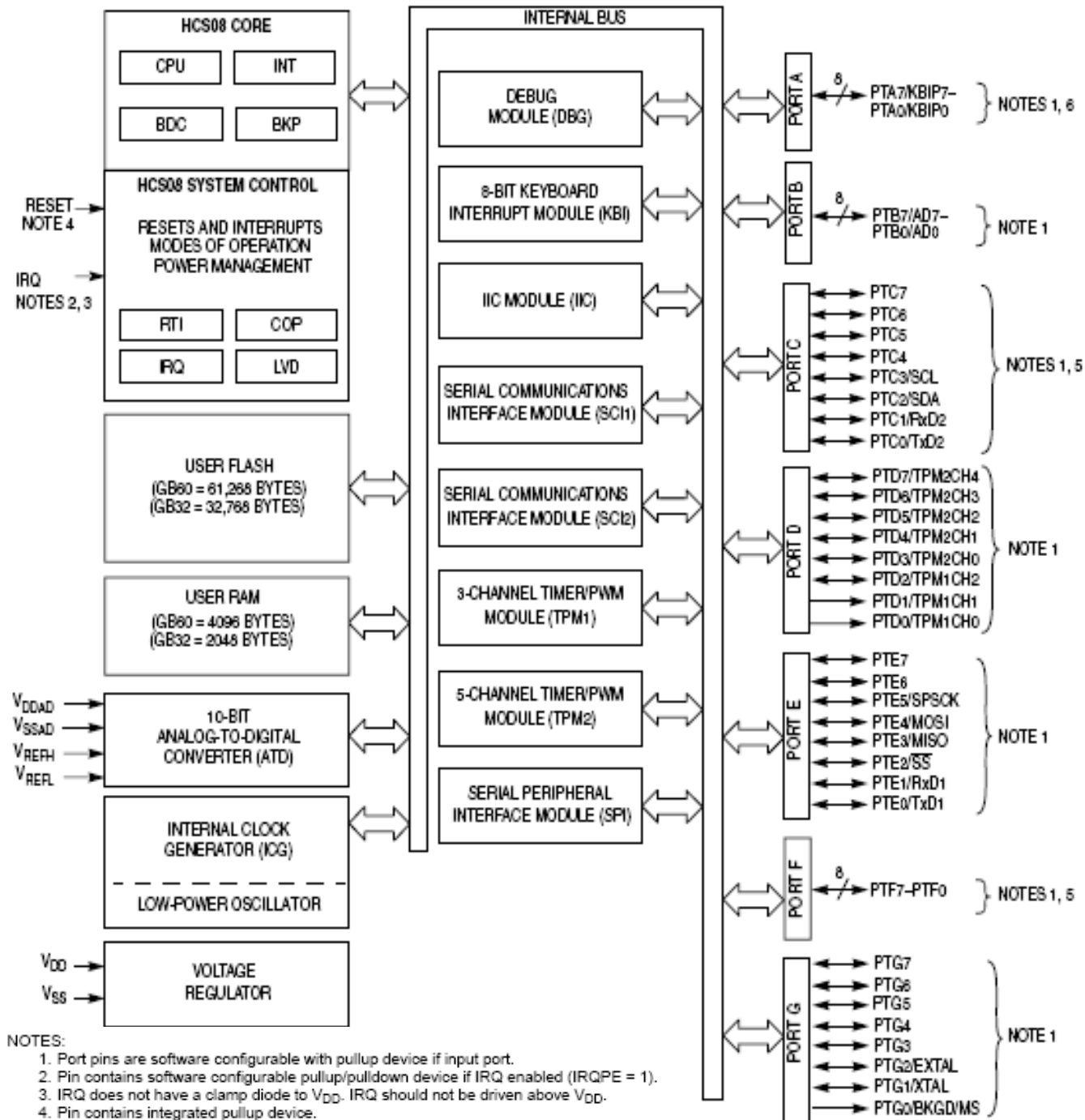
A/D pins

IO AND PERIPHERAL INTERFACE TO APPLICATION SYSTEM
TX/RX serial

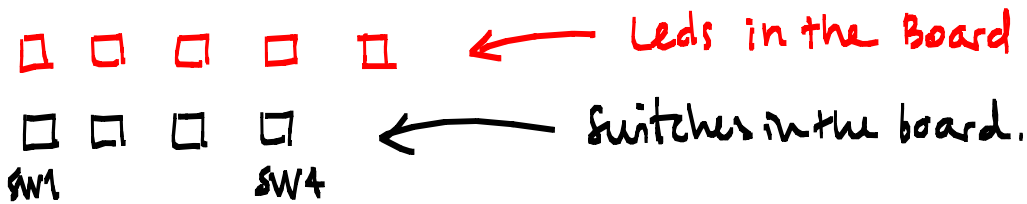
Timer functionality

Tx/Rx

DETAIL ON MC MODULES



PROJECT 0 DEFINITION



Project description/requirements

- Mode 1 - Reset/Initial
 - All LED's are OFF.
 - SW4 is pressed - enter Mode 2.
 - Ignore any button presses other than SW4.
- Mode 2 - Start
 - LED4 is ON.
 - LED1, LED2, LED3 count up in Binary, about once every second.
 - SW3 is pressed - enter Mode 3.
 - Ignore any button presses other than SW3.
- Mode 3 - Stop
 - LED4 is OFF.
 - LED1, LED2, LED3 remain showing last value.
 - SW4 is pressed - enter Mode2.
 - SW3 is pressed - remain in Mode3
 - Any other button presses (SW1, SW2) - enter Mode 1

DESIGN METHODOLOGY

PROJECT Ø - FLOW DIAGRAM

